

## The Silicon Tracking System of CBM: towards tests with heavy ion collisions

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## STS for the CBM experiment



- target beam 2.5°
  - track point measurement in high-rate collision environment:
    - $10^{5} 10^{7}$ /s (A+A), up to  $10^{9}$ /s (p+A)
  - physics aperture :  $2.5^{\circ} \le \Theta \le 25^{\circ}$
  - 8 tracking stations built with double-sided silicon microstrip sensors
  - self-triggering front-end electronics
  - material :  $\approx 0.4\% 1.4\% X_0$  per station

## Silicon sensors

#### **Double-sided n-type silicon sensors**

- Thickness:  $320 \,\mu\text{m} \pm 15 \,\mu\text{m}$
- 58 μm pitch
- 1024 strips/side
- strip length 2/4/6/12 cm
- 7.5° stereo angle for p-side strips (suppression of the ghost track rate)
- double-metal routing on p-side



- hit spatial resolution  $\approx 25 \,\mu m$
- $\Delta p/p \approx 1.8\%$  (p > 1 GeV/c, 1 Tm field)
- track (> 1 GeV/c) and single hit reconstruction efficiency 96% and 98%
- radiation tolerance: NIEL:  $1 \times 10^{14} n_{eq} (1 \text{ MeV}) / \text{cm}^2$ TID: 1 Mrad
- sensors developed and produced by Hamamatsu/Japan

### Front-end electronics & STS module



Block diagram of the STS-XYTER ASIC

## **STS-XYTER ASIC**

**STS** + **X**, **Y** coordinates Time and Energy Resolution

- 128 readout channels
- time resolution < 5 ns
- 5 bit flash ADC/channel
- (14 fC dynamic range)
- hit rate/channel: >250 kHz
- radiation hard layout

#### **Detector module as functional unit:**

- silicon sensor
- stack of polyamide readout cables
- 2 front-end boards (16 ASICs)

## STS readout chain



Block diagram of the STS readout chain

#### **STS detector in numbers:**

- 896 detector modules
- 106 ladders
- 1.6 million readout channels



High performance, free-streaming readout chain Main components: **FEB:** Front-end boards **ROB:** Readout board **CRI:** Common readout interface

**TFC:** Timing and fast control **DSC:** Detector slow control FLES: First level event selector

## mini-CBM @ SIS18: First tests with heavy-ions collisions

#### CBM full test setup for high rate, nucleus-nucleus collisions at GSI/FAIR **GOALS:**

- demonstrator for full CBM data taking and analysis
- integrating prototype detector's modules into a common, free-streaming DAQ
- reconstructing physics observable (A reconstruction using Ni-Ni at 1.93 AGeV and Au+Au at 1.54 AGeV)

#### **STS GETTING READY (mSTS) GOALS:**

- operation of full STS modules in a high-rate heavy-ion data taking scenario
- 13 STS sensor modules grouped in 2 tracking stations (5 ladders)
- 208 STS-XYTER ASICs (~26600 readout channels)





#### **Preliminary results from March 2019 run**

- 2 detector ladder with 4 modules installed
- Ag+Au collisions at 1.58 AGeV.
- Up to 10<sup>7</sup> ions/s in a 1% interaction target



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#### **Key participant institutes:**

GSI (Darmstadt, Germany), JINR (Dubna, Russia), Univ. Tübingen (Germany), KIT (Karlsruhe, Germany), AGH (Krakow, Poland), JU (Krakow, Poland), WUT (*Warsaw, Poland*), Goethe University (*Frankfurt, Germany*)

#### **Timeline**:

- Production Readiness: 2020
- Detector construction: 2020–2024

**Assembly Centers:** GSI-FAIR, JINR-VBLHEP

