

The powering concept for the Silicon Tracking System of Compressed Baryonic Matter experiment at FAIR

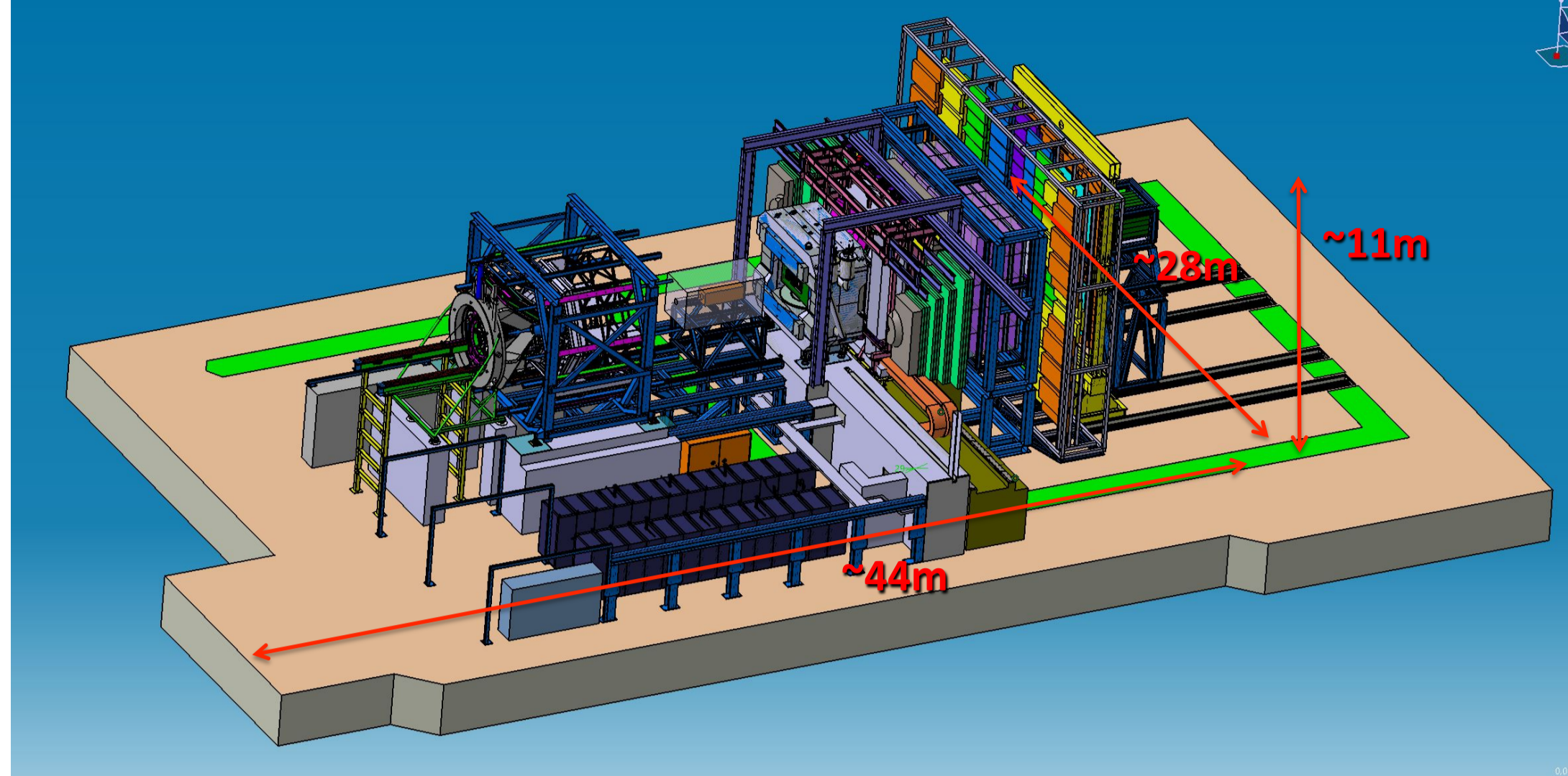
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The CBM experiment at FAIR/GSI

Compressed Baryonic Matter experiment:
fixed target heavy ion physics Experiment @ FAIR/GSI:

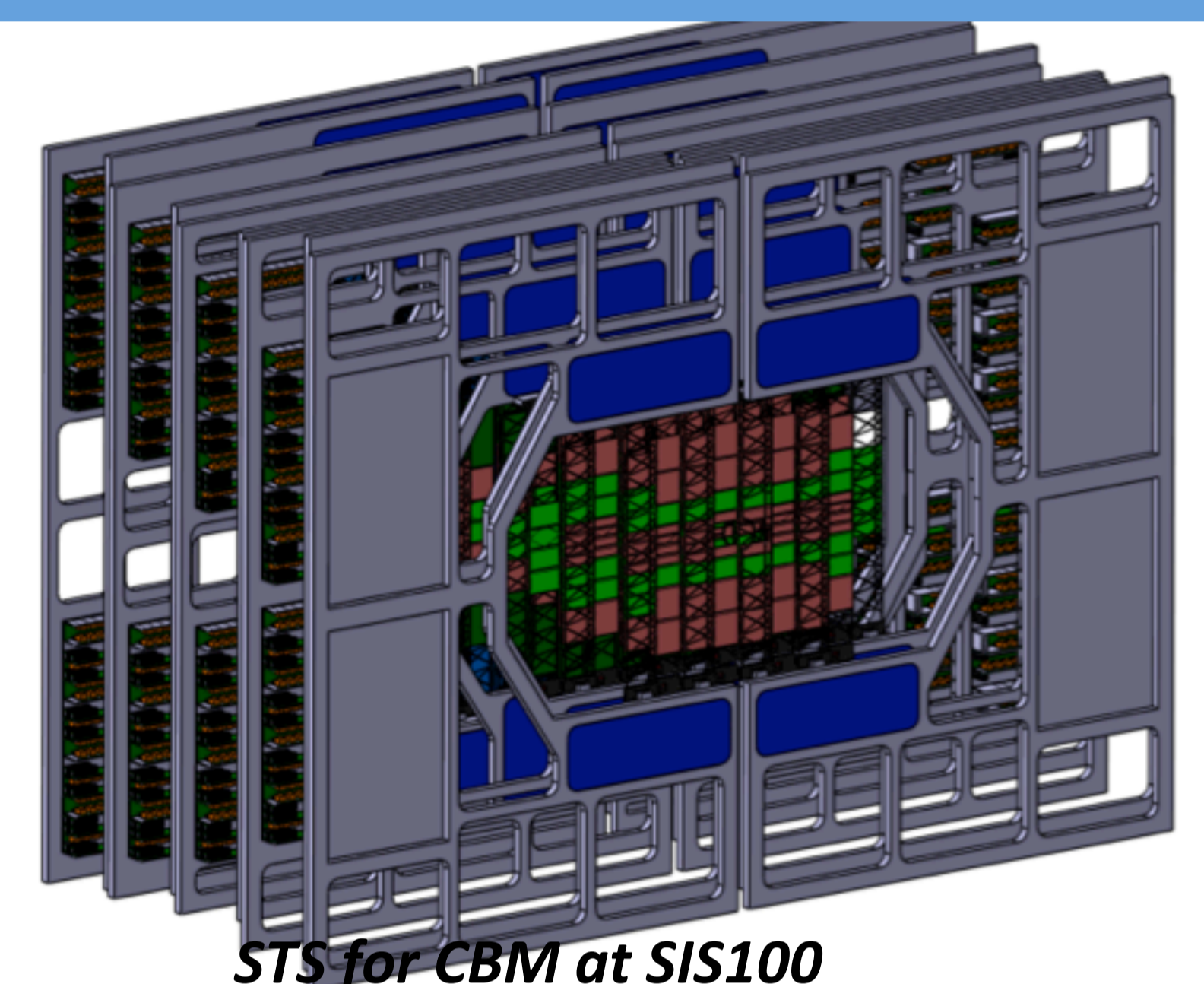
- high interaction rate
- high radiation dose on the electronic components
- 1T magnetic field



- fixed target setup to investigate the QGP phase diagram in region of high baryon-densities
- very high interaction rate environment: $10^5 - 10^7/s$ (A+A), up to $10^9/s$ (p+A)
- allows to study the equation-of-state of nuclear matter at neutron star core densities and the search for phase transitions, chiral symmetry restoration and exotic forms of QCD matter

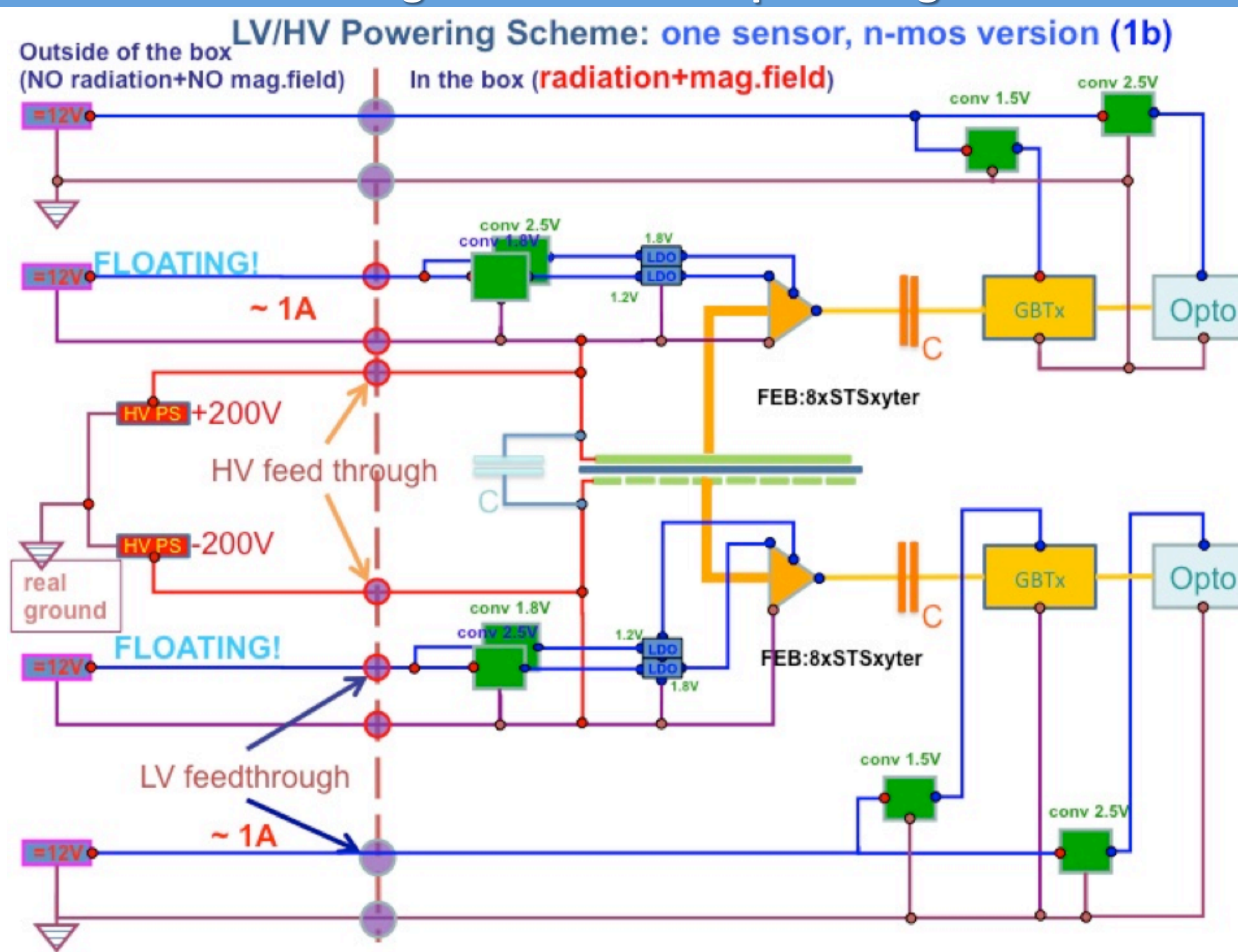
Silicon Tracking Station detector for CBM

- Compact (volume $\sim 3.5 \text{ m}^3$) Silicon Tracking Station sub-detector consisting of 900 double sided silicon strip sensors (4 m^2 sensitive area in 8 layers) placed in a strong (1T) magnetic field for momentum measurement
- requires radiation tolerant electronics and materials
- magnetic field resilient components
- efficient voltage converters to reduce heat production inside the detector and short and low ohmic connections inside STS
- 1800 LV and 1800 HV channels for floating individual powering of sensors and readout electronics



STS for CBM at SIS100

Single silicon sensor powering



Activities at GSI:

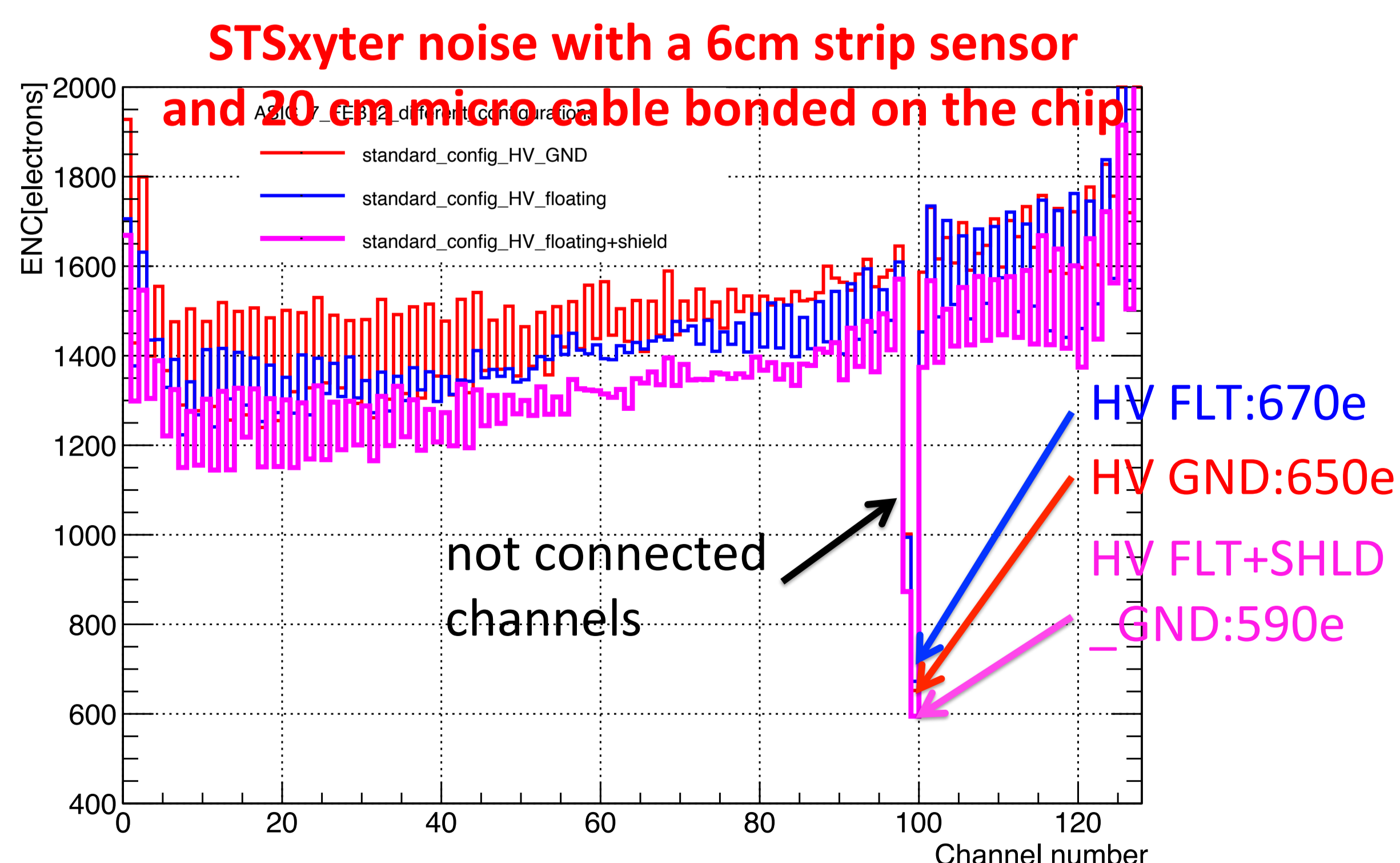
Low voltage components

- 1.2V, 1.8V low drop voltage stabilisers developed in SCI Chandigarh India
- 1.8V, 2.4V, 1.5 and 2.5V FEAST DC/DC converters developed at CERN
- driven by 12V floating power supplies from outside (~ 1800 channels, commercially available)

High voltage requirements

- 500 V/ few mA floating power supplies with pairwise common ground, placed some 100 m from the pit (~ 1800 channels, commercially available)

LV + HV noise measurement results



References

Ionising dose: A.Senger, https://fair-center.eu/fileadmin/fair/experiments/CBM/tmp/CBM_FLUKA.htm

STS Xyter: Kasinski, K., Koczon, P., Ayet, S., Löhnner, S., & Schmidt, C. J. (2017). System-level considerations for the front-end readout ASIC in the CBM experiment from the power supply perspective. *Journal of Instrumentation*, 12(3). <https://doi.org/10.1088/1748-0221/12/03/C03023>

Kasinski, K., Rodriguez-Rodriguez, A., Lehnert, J., Zubrzycka, W., Szczygiel, R., Otfinowski, P., ... Schmidt, C. J. (2018). Characterization of the STS/MUCH-XYTER2, a 128-channel time and amplitude measurement IC for gas and silicon microstrip sensors. *Nuclear Instruments and Methods in Physics Research Section A: Accelerators, Spectrometers, Detectors and Associated Equipment*, 908, 225–235. <https://doi.org/https://doi.org/10.1016/j.nima.2018.08.076>

LVR: Radiation Hardened Low Drop-Out Voltage Regulator (LDO) – 1.8 V / 1.6 A, Pre-Production Technical Interface Document, March 2019, Semi-Conductor Laboratory, Department of Space, Government of India, S.A.S. Nagar, Punjab-160071

DC/DC: CERN S.Michelis et al. https://project-dcdc.web.cern.ch/project-dcdc/public/Documents/FEASTMod_Datasheet.pdf

LV+HV Power supply: <http://www.wiener-d.com/sc/power-supplies/mpod-lvhv/mpod-crate.html>
<http://www.wiener-d.com/sc/power-supplies/mpod-lvhv/mpod-hv-module.html>
<http://www.wiener-d.com/sc/power-supplies/mpod-lvhv/mpod-crate.html>

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