

## The HitDetection ASIC – A self triggered transient recorder

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Up to now the digitisation part of the FEE and DAQ chain should be realised with COTS components. Especially for the EMC readout where the digitisation unit has to be placed inside the magnet an application specific highly integrated solution would have some advantages.

In this talk the concept for the HitDetection ASIC will be presented. The HitDetection ASIC is a self triggered transient recorder and digitiser well fitted to the needs of the PANDA readout.

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