

Gigabit Ethernet implementation on Lattice FPGAs

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Implementation of Gigabit Ethernet on TRB Addon boards is a cheap and efficient way to transport large amount of data from front-end electronics to event builders, and is a solution already used and being tested in HADES experiment in GSI, Darmstadt. The process of gathering data, constructing packets and finally transport them will be covered by my presentation.

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Sitzung Einordnung: Trigger and Data Acquisition II

Track Klassifizierung: Data Acquisition