CBM Archive: Wilga 2018 - XLIV-th IEEE-SPIE Joint Symposium on Photonics, Web Engineering, Electronics for Astronomy and High Energy Physics Experiments

Report of Contributions

Contribution ID: **0** Type: **not specified**

Application of Forth CPU for control and debugging of FPGA-implemented systems

Sunday, 26 May 2019 08:00 (1 minute)

Presenter: ZABOLOTNY, Wojciech (Warsaw University of Technology(WUT))

Contribution ID: **1**   Type: **not specified**

Automatic management of local bus address space in complex FPGA-implemented hierarchical systems

Sunday, 26 May 2019 08:01 (1 minute)

Presenter: ZABOLOTNY, Wojciech (Warsaw University of Technology(WUT))