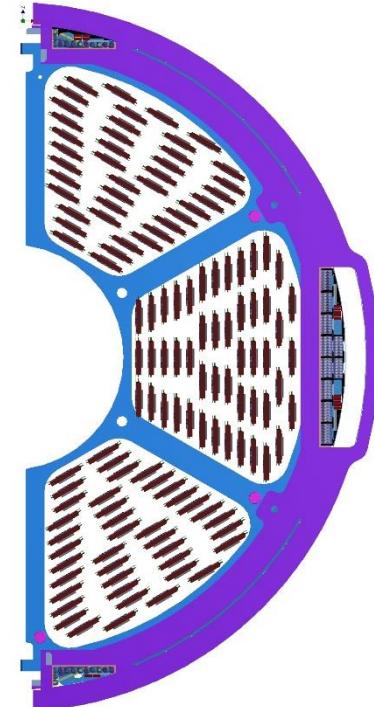


Readout System Update: ASIC/TRB

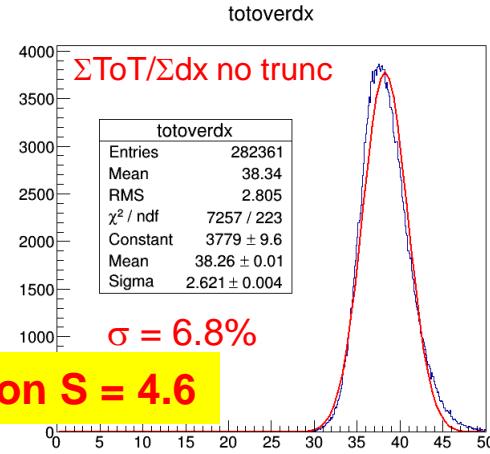
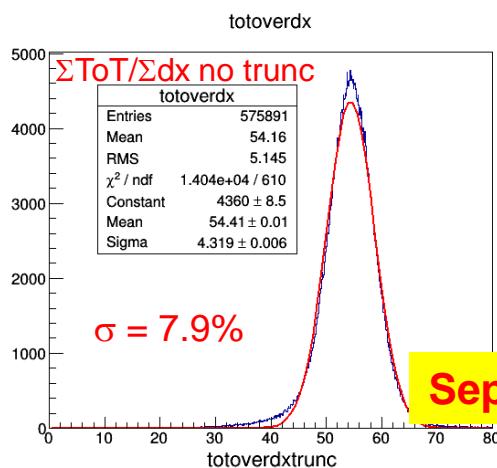
- Order new FE-PCBs: PANDA-STT “pre-series” version
 - v3 with smaller size, no analog out, connectors changed
 - additional: v4 with analog out all channels for general straw test systems
- Order new ASICs & PCBs
 - 250x ASICs, 125x PCBs
 - 100x PCB-v3: 46x HADES-STS, 54x PANDA-STT
 - 25x PCB-v4
- Front-end area layout in progress
 - Single straw – FEB association, optimisation done
 - 135x FE-PCB per semi-barrel (2160 ch, net: 2112 ch)



CAD-Layout of ASIC front-end boards

Prelim. ToT Separation Power (for Deuteron & Cosmic)

- use $\Sigma \text{ToT} / \Sigma \text{dx}$ per track (top figures), gives better distribution symmetry than $\Sigma(\text{ToT}/\text{dx})$
- truncate single ToT/dx with lowest and highest entries (bottom figures)
- preliminary:** $S \sim 5.1$ separation power, $S \sim 9$ in TDR (π/K at 0.23 GeV/c), similar to Pawel's results



Summary of TOT/dx

