

# Single-Event Upsets in the PANDA EMC

Status of neutron and proton irradiations of the front-end digitiser board



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# Front-end electronics in the Electromagnetic Calorimeter

~ 600 front-end digitiser boards in the EMC

375 in Barrel\*    217 in Forward Endcap

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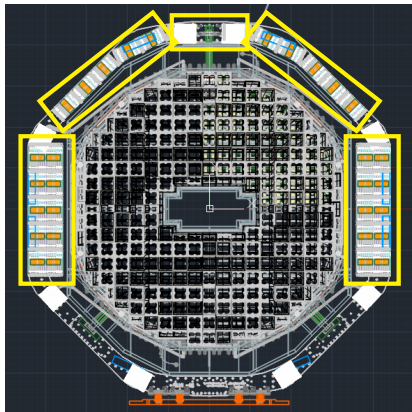


Figure courtesy of C. Schnier.

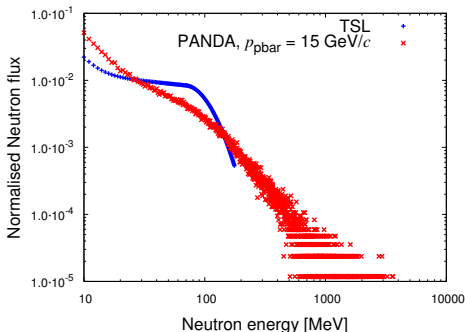
- ▶ Distributed over several crates placed around the detector perimeter (average 1.3 metres from beam pipe at the Forward Endcap).
- ▶ Exposure to a high flux of particles  $\Rightarrow$  Radiation effects
- ▶ Of interest here: **neutrons and protons**

# Neutron irradiation

- ▶ The board was irradiated in June 2016 at the The Svedberg Laboratory (TSL) in Uppsala.
- ▶ Proton beam  $\rightarrow$  W target  $\rightarrow$  Neutron beam. Board perpendicular to the beam. **One** FPGA read out.

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- ▶ Proton beam  $\rightarrow$  W target  $\rightarrow$  Neutron beam. Board perpendicular to the beam. **One** FPGA read out.
- ▶ Neutron flux between  $5 \cdot 10^5$  and  $1 \cdot 10^6 \text{ s}^{-1}\text{cm}^{-2}$  ( $>10 \text{ MeV}$ ).



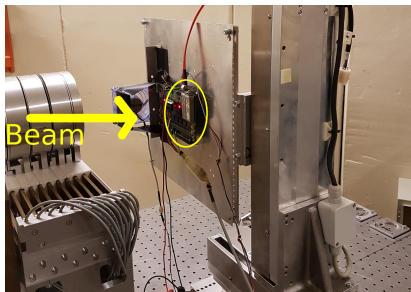
pandaROOT simulation courtesy of K. Makònyi.

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- ▶ Board perpendicular to the beam (covering half of the board).  
**One** FPGA read out.



- ▶ Three proton energies:
  - ▶ 184 MeV (primary beam energy)
  - ▶ 80 and 100 MeV (with degrader)
- ▶ Total proton fluence over whole irradiation:  $\sim 10^{10} \text{ cm}^{-2}$ .

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  - ▶ Using Xilinx Soft-Error Mitigation (SEM)
  - ▶ SEM can correct Single-Bit Upsets and Multi-Bit Upsets in adjacent memory frames (using Error Correction Code)
  - ▶ Multi-Bit Upsets within same memory frame are **not** correctable by SEM

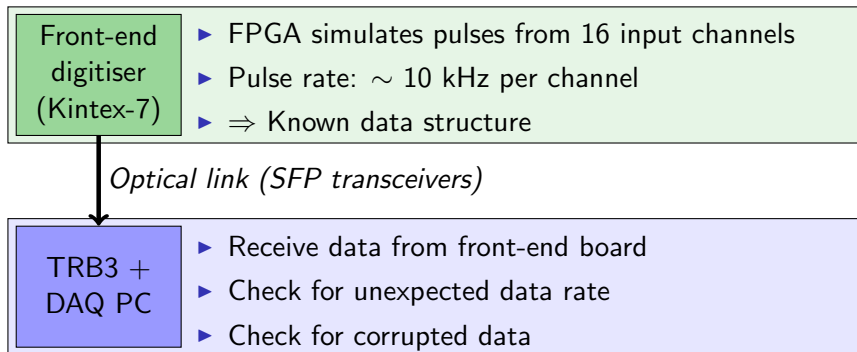
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  - ▶ Multi-Bit Upsets within same memory frame are **not** correctable by SEM
- ▶ The full readout (Digitiser + TRB3 Data Concentrator)
  - ▶ Only studied in November (protons)
  - ▶ Using custom FPGA firmware
  - ▶ Generate data stream from digitiser
  - ▶ Check for unexpected data received by data concentrator
  - ▶ Test errors related to data transfer in FPGA, optical link, etc.

# Testing the full readout chain

## Measurement process

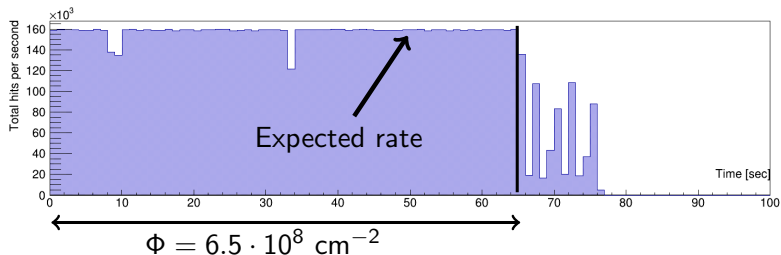


- ▶ Reasons for corrupted data during irradiation could be:
  - ▶ SEU in FPGA memory section where data is transferred
  - ▶ Radiation-induced error in optical connection

# Full readout chain tests

## Results

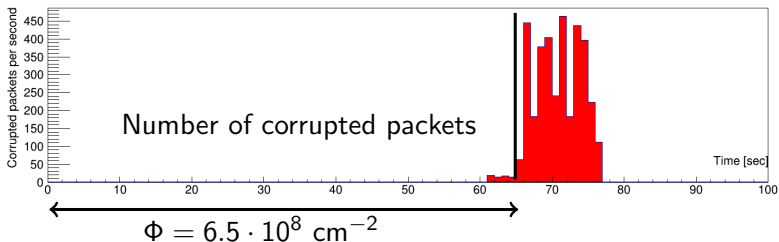
- ▶ Analysis still at an early stage.
- ▶ At low proton fluxes, no large deviation from expected data observed.
- ▶ Increase proton flux to  $1 \cdot 10^7 \text{ cm}^{-2} \text{ s}^{-1} \Rightarrow$ 
  - ▶ Occasional errors in received data observed
  - ▶ Large drop in data rate and corruption of data observed after  $\sim 65$  seconds ( $\Leftrightarrow$  total proton fluence of  $6.5 \cdot 10^8 \text{ cm}^{-2}$ ).



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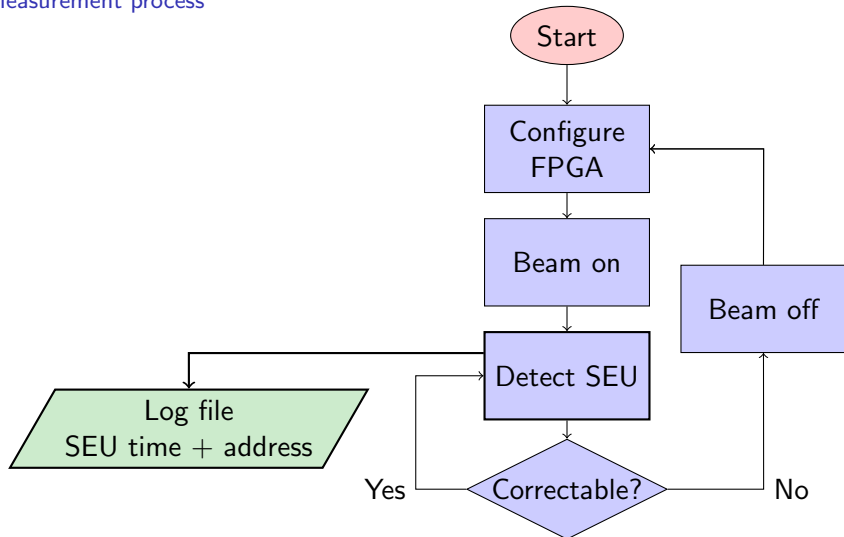
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# FPGA configuration memory tests

## Measurement process



**Note:** during proton irradiation, SEM automatically reset after uncorrectable error.

# Results

## SEU cross section

The cross section for an SEU in the FPGA is given by

$$\sigma_{\text{SEU}} = \frac{N_{\text{SEU}}}{T_{\text{meas}} \cdot \Phi_n \cdot N_{\text{bits}}}$$

The diagram illustrates the formula for the SEU cross section,  $\sigma_{\text{SEU}}$ . The formula is presented as a fraction where the numerator is  $N_{\text{SEU}}$  and the denominator is the product of  $T_{\text{meas}}$ ,  $\Phi_n$ , and  $N_{\text{bits}}$ . Curved arrows point from descriptive text labels to each variable in the formula: 

- An arrow points from "Number of SEU" to  $N_{\text{SEU}}$ .
- An arrow points from "Measurement duration" to  $T_{\text{meas}}$ .
- An arrow points from "Particle flux" to  $\Phi_n$ .
- An arrow points from "Total number of bits" to  $N_{\text{bits}}$ .

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Diagram illustrating the SEU cross section formula with labels and arrows:

- $N_{\text{SEU}}$ : Number of SEU
- $T_{\text{meas}}$ : Measurement duration
- $\Phi_n$ : Particle flux
- $N_{\text{bits}}$ : Total number of bits

Upset rate,  $r_{\text{SEU}}$ :

$$r_{\text{SEU}} = \sigma_{\text{SEU}} \cdot \Phi_n \cdot N_{\text{bits}}$$

Mean Time Between Failures,  $MTBF$ :

$$MTBF = \frac{1}{\sigma_{\text{SEU}} \cdot \Phi_n \cdot N_{\text{bits}}}$$

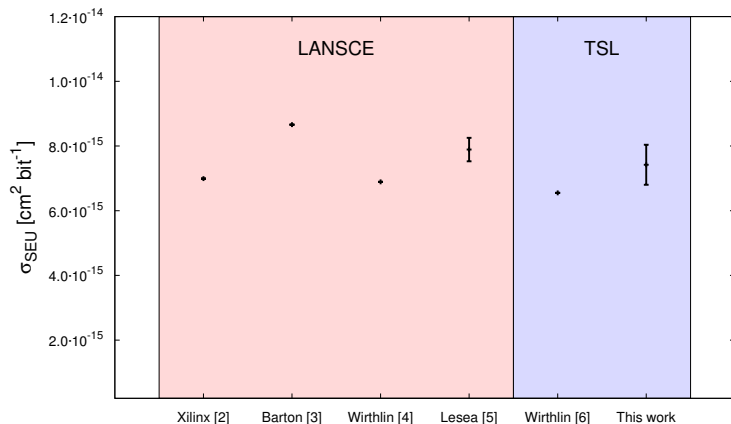


# Results

## FPGA configuration memory SEU cross section - neutron irradiation

Measured cross section:  $\sigma_{\text{SEU}} = 7.4 \cdot 10^{-15} \text{ cm}^2 \text{ bit}^{-1}$ .

Good agreement with other measurements on Kintex-7:



TSL: The Svedberg Laboratory

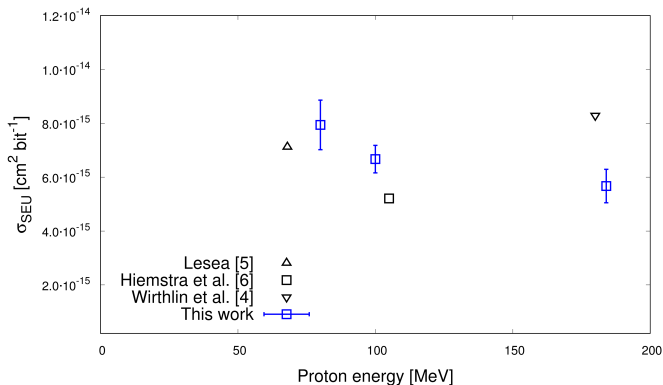
LANSCE: Los Alamos Neutron Science Center (similar neutron spectrum)

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## FPGA configuration memory SEU cross section - proton irradiation

Measured cross sections:

- ▶ 80 MeV protons:  $\sigma_{\text{SEU}} = 7.9 \cdot 10^{-15} \text{ cm}^2 \text{ bit}^{-1}$
- ▶ 100 MeV protons:  $\sigma_{\text{SEU}} = 6.7 \cdot 10^{-15} \text{ cm}^2 \text{ bit}^{-1}$
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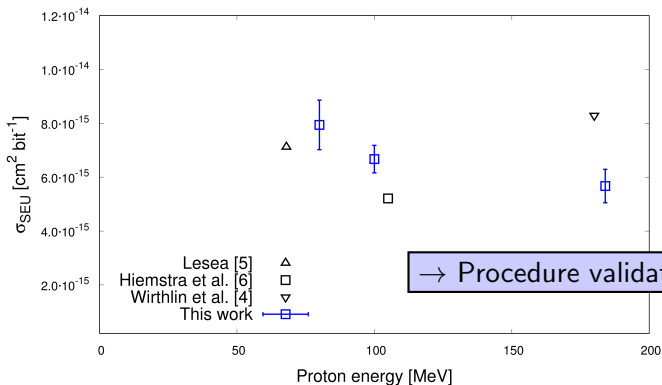


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# SEU rates in the Electromagnetic Calorimeter

- ▶ Simulations of particle fluxes in the forward endcap of the EMC have been made:
  - ▶ 2016 simulation of the neutron flux (K. Makònyi)
  - ▶ 2012 simulation of the proton flux (M. Kavatsyuk et al., 2012 IEEE NSS/MIC)
- ▶  $p_{\text{pbar}} = 15 \text{ GeV}/c$ ,  $\mathcal{L} = 2 \cdot 10^{32} \text{ cm}^{-2} \text{ s}^{-1} \Rightarrow$ 
  - ▶  $\Phi_{\text{n}}$  at position of digitisers is  $\sim 150 \text{ cm}^{-2} \text{ s}^{-1}$ .
  - ▶  $\Phi_{\text{p}}$  at position of digitisers is  $\sim 60 \text{ cm}^{-2} \text{ s}^{-1}$ .
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  - ▶ MTBF due to neutrons:
    - ▶ Any type of SEU: 18 seconds
    - ▶ SEUs not correctable by SEM: 9 minutes
  - ▶ MTBF due to protons (from average of cross sections):
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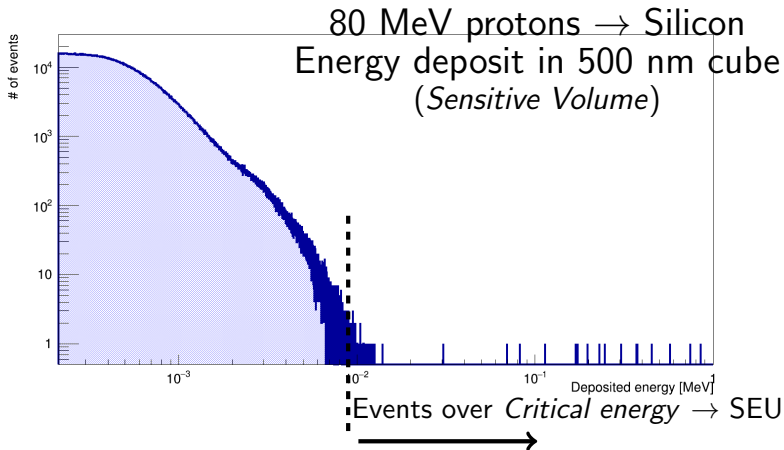
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- ▶ **These values describe how often the different error types would occur in the whole EMC.**

# Monte Carlo simulation of SEUs in FPGA memory

- ▶ Simulating energy deposit in “memory cell” (nanometric volume in silicon) using GEANT4.
- ▶ Goal: combine with EMC flux simulations, to get better estimate of error rates in the EMC.

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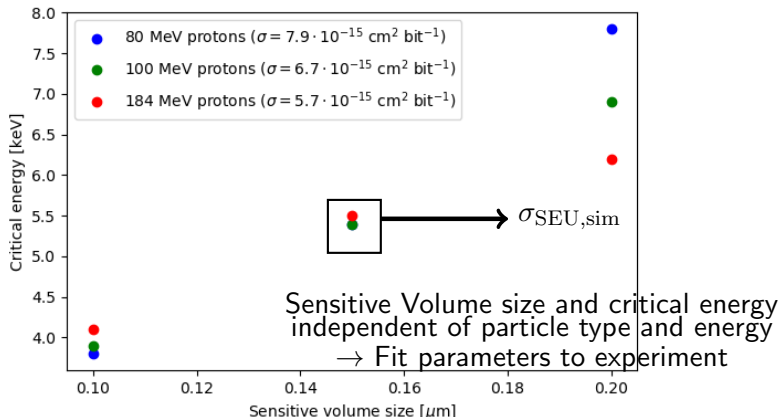
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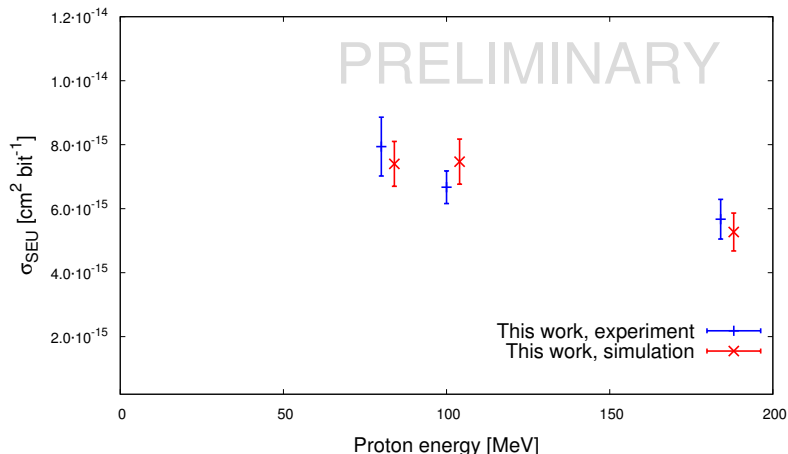
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# Summary

- ▶ The readout of the digitiser has been tested under proton irradiation
  - ▶ Large data loss and corruption seen after a proton fluence of  $6.5 \cdot 10^8 \text{ cm}^{-2}$
  - ▶ This corresponds to 125 days of PANDA operation for one digitiser board (assuming a proton flux of  $60 \text{ cm}^{-2} \text{ s}^{-1}$  in the EMC). Analysis ongoing!
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- ▶ Monte Carlo simulation of SEUs under development.

# Outlook

- ▶ Further irradiations are planned:
  - ▶ Protons at KVI
  - ▶ Thermal neutrons in Stockholm
- ▶ Sensitivity of other parts of the digitiser board should be tested.
- ▶ Analysis of measurements from November still ongoing.
- ▶ New pandaROOT-based simulation of charged-particle flux in EMC ( $\rightarrow$  energy spectrum of particles) - work ongoing.
- ▶ Monte Carlo simulation of SEUs in progress
  - ▶ Future: simulation of neutron-induced SEUs

Thank you!