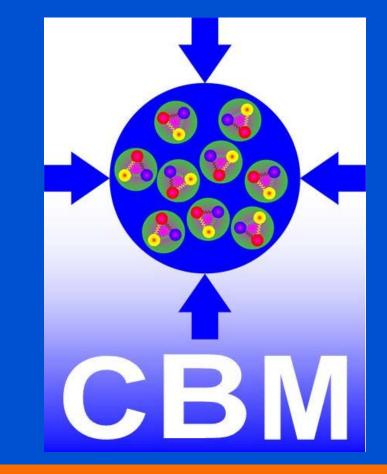
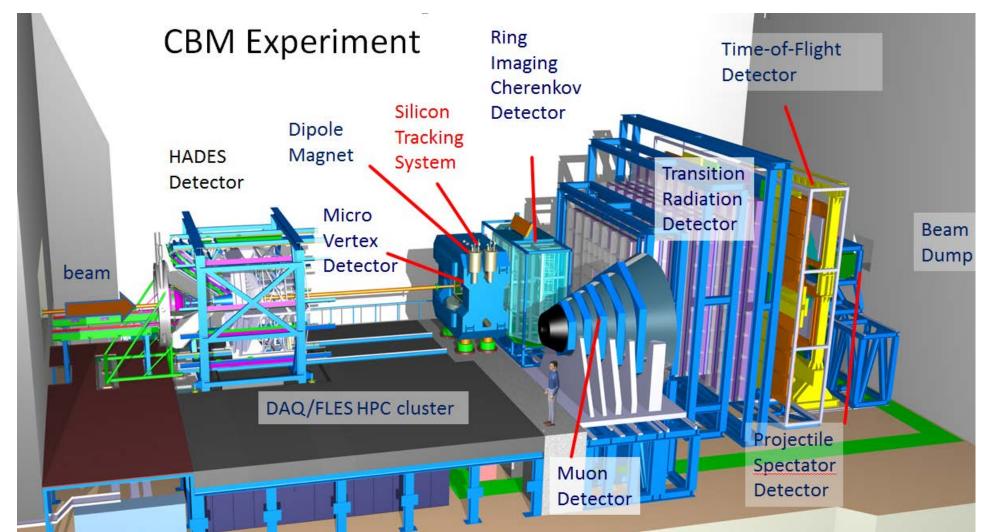
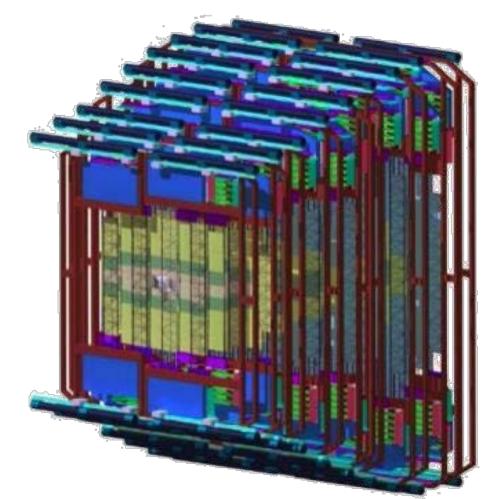
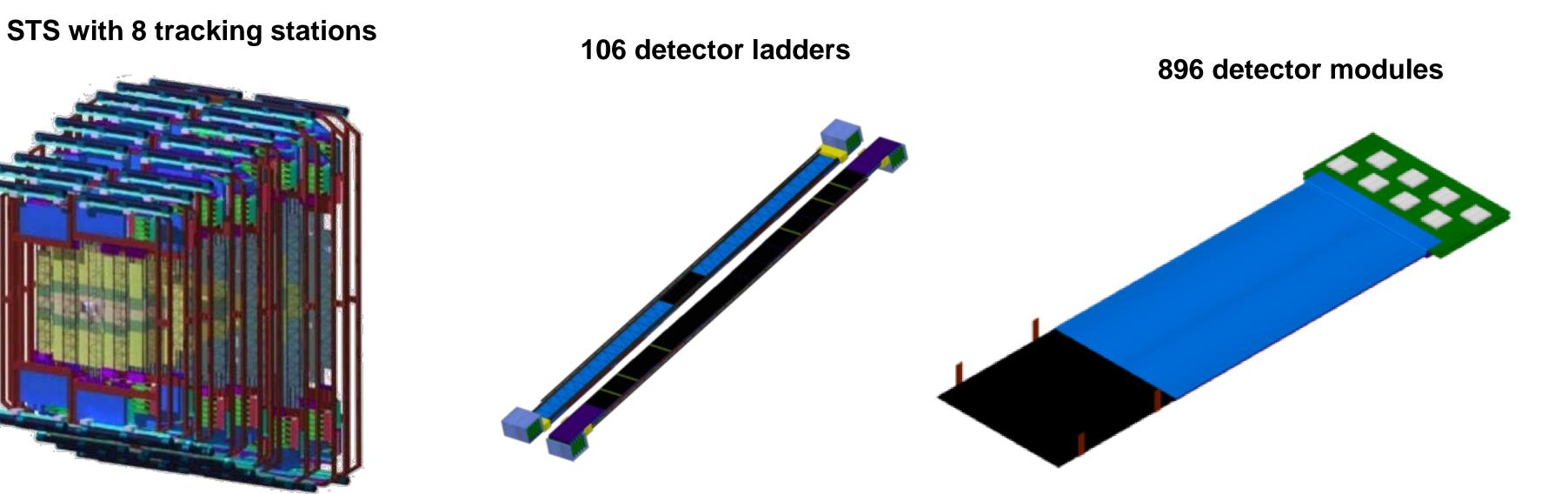
The workflow of module assembly for the **CBM Silicon Tracking System** Carmen Simons, Daniel Soyk and Robert Visinka, GSI, for the CBM Collaboration



The STS Detector of the CBM Experiment @ FAIR





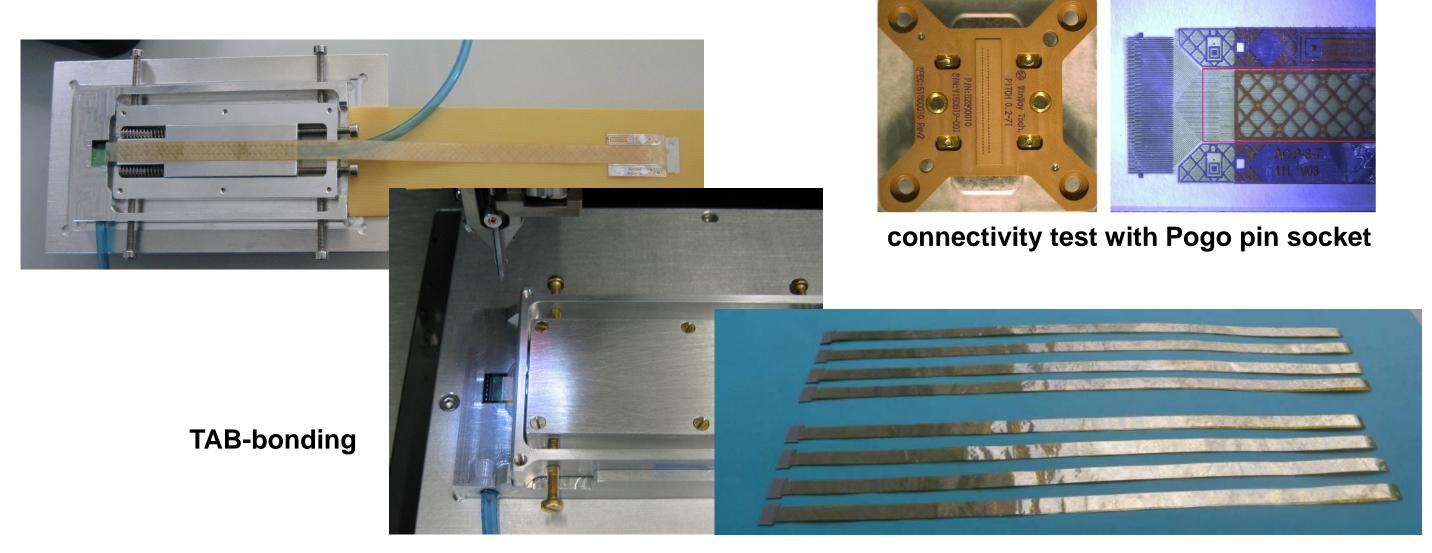




The Silicon Tracking System (STS) is the core detector that provides track reconstruction and momentum determination of charged particles from beam-target interactions. It will consist of 8 tracking stations that are built from different types of basic functional modules which are mounted on carbon fiber ladders.

STS module assembly

<u>Step 1:</u> TAB-bonding of the microcables to the STS-XYTER-ASIC (first and second layer), quality measurement and protection with Globtop



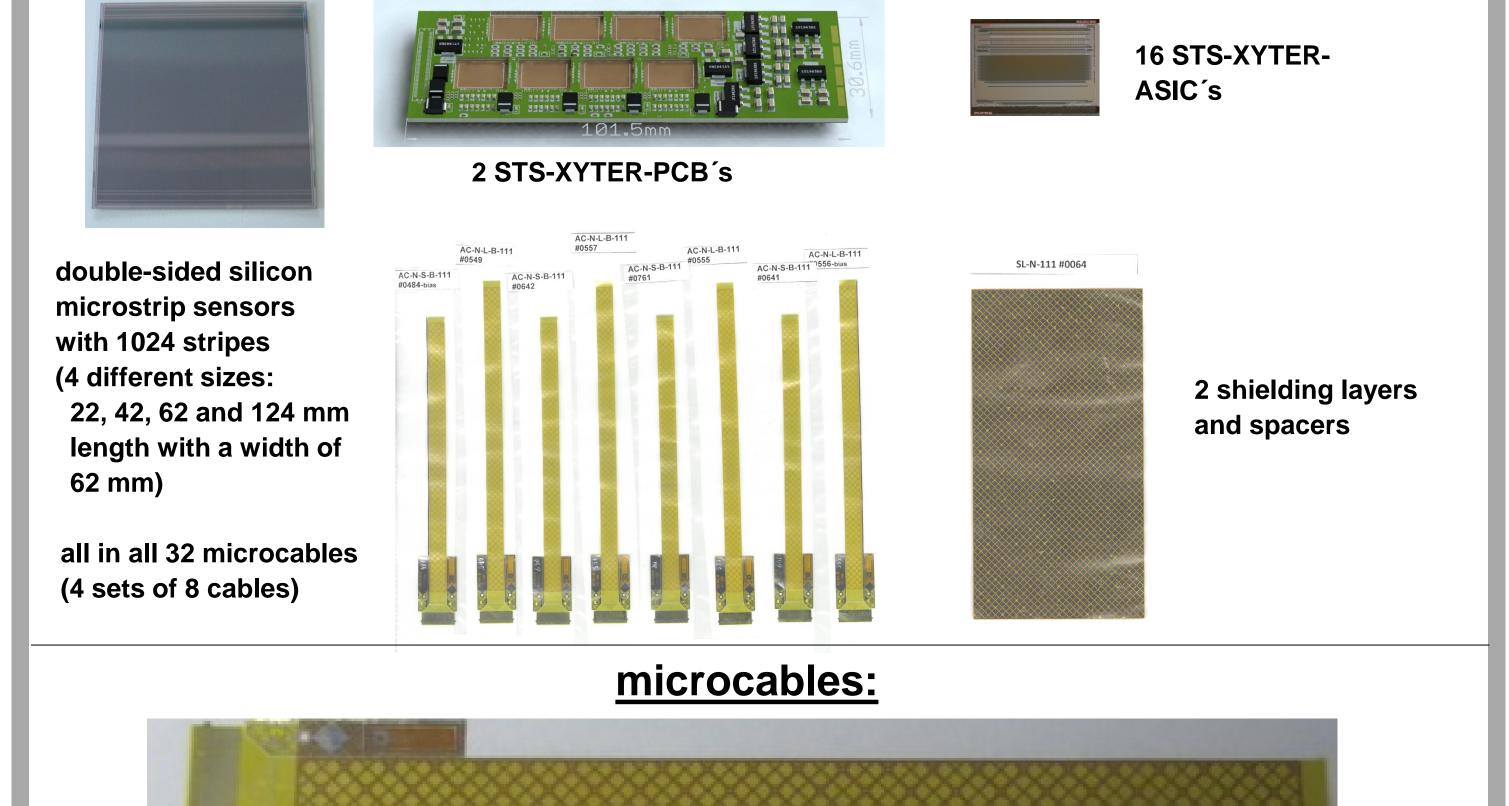
Step 2: TAB-bonding of the microcables to the silicon sensor (first

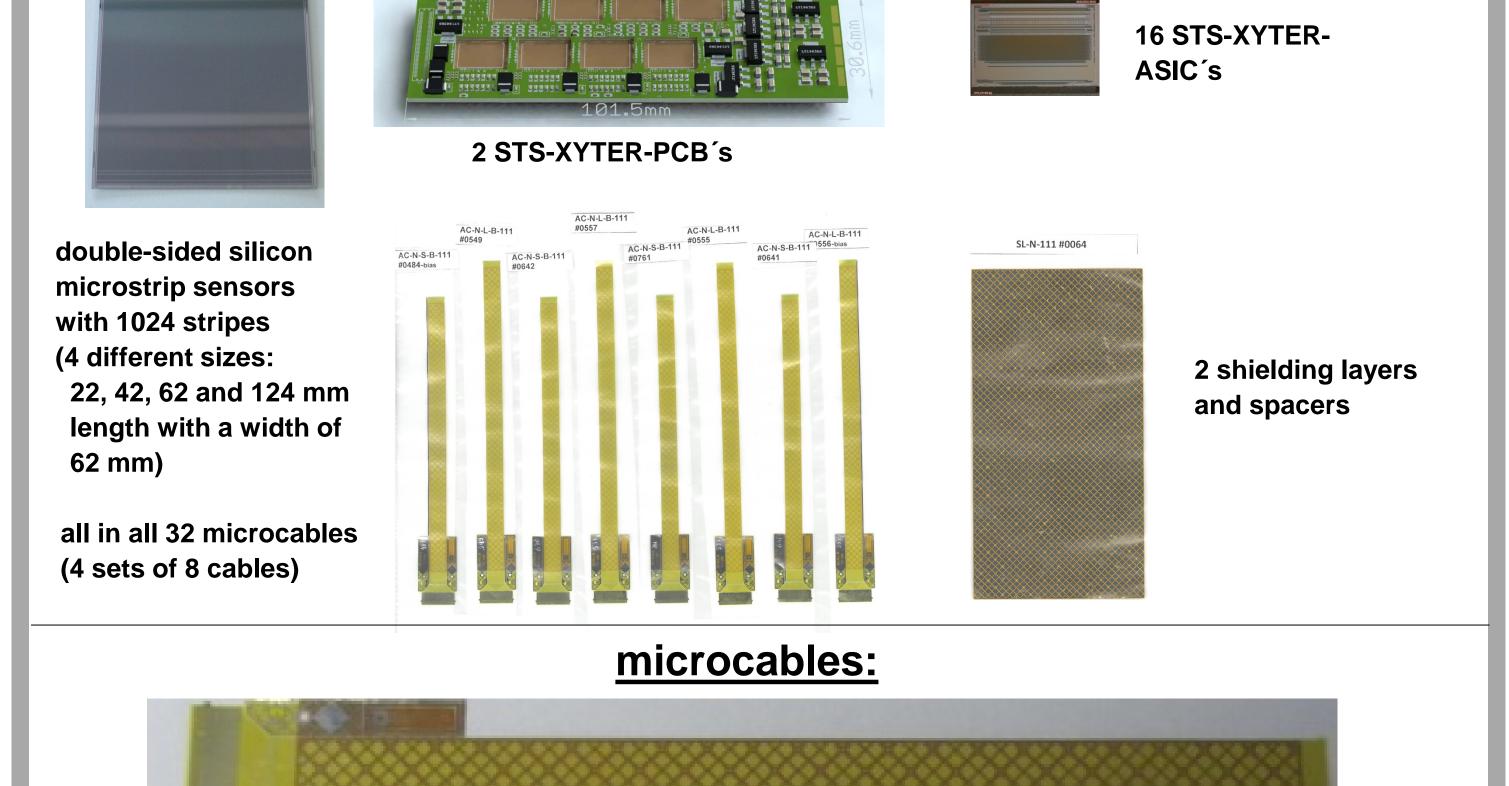
STS module



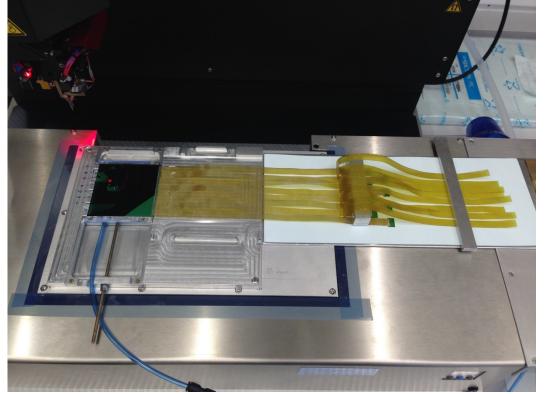
A STS-module consists of a double-sided silicon microstrip sensor that is connected via microcables to two front-end-electronics PCB's.

Assembly material

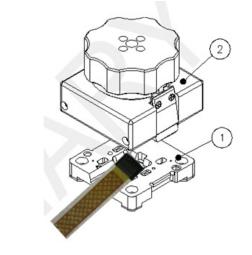




and second layer), quality measurement and protection with Globtop



aligning and TAB-bonding



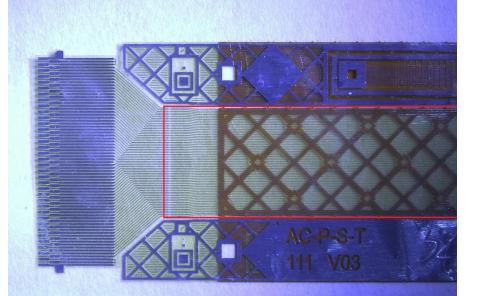
connectivity tests will be performed with a Pogo pin socket that puts the **STS-ASIC** in minimal operation

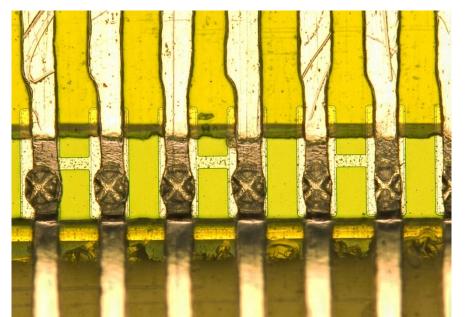


<u>Step 3:</u> die- and wirebonding of 4 STS-XYTER-ASIC's to the first row of the PCB, quality measurement and protection with Globtop; then procedure is repeated for the second row

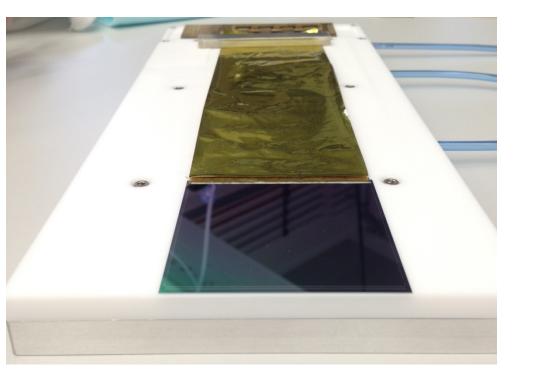


The 24 µm thick microcables (10 µm polyimide + 14 µm Aluminum), each with 64 aluminum traces of about 40 µm width, have to be connected on one side to an STS-XYTER-ASIC and on the other side to the P- or N-side of the sensor in two staggered layers with TAB-bonding.





Step 4: glueing of shielding layers and spacers



This semi-module then has to be turned to the other side of the sensor and the steps have to be repeated!

The developed fixtures fix the microcables with vacuum and allow alignment in x-, y- and theta-direction before bonding.

testfan of the microcable with bonding area, will be cut after connectivity tests

aluminum traces, TAB-bonded to bond pads of a silicon sensor

Cable stack

Additional spacers are placed between two signal layers to reduce the capacitance contributions from the adjacent connecting layers. Shielding layers reduce the noise level and prevent shorting between the stacks of cables.

