

RPC 2016 – XIII Workshop on Resistive Plate Chambers and Related Detectors



Performance studies of a single HV stack MRPC prototype for CBM

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Outline:

- CBM-ToF requirements
- TDR Tof wall design
- Test beam time at GSI
- Single stack vs. double stack
- Performance results
- Summary / Outlook



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CBM spectrometer



Engineering design of the CBM experiment



Nominal ToF position is between 6 m and 10 m from the target

Movable design allows for optimization of the detection efficiency of weakly decaying particles (Kaons)

Interaction rate 10 MHz



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Incident particle flux



URQMD simulated charged particle flux for Au + Au (minimum bias) events at 25 **AGeV** assuming an interaction rate of 10 MHz





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Requirements



Charged hadron identification is provided by Time-of-Flight (ToF) measurement



CBM-ToF Requirements

- > Full system time resolution $\sigma_T \sim 80$ ps
- Efficiency > 95 %
- ➢ Rate capability ≤ 30 kHz/cm²
- Polar angular range 2.5° 25°
- > Occupancy < 5 %</p>
- Low power electronics
 - (~120.000 channels)
- Free streaming data acquisition







TDR ToF wall layout



	M6	M5	M5	M4	M5	M5	M6		
M6		M5	M5	M4	M5	M5	1110	M6	
	M6	M5	M5	M4	M5	M5	M6		
M6	WIO	M5	M5	M4	M5	M5	IVIO	M6	
	M6	M5	M5	M4	M5	M5	M6		
M6	MO	M5	M5	M4	M5	M5	IVIO	M6	
	M6	M5	M5	M4	M5	M5	M6		
M6	INIO	M5	M5	M4	M5	M5	IVIO	M6	
	M6	M5	M5	M4	M5	M5	M6		
M6	INIO	M5	M5	M4	M5	M5	IVIO	M6	
		M5	M5	M4	M5	M5	ME		
M6	IVIO	M5	M5	M4	M5	M5	IVIO	M6	
		M5				M5	ME		
M6	IVIO	M5				M5	IVIO	M6	
	M5	M5	IVI3	IVII	IVI3	M5	M5		
M6	M5	M5				M5	M5	M6	
	M5	M5				M5	M5	M6	
M6	M5	M5	M2		M2	M5	M5		
	M5	M5				M5	M5		
M6	M5	M5				M5	M5	M6	
	M5	M5		N41	142	M5	M5		
M6	MG	M5	IVI3	IVII	IVI3	M5	ME	M6	
		M5				M5	IVIO		
M6		M5	M5	M4	M5	M5	MC	M6	
		M5	M5	M4	M5	M5	IVIO		
M6		M5	M5	M4	M5	M5	MC	M6	
		M5	M5	M4	M5	M5	IVID		
M6		M5	M5	M4	M5	M5	MG	M6	
		M5	M5	M4	M5	M5	IVIO		
M6	MG	M5	M5	M4	M5	M5	ME	M6	
	IVIO	M5	M5	M4	M5	M5	IVID		
M6	MG	M5	M5	M4	M5	M5	MG	M6	
	1/16	M5	M5	M4	M5	M5	IVID		
M6	MG	M5	M5	M4	M5	M5	ME	M6	
	M6	M5	M5	M4	M5	M5	IVID		

- 6 types of modules (M1 – M6) only
- A module contains several MRPC counters
- Region containing counters equipped with float glass
 - Region containing counters equipped with low resistive glass







TDR ToF wall layout



		M5	M5	MA	MS	MS		
M6	M6	CIVI	CIVI	1014	CIVI	CIVI	M6	
		M5	M5	M4	M5	M5		M6
M6	M6	M5	M5	M4	M5	M5	MC	
		M5	M5	M4	M5	M5	IVIO	M6

6 types of modules (M1 – M6) only

Module	Number	Module size	Number of	Number of	Number of	Number
notation	of		MRPCs	MRPCs	cells per	of cells
	modules	mm ³	per module	in total	module	in total
M1	2	$1270 \times 1417 \times 239$	32	64	2048	4096
M2	2	$2140\times705\times239$	27	54	1728	3456
M3	4	$1850 \times 1417 \times 239$	42	168	2688	10752
M4	24	$1802 \times 490 \times 110$	5	120	160	3840
M5	132	$1802 \times 490 \times 110$	5	660	160	21120
M6	62	$1802\times740\times110$	5	310	160	9920
Sum	226			1376		53184

Table 3.1: Numbers and dimensions of the modules.

IVID	M6	IVI5	IVI5	IV14	IVI5	IVI5	M6	IVID		
		M5	M5	M4	M5	M5				10(2(0))
M6	M6	M5	M5	M4	M5	M5	M6	M6	⇒ 106368 read-	
	IVIO	M5	M5	M4	M5	M5				, 200000 2000
										out channels







TDR MRPC arrangement

	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC1	MRPC1	MRPC1	MRPC1	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC1	MRPC1 MRPC1	MRPC1	MRPC1	MRPC1 MRPC1	MRPC1	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC1 MRPC1	MRPC1 MRPC1			MRPC1 MRPC1	MRPC1 MRPC1	MRPC2	MRPC2	MPPC2	MRPC2	MRPC2
	MIDDC2	MDDC2	MDDC2	MDDC2	MODCO	MRPC1 MRPC1	MRPC1 MRPC1			MRPC1 MRPC1	MRPC1 MRPC1	MDDC2	MADDCO	MDDC2	MADDC2	MARROR
	MRPC2	MIRPC2	MRPC2	MRPC2	MRPC2	MRPC1	MRPC1 MRPC1	MRPC1	MRPC1	MRPC1 MRPC1	MRPC1	MRPC2	MRPC2	MIRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC1	MRPC1	MRPC1	MRPC1	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
Next Next Next Next Next Next Next Next	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
ban	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
Neva Neva Neva Neva Neva Neva Neva Neva	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
Desit Desit <th< td=""><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td><td>MRPC2</td></th<>	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2	MRPC2
NBCG NBCG NBCG NBCG NBCG NBCG NBCG NBCG	IRPCS HRPCS HI	IPCS HRPCS HR	PCS HRPCS H	RPCS HRPC4 H	IRPCI HRPCI I	IRPC4 MRPC4							_			
	IRPCS HRPCS HI	IPCS HRPCS HE	PCS HRPCS H	RPC1 HRPC4 H		1RPC4 HRPC4			_							
Hera Hera Hera Hera Hera Hera Hera Hera	IRPCS HRPCS HI	IPCS HRPCS HR	PCS HRPCS H	RPCS		_										
Here Here Here Here Here Here Here Here	IRPCS HRPCS	ev HRPCK HR	PCI HRPCI H	HRPC4 H	IRPC4 HRPC4 I	IRPCI HRPCI										
	IRPCS HRPCS			-		-					MF	RPC	21			
	IRPCS HRPCS	IPCO HRPCO HR	PCI MRPCI H	RPC4									<u> </u>	_		
Nan Na	IRPCS HRPCS			HRPC4 P	1RPC4 HRPC4 1	-					ΝЛΕ		2			
	IRPCS HRPCS	FCI INFCI IN	PCI HEPCI H	<u> </u>									~~			
	IRPCS HRPCS	19C4 HRPC4 HB		HRPC4 H	IRPCA HRPCA I	IRPC4 HRPC4				_			_			
	IRPCS HRPCS										ЛR	PC	За			
	IRPCS HRPCS	IPCE HRPCE HE	PC4 HRPC4 H	RPC4	IRPCE HRPCE I	TRPCC MRPCC										
	IRPCS HRPCS			HRPC4 H	IRPCI HRPCI I	IRPC4 MRPC4				Ν	1D	DC	2h			
	IRPCS HRPCS	nerce Hi	A ST TREST H							- 1	VII/		20			
	IRPCS HRPCS HI	19C4 HRPC4 HB	PC4 HRPC4 H	RPC4	IRPC4 MRPC4 1	IRPC4 MRPC4							24			
												(PC	.4			





CBM ToF



TDR MRPC arrangement



| MRPC2 |
|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|-------|
| MRPC2 |
| MRPC2 |

MRPC notation	MRPC1	MRPC2	MRPC3a	MRPC3b	MRPC4
Number of MRPCs	40	246	580	200	310
Active area [mm ²]	300×100	300×200	320×270	320×270	320×530
Number of Strips per MRPC	64	64	32	32	32
Strip length [mm]	100	200	270	270	530
Granularity (cell size) [mm ²]	472.4	944.8	2700	2700	5300
Number of gas gaps	10	10	8	8	8
Gap size μ m	140	140	220	240	280,140
Glass size [mm ²]	320×100	320×200	330 × 280	330×280	330×540
Glass thickness [mm]	0.7	0.7	0.7	0.2	B X 0.28
Number of glass plates	12	12	9	12	X 12
Glass type	low res.	low res.	low res.	float	float
Total glass surface [m ²]	15.36	188.93	482.33	166.32	497.18

Table 3.2: Numbers and dimensions of different MRPC counters.









Modules





a: MRPC, b: Preamplifier (PADI), c: feed-through PCB, d: connectors, e: crate, f: TDC and read out



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Modules







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Test beam time @ GSI



Setup



- Test beam time in October 2014 at GSI (Hades cave)
- Sm beam with 1.2A GeV kin. energy
- 5 mm thick lead target •
- "Uniform" illumination of the counter surface
- Flux on the lower part of the setup was about few hundred Hz/cm²
- Delivered flux does not meet the **CBM** requirements
- R143a 85%, SF6 10%, iBut 5%







Test beam time @ GSI



Full size demonstrator and reference MRPC used for the performance analysis

MRPC glass stack active area strips strip / gap glass type glass thickness number of gaps gap width MRPC-P2 (HD) differential single 32 x 27 cm² 32 7/ 3 low resistive glass 0.7 mm 8 220 μm THU-strip (Beijing) differential double 24 x 27 cm² 24 7/ 3 low resistive glass 0.7 mm 2 x 4 250 μm MRPC-P5 (HD) differential single 15 x 4 cm² 16 7.6 / 1.8 mm low resistive glass 1.0 mm 6 220 μm

MRPC-P2



THU-strip



MRPC-P5









2 MRPC concepts





Advantages

- simpler construction

- symmetric signal path
- fewer glass plates (#9)
- lower weight
- impedance matching easy possible (100 Ω)

Disadvantages

- higher High Voltage (> ± 10 kV)
- bigger cluster size

Advantages

- lower High Voltage ($< \pm 6$ kV)
- smaller cluster size

Disadvantages

- more complex construction
- more glass plates (#10)
- impedance matching hardly possible (100 Ω)







Counter occupation

TOF

CBM





Efficiency





Efficiency=
 Matched hit pairs in dut - ref
 Matched bit pairs in dia - ref

Matched hit pairs in dia - ref

- Data points at ±11 kV in the left plot can be compared with ±5.5 kV in the right plot.
- Single stack MRPC shows slightly better efficiency









Edge effects





→ CBM

Time difference vs. particle velocity







Time difference vs. particle velocity







Time resolution



Differential singel stack MRPC Differential double stack MRPC VS. with 8 gaps with 2 x 4 gaps Time resolution vs. applied high voltage (HV) Time resolution vs. applied high voltage (HV) 80 80 Time resolution [ps] Time resolution [ps] **Resolution** ≈ 62 75 75 ns 70 70 **Resolution** ≈ 65 ps 65 65 60 60 Cut condition: Set3BV Cut condition: Set3B\ $U_{threshold} = 150 \text{ mV}$ 55 U_{threshold} = 170 mV 55 $U_{threshold} = 200 \text{ mV}$ = 200 m∨ 50 50 10 10.5 11 11.5 12 12.5 13 5 5.1 5.9 5.2 5.3 5.4 5 .5 5.65.7 5.8 6 HV [kV] HV [kV] Time resolution vs. PADI6 threshold 80 Time resolution [ps] 75 70 65 60 High Voltage = 5.5 kV cut condition: set3 55 cut condition: set3best 50 165 170 175 180 185 190 195 200 205 Threshold [mV] **RPC 2016** Ingo Deppner Gent 22 - 26.02.2016

- Data points at $\pm 11 \text{ kV}$ in the left plot can be compared with $\pm 5.5 \text{ kV}$ in the right plot.
- Single stack
 MRPC shows
 slightly time
 resolution.
- Single counter resolution is in the order of 45 ps including all electronic components.

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Cluster size





• Time resolution does not deteriorate with cluster size bigger than one



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Cluster multiplicity





Counter time resolution below 50 ps up to the highest multiplicity @ an occupancy of about 50%



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Summary/Outlook



Summary

- > TDR is approved. However no final decision regarding counter design is taken.
- ➤ The design of the differential single stack MRPC from Heidelberg is driven by the freestreaming readout ⇒ impedance matching is realized.
- The single stack MRPC shows slightly better efficiency and time resolution in comparison to a double stack MRPC.
- > The double stack MRPC shows a smaller cluster size (about 1.6).
- Single counter resolution is in the order of 45 ps including all electronic contributions.
- However, in a free running mode an impedance matched MRPC might show a better performance due to minimized signal reflections.

<u>Outlook</u>

- Load test for all available full size prototypes in Nov. 2015 with heavy ions at SPS CERN
- Among them 3 full size modules M4 with counters MRPC3a and MRPC3b were tested
- Data analysis is still ongoing
- Selection of the final layout and counter configurations this year based on beam time results.
- Start of the low resistive glass production this year



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Outlook



Event display after calibration





Thank you for your attention



Contributing institutions:

Tsinghua	Beijing,
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GSI	Darmstadt,
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USTC	Hefei,
PI	Heidelberg,
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CBM ToF



Backup Slides



CBM Physics topics

- > Deconfinement / phase transition at high ρ_B
- QCD critical endpoint
- > The equation-of-state at high ρ_B
- \blacktriangleright chiral symmetry restoration at high ρ_{B}

Observables

- excitation function and flow of strangeness and charm
- collective flow of hadrons
- particle production at threshold energies
- excitation function of event-by-event fluctuations
- excitation function of low-mass lepton pairs
- \succ in-medium modifications of hadrons $(\rho, \omega, \phi \rightarrow e {+} e {-} (\mu {+} \mu {-}), \, D)$





Kaon acceptance depends critically on TOF resolution







Backup Slides





T0 – determination

Diamond start counter

- use HADES development,
- develop DAQ interface,
- limited to reaction rates ~ 100kHz

Software solution

- available for all systems
- needs fast particles from reaction
- demonstrated to work for central and semi-central heavy system

Beam fragmentation counter

- peripheral HI reaction have fast particles from projectile fragmentation
- equip region E with timing counters (BFTC)

Reaction counter

- needed for high rate pA reactions (charm at SIS 100)
- reaction counter at polar angles $35^{\circ} < \theta < 60^{\circ}$.

CBM Collaboration Meeting, Dubna, 27.09.2013

N.Herrmann, PI, Uni-HD

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Backup Slides



Main parameters comparison	PADI-1	PADI-2	PADI-6	PADI-8
Channels per chip	3	4	4	8
PA Bandwidth (MHz)	280	293	416	411
PA Voltage Gain	74	87	244	251
Conversion Gain (mV/fC)	6.3	7.8	35	30
Baseline DC offset σ (mV)	6.7	21.9	5.9	1
PA Noise (mV_{RMS})	3.37	2.19	5.82	5.5
Equivalent Noise Charge (e_{RMS})	3512	1753	1039	1145
Threshold type	Extern	Extern	Ext. & DAC	DAC
Threshold dynamics $(\pm \text{ mV})$	Non.lin. 280	Non.lin. 300	Lin. 500	Lin. 750
Input Impedance Range (Ω)	30-450	37 - 370	38 - 165	30 - 160
Power consumption (mW/channel)	21.6	17.4	17.7	17

Table 3.4: Main parameters of the PAD





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Selection cuts in ana hits.C switch(iSet){ case 0: switch(iSel){ // selection cuts case 0: tofAnaTestbeam->SetMul4Max(10); // Max Multiplicity in Ref - RPC tofAnaTestbeam->SetCh4Sel(8.); // Center of channel selection window tofAnaTestbeam->SetDCh4Sel(70.); // Width of channel selection window tofAnaTestbeam->SetPosY4Sel(10.5); // Y Position selection in fraction of strip length tofAnaTestbeam->SetMulDMax(10.); // Max Multiplicity in Diamond tofAnaTestbeam->SetDTDia(0.); // Time difference to additional diamond break: case 1: tofAnaTestbeam->SetMulOMax(10); // Max Multiplicity in dut - RPC tofAnaTestbeam->SetMul4Max(1); // Max Multiplicity in Ref - RPC Cut 1 tofAnaTestbeam->SetCh4Sel(8.); // Center of channel selection window tofAnaTestbeam->SetDCh4Sel(7.); // Width of channel selection window tofAnaTestbeam->SetPosY4Sel(0.5); // Y Position selection in fraction of strip length tofAnaTestbeam->SetMulDMax(1.); // Max Multiplicity in Diamond // Time difference to additional diamond tofAnaTestbeam->SetDTDia(0.); break; ase 2: tofAnaTestbeam->SetMul4Max(1); // Max Multiplicity in Ref - RPC // Center of channel selection window tofAnaTestbeam->SetCh4Sel(8.); // Width of channel selection window tofAnaTestbeam->SetDCh4Sel(7.); tofAnaTestbeam->SetPosY4Sel(0.5); // Y Position selection in fraction of strip length tofAnaTestbeam->SetMulDMax(1.); // Max Multiplicity in Diamond // Time difference to additional diamond tofAnaTestbeam->SetDTDia(500.); break: case 3: tofAnaTestbeam->SetMul4Max(1): // Max Multiplicity in Ref - RPC tofAnaTestbeam->SetCh4Sel(8.); // Center of channel selection window Cut 3 tofAnaTestbeam->SetDCh4Sel(4.); // Width of channel selection window tofAnaTestbeam->SetPosY4Sel(0.3); // Y Position selection in fraction of strip length tofAnaTestbeam->SetMulDMax(1.); // Max Multiplicity in Diamond // Time difference to additional diamond tofAnaTestbeam->SetDTDia(500.); ault : 31 mes repair

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Selection cuts in ana hits.C switch(iSet){ case 0: switch(iSel){ // selection cuts Time - position correlation Time - position correlation case 0: tofAnaTestbeam->SetMul4Max(10): // Ma /lean x 0.359 tofAnaTestbeam->SetCh4Sel(8.); // Cer 2800 380 Mean y -0.02347 Mean y -0.02347 2.483 RMS x RMS x 0 612 tofAnaTestbeam->SetDCh4Sel(70.); // W1 600 tofAnaTestbeam->SetPosY4Sel(10.5); // Y 400 tofAnaTestbeam->SetMulDMax(10.); // Ma 200 tofAnaTestbeam->SetDTDia(0.); // Tin break: -200 case 1: tofAnaTestbeam->SetMulOMax(10): // Ma -400 tofAnaTestbeam->SetMul4Max(1); // Max -600 tofAnaTestbeam->SetCh4Sel(8.): // Cer -800 -80 tofAnaTestbeam->SetDCh4Sel(7.); // Wic -1000 tofAnaTestbeam->SetPosY4Sel(0.5): // Y / X4 [cm] tofAnaTestbeam->SetMulDMax(1.); // Max Multiplicity in Diamond // Time difference to additional diamond tofAnaTestbeam->SetDTDia(0.); break: ase Z: Time - position correlation Time - position correlation tofAnaTestbeam->SetMul4Max(1); // Max 1000 tofAnaTestbeam->SetCh4Sel(8.); // Cent 300 Mean x 0.685 200 0.36 Mean y -0.637 -0.63 // Widt 800 tofAnaTestbeam->SetDCh4Sel(7,); RMS x 1.764 0.492 600 tofAnaTestbeam->SetPosY4Sel(0.5); // Y Pc 400 tofAnaTestbeam->SetMulDMax(1.); // Max 20 tofAnaTestbeam->SetDTDia(500.); // Time break: -200 case 3: 10 tofAnaTestbeam->SetMul4Max(1): // Max -400 tofAnaTestbeam->SetCh4Sel(8.); // Cent 600 -600 tofAnaTestbeam->SetDCh4Sel(4.); // Widt -800 -80 tofAnaTestbeam->SetPosY4Sel(0.3); // Y PC_1000 tofAnaTestbeam->SetMulDMax(1.); // Max X4 [cm] tofAnaTestbeam->SetDTDia(500.); // Time university efault 32 men nehener

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Time – velocity correlation





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