

1st FAIR FEE Workshop

Thursday 13 October 2005

Plenary (09:00-10:30)

time	[id] title	presenter
09:00	[24] Front-end electronics for the PANDA pixel detectors: a custom solution in 0.13 mm?	RIVETTI, Angelo
09:30	[25] Front-End Electronics for DEPFET Sensors	FISCHER, Peter
10:00	[26] State of the Art CMOS Sensors for Charged Particle Tracking : On Signal Processing and On Chip Readout Architectures	HEINI, Sebastien

Plenary (11:00-12:50)

time	[id] title	presenter
11:00	[27] N-XYTER - The First Dedicated Neutron Detector Readout ASIC	SCHMIDT, Christian Joachim
11:35	[28] Status and perspective of the PASA developments for the FAST-TRD for the CBM experiment	SOLTVEIT, Hans Kristian
12:00	[29] Si-Strip FEE development for CBM	VORONIN, Alexander
12:25	[30] Where now for FEE in FutureDAQ and NUSTAR DAQ?	LAZARUS, Ian