

**Joint CBM / Panda DAQ  
developments - "Kick-off"  
meeting**

**Report of Contributions**

Contribution ID: 0

Type: **not specified**

## **Panda DAQ Overview**

*Thursday, 19 February 2015 13:15 (30 minutes)*

**Presenter:** KÜHN, Wolfgang (Univ. Giessen, II.Physikalisches Institut)

**Session Classification:** Introduction

Contribution ID: 1

Type: **not specified**

## **Introduction**

*Thursday, 19 February 2015 13:00 (15 minutes)*

**Session Classification:** Introduction

Contribution ID: 2

Type: **not specified**

## **CBM DAQ Overview**

*Thursday, 19 February 2015 14:00 (20 minutes)*

**Presenter:** MÜLLER, Walter F. J. (FAIR, Darmstadt)

**Session Classification:** Introduction

Contribution ID: 3

Type: **not specified**

## **CBM FPGA-based sub-systems**

*Thursday, 19 February 2015 14:20 (10 minutes)*

**Presenter:** PAULY, Christian (Bergische Universität Wuppertal)

**Session Classification:** Introduction

Contribution ID: 4

Type: **not specified**

## **Hardware: Trb3 family**

*Thursday, 19 February 2015 14:45 (10 minutes)*

**Presenter:** MICHEL, Jan (IKF, Universität Frankfurt)

**Session Classification:** Hardware

Contribution ID: 5

Type: **not specified**

## Hardware: Fpga crate modules

*Thursday, 19 February 2015 15:05 (15 minutes)*

**Presenter:** MARCINIEWSKI, Pawel (Uppsala University)

**Session Classification:** Hardware

Contribution ID: 6

Type: **not specified**

## Hardware: AFCK

*Thursday, 19 February 2015 15:55 (20 minutes)*

**Presenter:** ZABOLOTNY, Wojciech (WUT, Warsaw)

**Session Classification:** Hardware



Contribution ID: 7

Type: **not specified**

## **Discussion: Hardware Projects**

*Thursday, 19 February 2015 16:25 (30 minutes)*

**Session Classification:** Hardware

Contribution ID: 8

Type: **not specified**

## **SODA optical network**

*Thursday, 19 February 2015 16:55 (20 minutes)*

**Presenter:** KAVATSYUK, Myroslav (KVI, University of Groningen)

**Session Classification:** FPGA Network

Contribution ID: 9

Type: **not specified**

## **FPGA Network for CBM**

*Thursday, 19 February 2015 17:25 (20 minutes)*

**Presenter:** MICHEL, Jan (IKF, Universität Frankfurt)

**Session Classification:** FPGA Network

Contribution ID: **10**

Type: **not specified**

## **Discussion: "FairNet" for both experiments?**

*Thursday, 19 February 2015 17:55 (30 minutes)*

**Session Classification:** FPGA Network

Contribution ID: 11

Type: **not specified**

## **FPGA-TDC: Status and Plans**

*Friday, 20 February 2015 09:00 (15 minutes)*

**Presenter:** UGUR, Cahit (GSI, Darmstadt)

**Session Classification:** TDC

Contribution ID: 12

Type: **not specified**

## **A low-accuracy TDC?**

*Friday, 20 February 2015 09:25 (5 minutes)*

**Presenter:** MICHEL, Jan (IKF, Universität Frankfurt)

**Session Classification:** TDC

Contribution ID: 13

Type: **not specified**

## **RICH / DIRC front-ends**

*Friday, 20 February 2015 09:35 (10 minutes)*

**Presenter:** TRAXLER, Michael (GSI, Darmstadt)

**Session Classification:** TDC

Contribution ID: 14

Type: **not specified**

## **Control of ASICS via GBTx**

*Friday, 20 February 2015 10:15 (10 minutes)*

**Presenter:** ZABOLOTNY, Wojciech (WUT, Warsaw)

**Session Classification:** GBTx



Contribution ID: 15

Type: **not specified**

## **GBT-FPGA implementation**

*Friday, 20 February 2015 09:55 (15 minutes)*

**Presenter:** BYSZUK, Adrian (WUT, Warsaw)

**Session Classification:** GBTx

Contribution ID: 16

Type: **not specified**

## **Fast Control & Timing (Panda)**

*Friday, 20 February 2015 12:00 (10 minutes)*

**Presenter:** KAVATSYUK, Myroslav (KVI, University of Groningen)

**Session Classification:** Fast Control

Contribution ID: 17

Type: **not specified**

## **Fast Control & Timing (CBM)**

*Friday, 20 February 2015 11:40 (15 minutes)*

**Presenter:** MÜLLER, Walter F. J. (FAIR, Darmstadt)

**Session Classification:** Fast Control

Contribution ID: **18**

Type: **not specified**

## **Discussion: Fast Control / Timing**

*Friday, 20 February 2015 12:15 (15 minutes)*

**Session Classification:** Fast Control

Contribution ID: 19

Type: **not specified**

## **Ethernet transport protocols**

*Friday, 20 February 2015 14:05 (10 minutes)*

**Presenter:** ZABOLOTNY, Wojciech (WUT, Warsaw)

**Session Classification:** Data Transport

Contribution ID: 20

Type: **not specified**

## **Data transport FPGA to Computer (Panda)**

*Friday, 20 February 2015 14:20 (10 minutes)*

**Presenter:** KÜHN, Wolfgang (Univ. Giessen, II.Physikalisches Institut)

**Session Classification:** Data Transport

Contribution ID: 21

Type: **not specified**

## **Data & Control link via GbE**

*Friday, 20 February 2015 13:30 (10 minutes)*

**Presenter:** KORCYL, Grzegorz (Jagiellonian University)

**Session Classification:** Data Transport

Contribution ID: 22

Type: **not specified**

## **Close-Out & Further Plans**

*Friday, 20 February 2015 14:40 (20 minutes)*



Contribution ID: 23

Type: **not specified**

## **Panda GBT**

*Friday, 20 February 2015 10:55 (20 minutes)*

**Presenter:** DROCHNER, Matthias (FZJ)

**Session Classification:** GBTx

Contribution ID: 24

Type: **not specified**

## **Discussion: GBTx**

*Friday, 20 February 2015 11:25 (15 minutes)*

**Session Classification:** GBTx

Contribution ID: 25

Type: **not specified**

## **FLES Input Interface**

*Friday, 20 February 2015 13:50 (10 minutes)*

**Presenter:** HUTTER, Dirk (FIAS, Frankfurt)

**Session Classification:** Data Transport

Contribution ID: 26

Type: **not specified**

## **CBM ROB Prototype**

*Friday, 20 February 2015 11:20 (5 minutes)*

**Presenter:** LEHNERT, Jörg (GSI, Darmstadt)

**Session Classification:** GBTx