

General Concepts of the \bar{P} ANDA DCS

Florian Feldbauer

Helmholtz-Institut Mainz
Johannes Gutenberg-Universität Mainz

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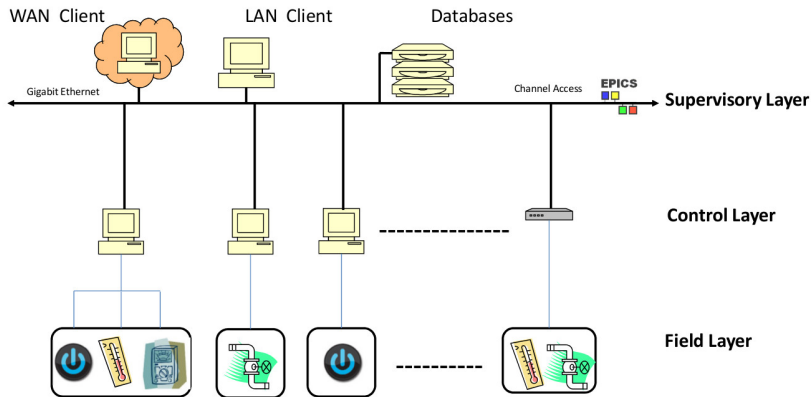


(Some) Requirements of $\overline{\text{P}}\text{ANDA}$ DCS:

- Scalable, modular
- Autonomous operation of each sub-detector (calibration, physics runs, maintenance)
- Common operation of all sub-detectors in *one* DCS system
- Archiving
- Alarm handling
- Non-expert operation
- Graphical UI

16 sub-detectors, 2 magnets, targets, beam
⇒ order of $2 \cdot 10^4$ “slow” channels expected

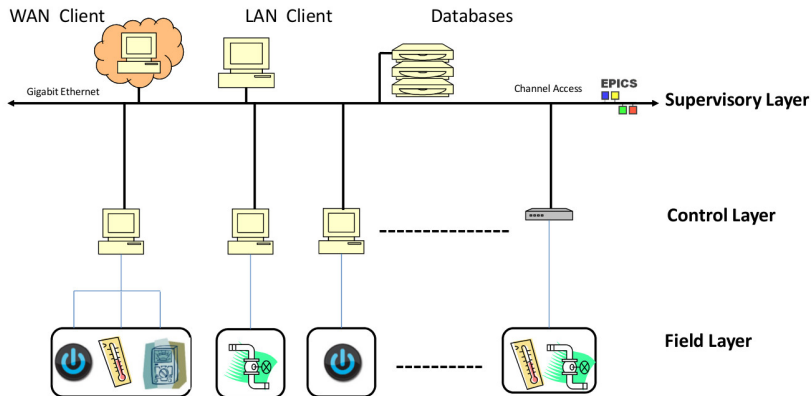
PANDA DCS Overview



Field Layer (FL):

- Temperature monitoring, power supplies, valves,...
- Every device that is monitored or controlled

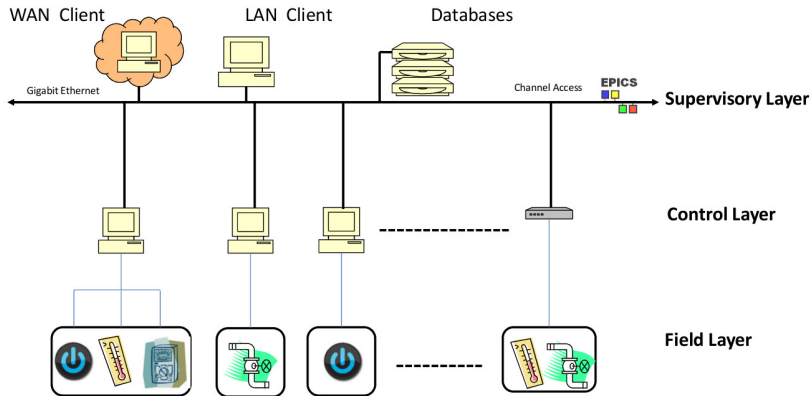
PANDA DCS Overview



Control Layer (CL):

- Input/Output controller communicating with devices in FL
- Used protocols RS232, RS485, TCP/IP, SNMP, CAN bus, ...
- Communication with Supervisory Layer via Ethernet

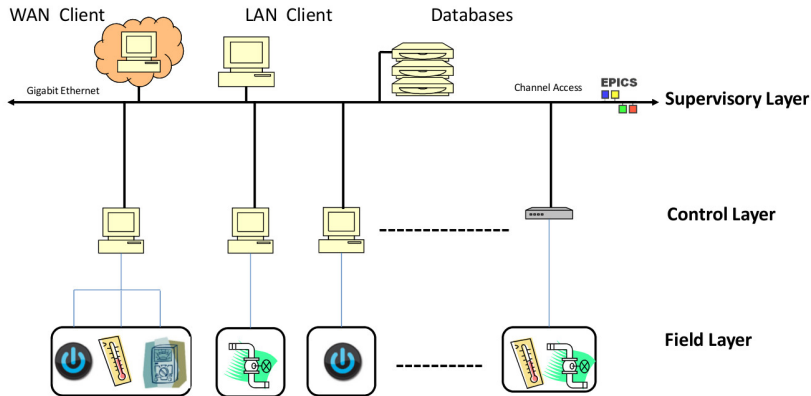
PANDA DCS Overview



Supervisory Layer (SL):

- Databases for data storage
- LAN Clients for graphical user interfaces

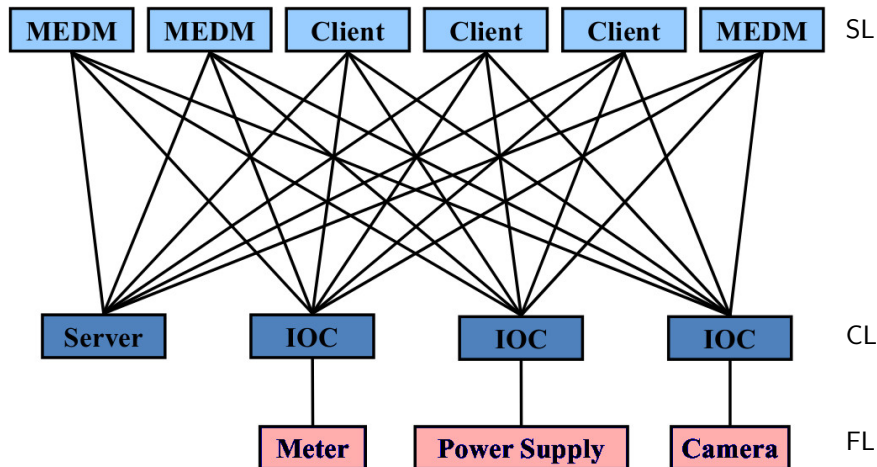
PANDA DCS Overview



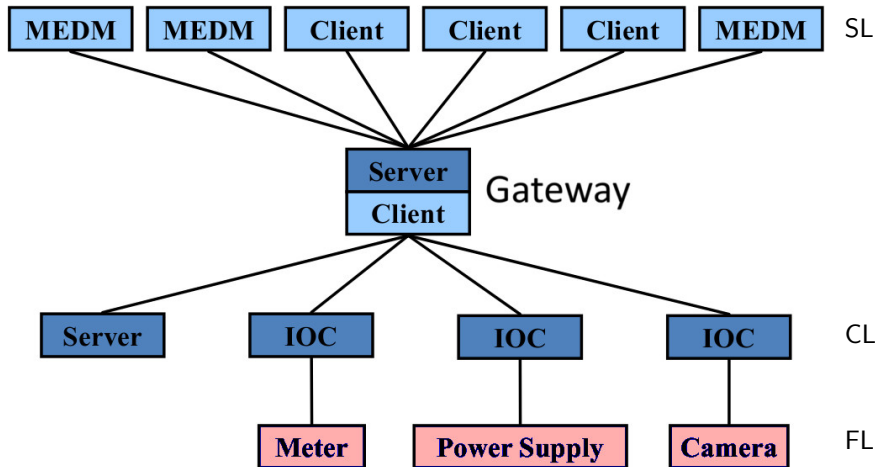
EPICS - **E**xperimental **P**hysics and **I**ndustrial **C**ontrol **S**ystem

- Network protocol based on UDP and TCP ("Channel Access")
- Decentralized architecture
- Freely scalable

EPICS Channel Access

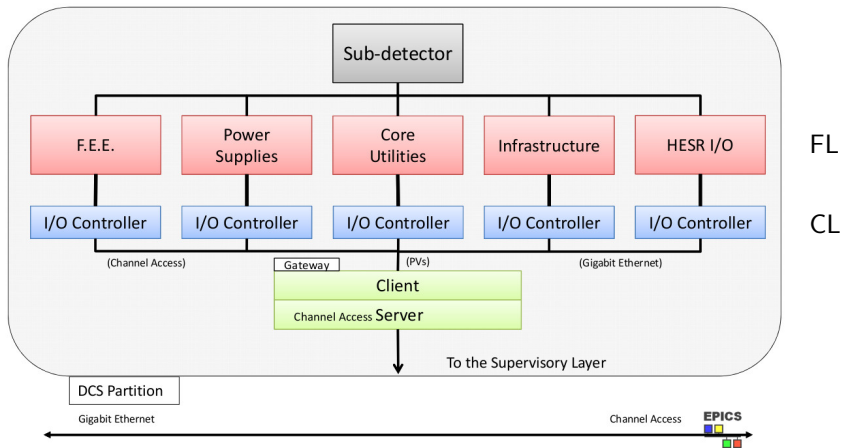


EPICS Channel Access



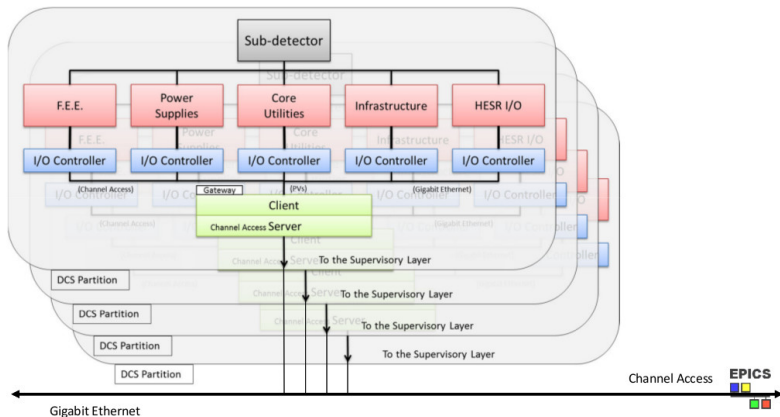
PANDA DCS Architecture - Sub-detector

PANDA DCS partitioning: Each sub-detector has its own DCS Partition



PANDA DCS Architecture - Modularity

Partitions and components in the supervisory layer communicate via Gigabit Ethernet network

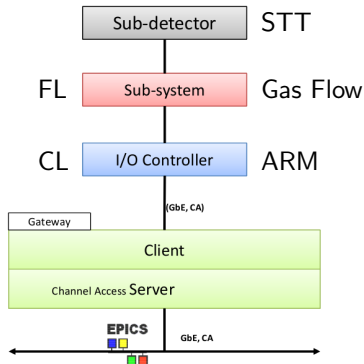


PANDA DCS Architecture - I/O Controller

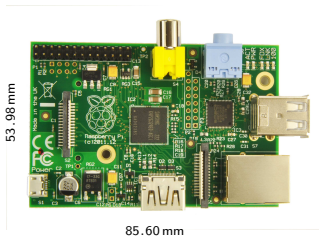
I/O Controller (IOC)

- All devices managing the I/O of a subsystem (PC, micro-controller board, FPGA board etc.)
- Usage of IOCs running on embedded Linux devices
- ARM Development Boards currently used:
 - ARMv6: Raspberry Pi Computer
 - ARMv7: PandaBoard ES

Example:

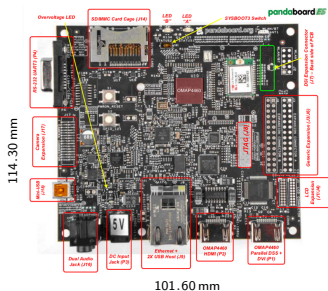


Linux Ready ARM IOC candidates



Raspberry Pi Computer

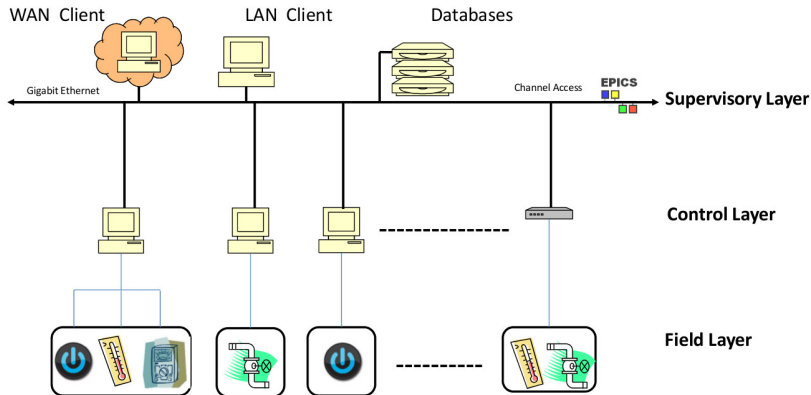
- ARM CPU, 700 MHz
- 512 MB RAM
- 10/100 Ethernet
- 2x USB 2.0, GPIO expansion header



PandaBoard ES

- Dual-core ARM CPU, 1.2 GHz
- 1 GB DDR2 RAM
- 10/100 Ethernet, 802.11 b/g/n Wi-Fi
- 3x USB 2.0, RS-232, GPIO expansion header

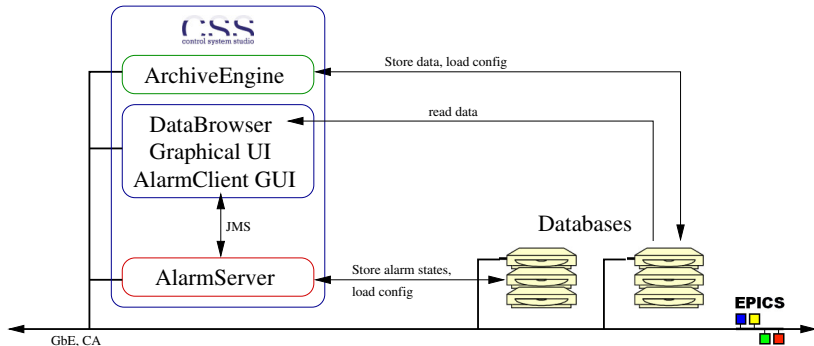
PANDA DCS Supervisory Layer



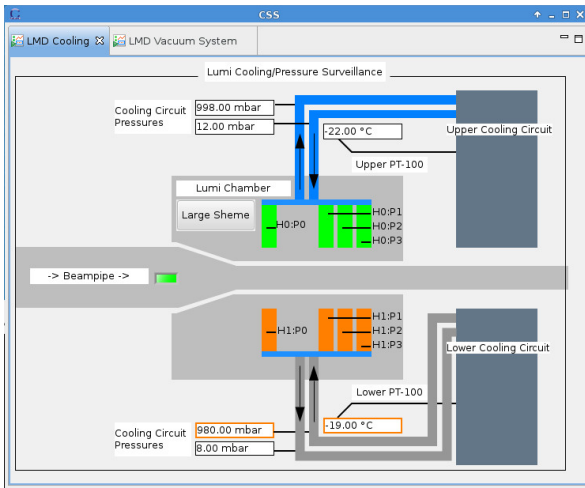
SL: $\overline{\text{PANDA}}$ specific version of Control System Studio (cs-studio)

- Collaboration between DESY, SNS, CLS, BNL, ITER, ...
- Toolkit based on Java and Eclipse RCP
- Modular infrastructure

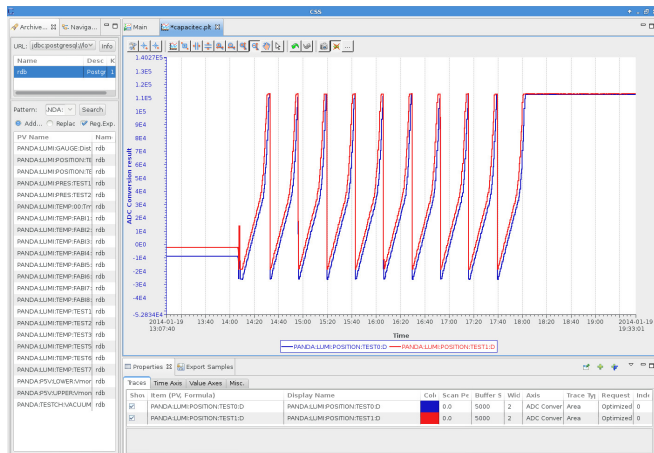
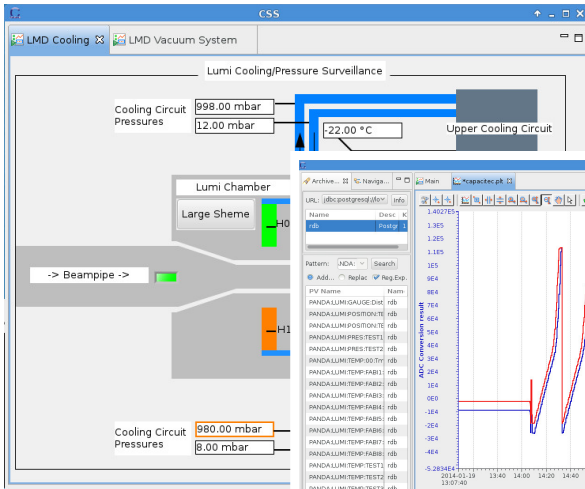
PANDA DCS Supervisory Layer



PANDA DCS Supervisory Layer



PANDA DCS Supervisory Layer



- PANDA DCS based on EPICS and cs-studio
- Modularized architecture
- I/O Controller running on embedded Linux devices
- EPICS CA Gateway to reduce network traffic
- Available/supported hardware \Rightarrow Talks by Tobias and Robin