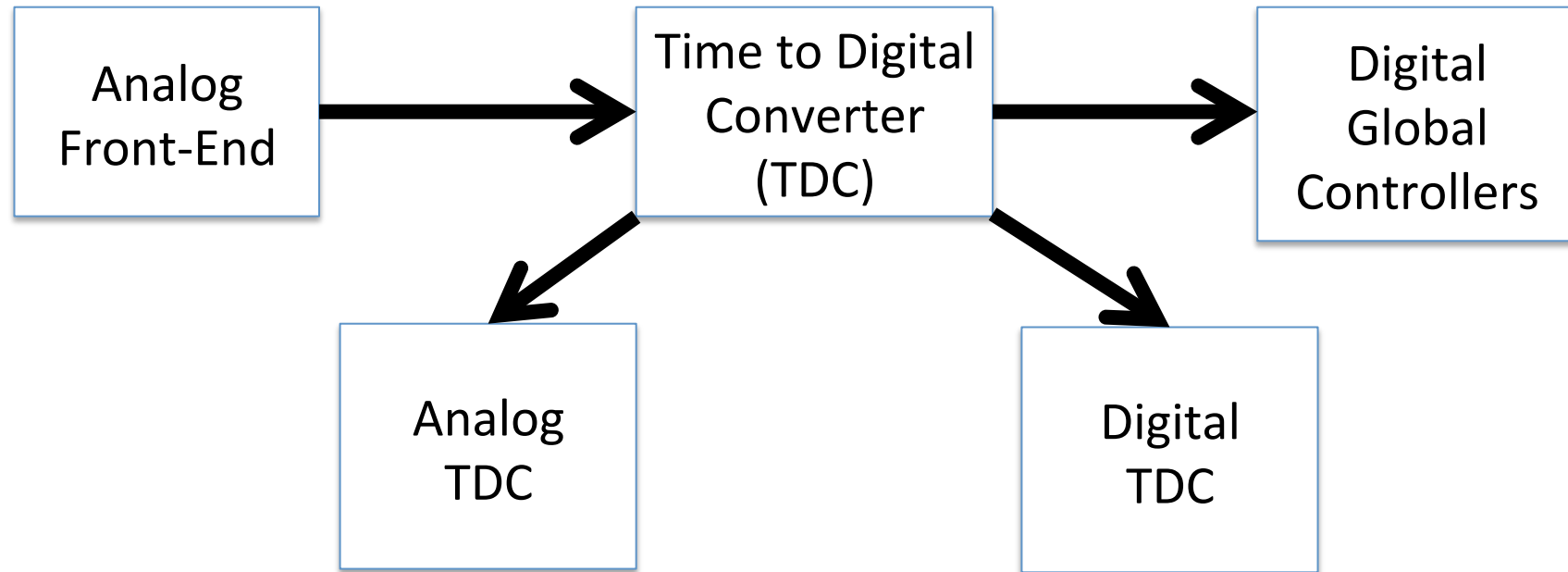


PASTA Analog Part Report

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II. Physikalisches Institut, JLU Gießen

PANDA Meeting

PAnda STrip Asic



Fron-End

	Schematic	Layout	Post-Layout
Preamplifier	Done	To do	To do
Current Buffer	Done	To do	To do
ToT Amplifier	Further studies needed	To do	To do
Hysteresis Comparator	Done	Done	Further studies needed

Done

Further studies needed

To do

Time to Digital Converter

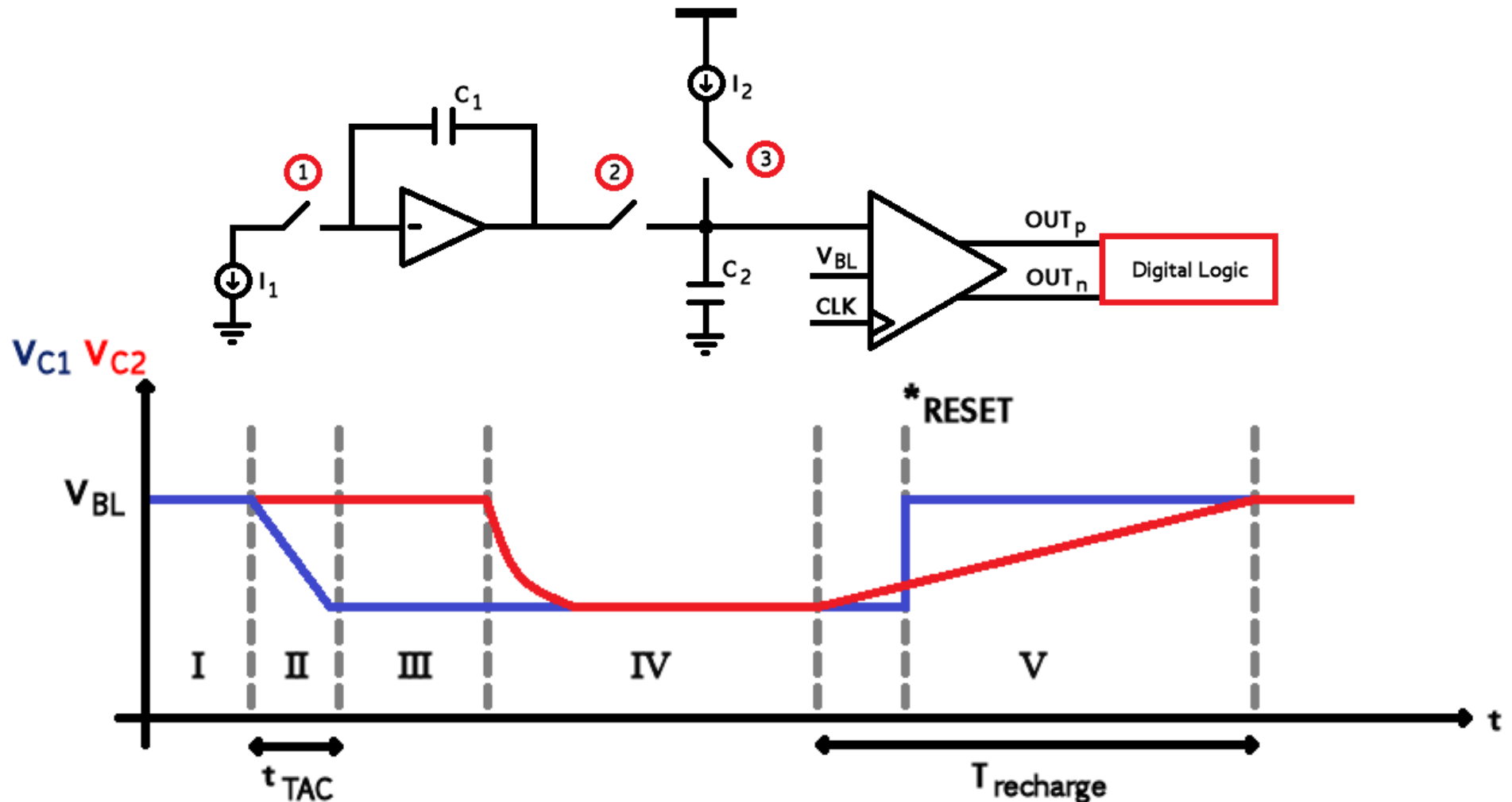
	Schematic	Layout	Post-Layout
Current Source			
Time to Amplitude Converter			
Latched Comparator			

Done

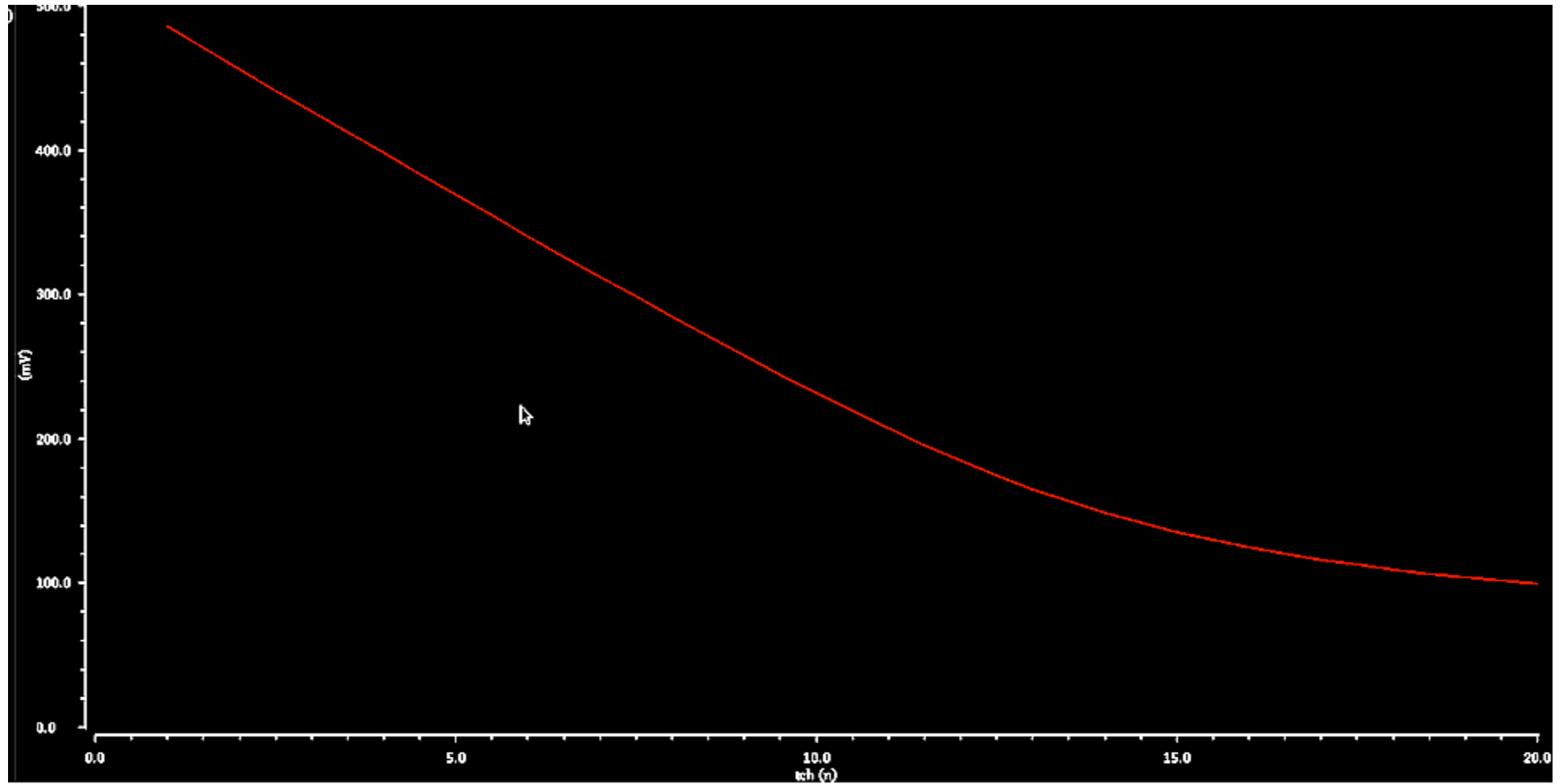
Further studies needed

Not in UMC

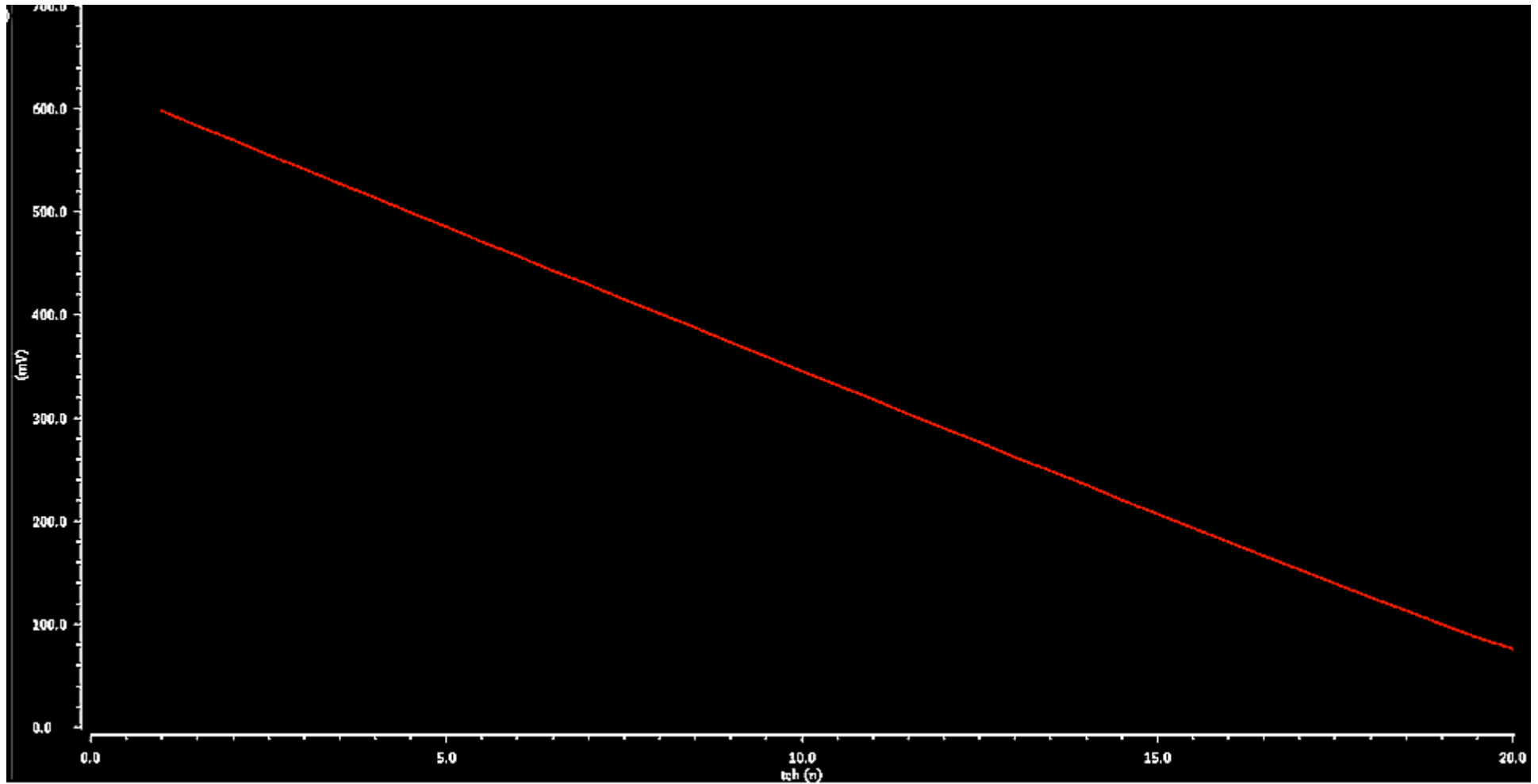
Time to Digital Converter ARCHITECTURE



Time to Amplitude Converter



Time to Amplitude Converter



**Thank you for your
attention**