



# Panda Front-End and DAQ Workshop

## Monday 29 April 2013

### DAQ/Trigger Session I (15:45-16:45)

time	[id] title	presenter
15:45	[18] A 32-ch, 14-bit dual-range, 80-MSPS ADC	Mr MARCINIEWSKI, Pawel
16:05	[19] UDP Implementation in the Juelich readout system	Mr GOERRES, André
16:25	[20] Approach to pile-up resolution in the Forward Shashlik Calorimeter	Mr RYZHIKOV, Sergey