

MVD - DAQ Status

mMVD in May-2025 Beam Time

Benedikt Gutsche for the MVD team

mMVD Hardware

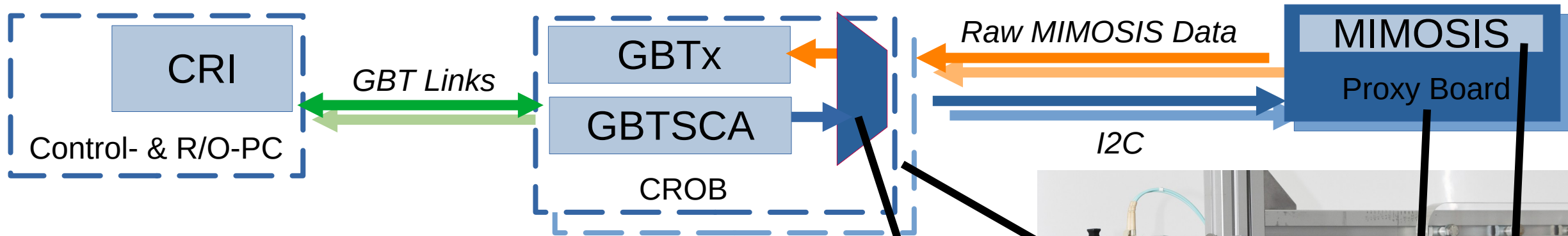
Cave Installation & Synchronization

Beam Time Observations

Results

Status and Outlook

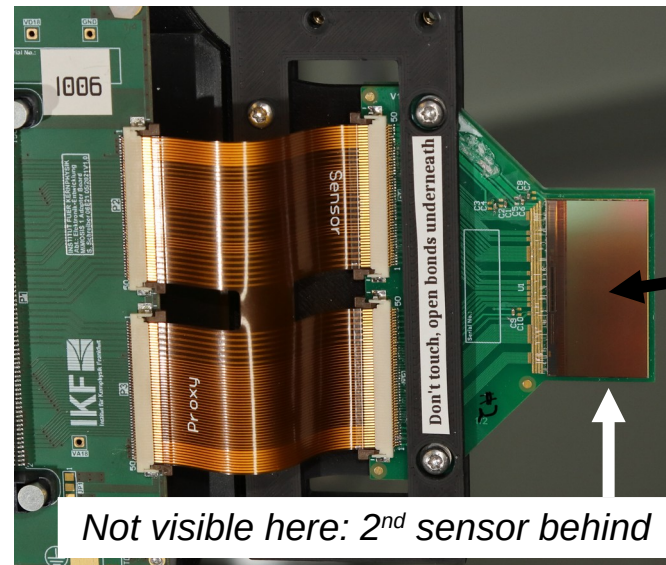
Summary



Hardware setup:

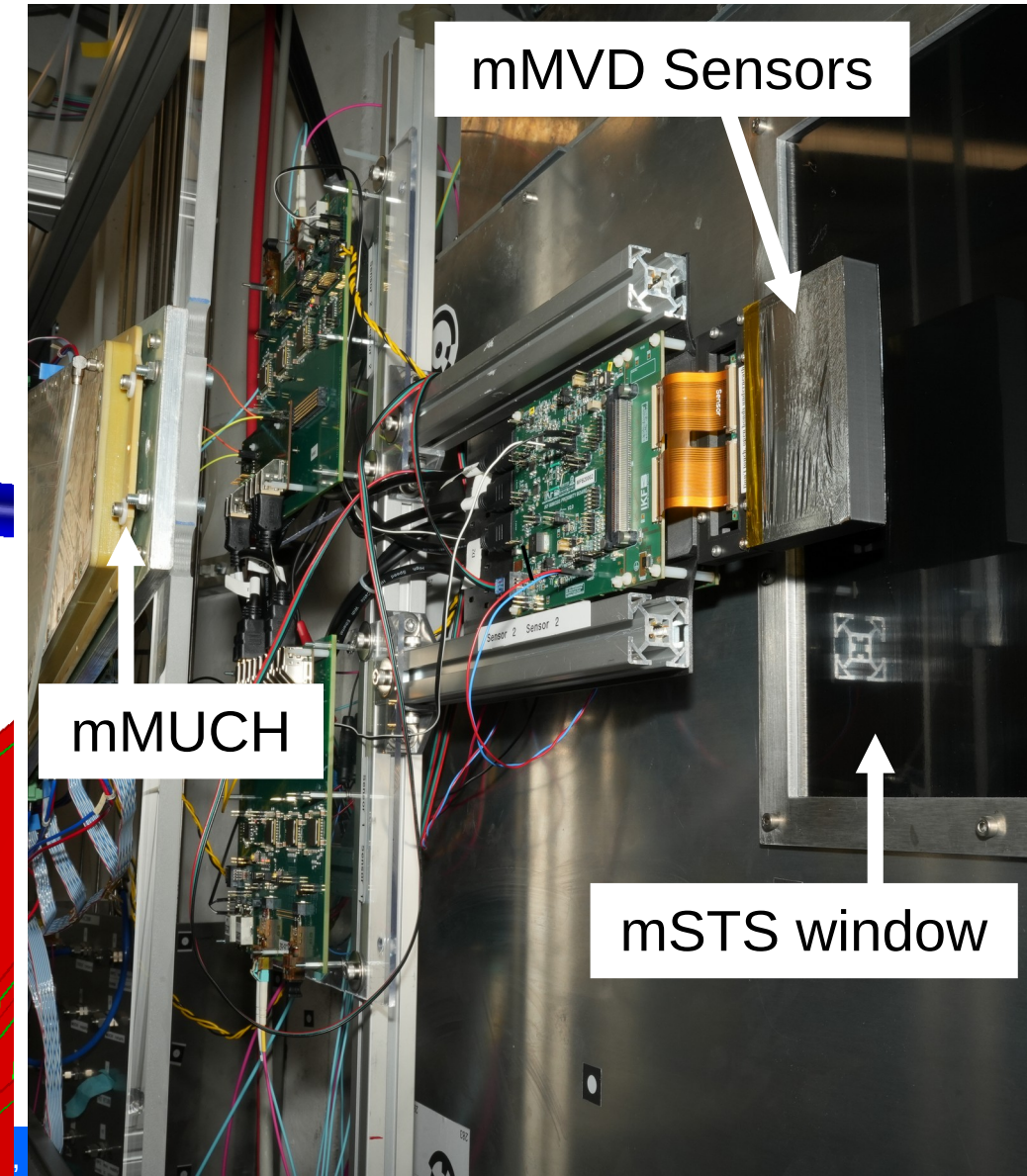
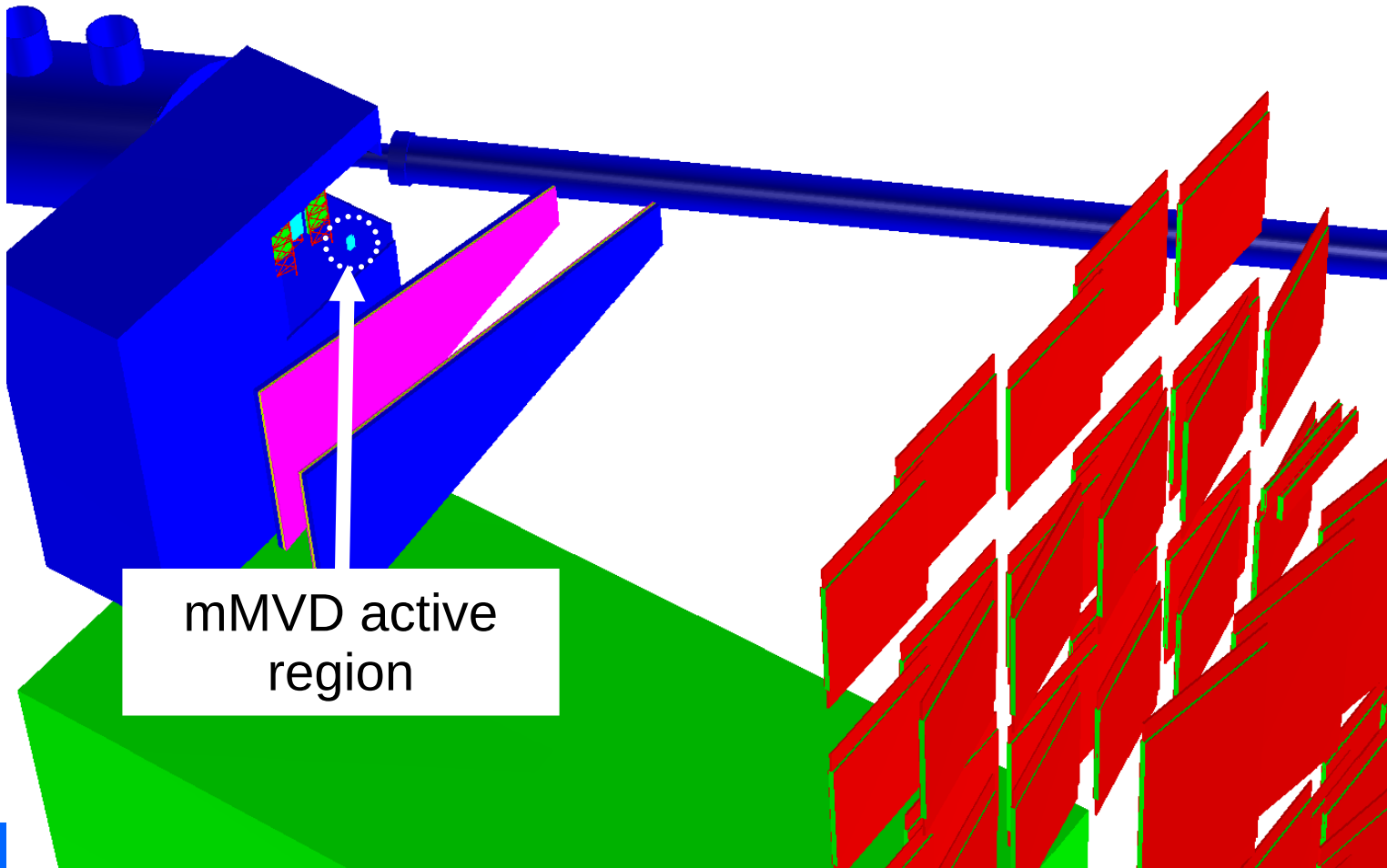
- 1 CRI to readout 2 identical setups
- Each setup: 1 MIMOSIS, 1 PROXY board, 1 CROB, 1 Adapter-board
- 2 Sensors back-to-back
→ measure correlation, redundancy
- All boards are electrically isolated from the Flex-link structure, and therefore isolated from the rest of mCBM.

mMVD
Adapter Board

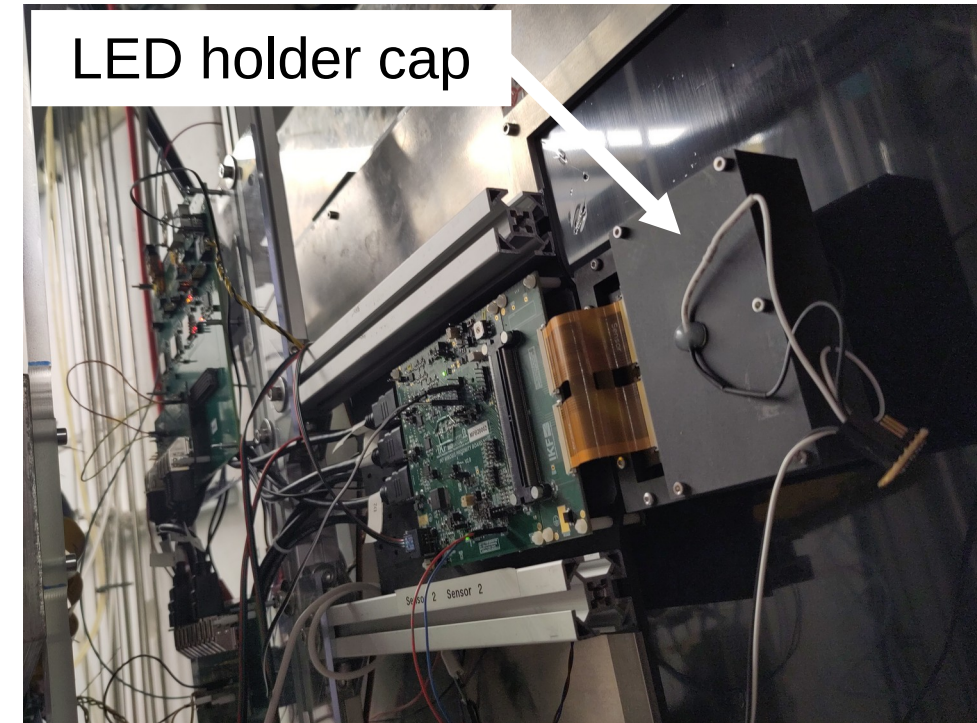
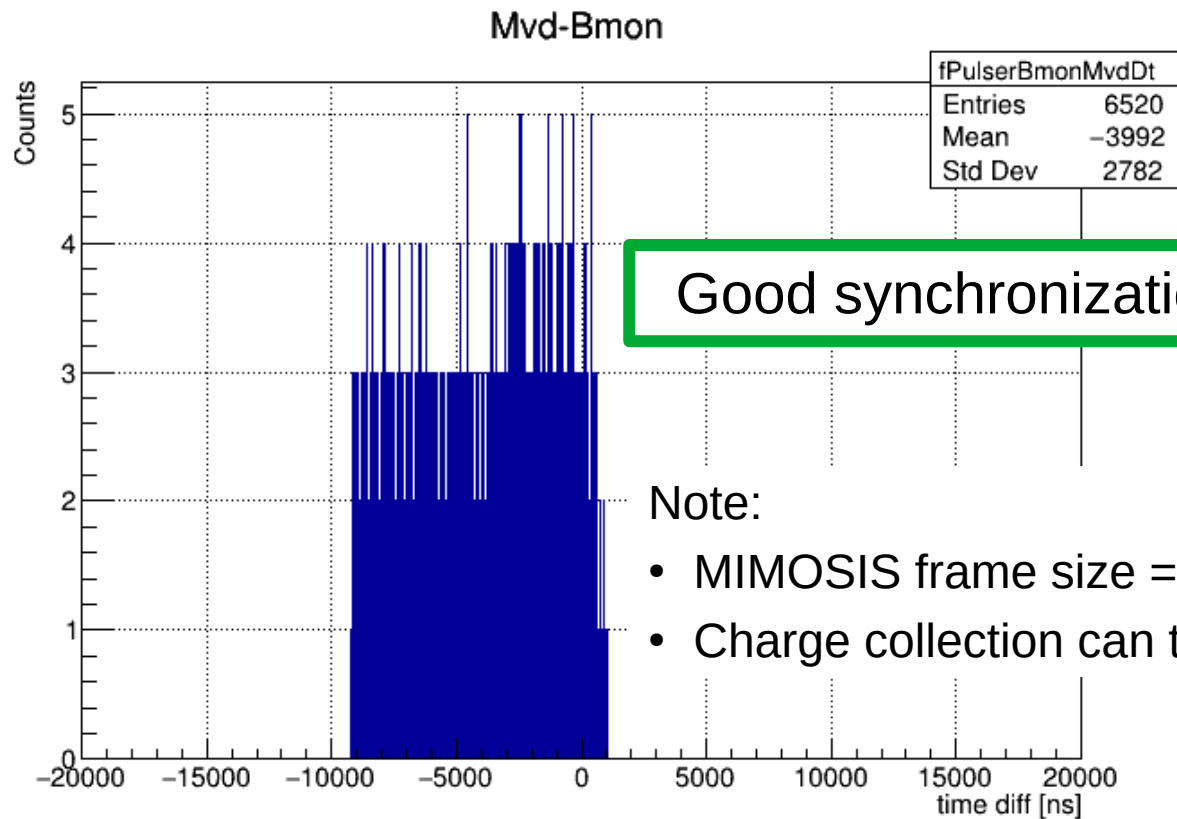


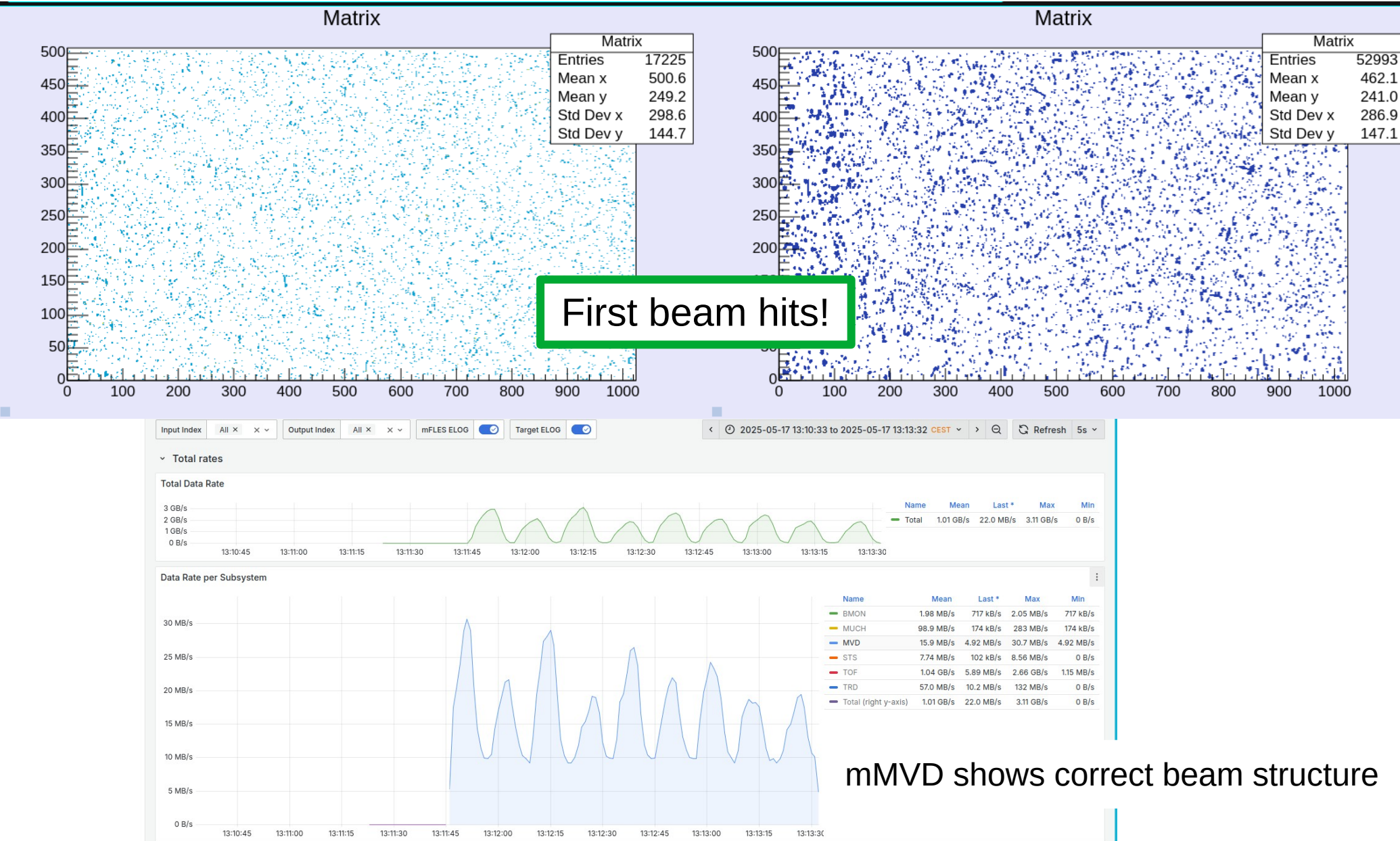
Cave positioning:

- mMVD Sensors between mSTS and mMUCH
- Positioned behind a mSTS ladder
- Allows for mMVD-mSTS correlation studies



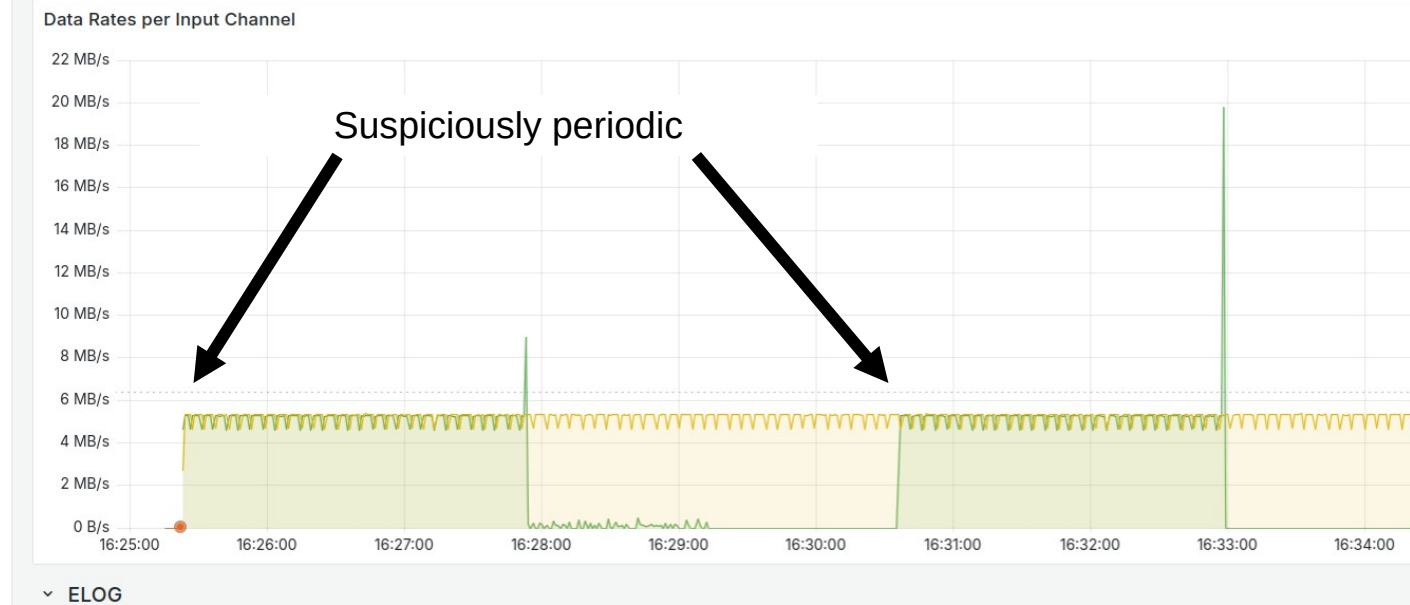
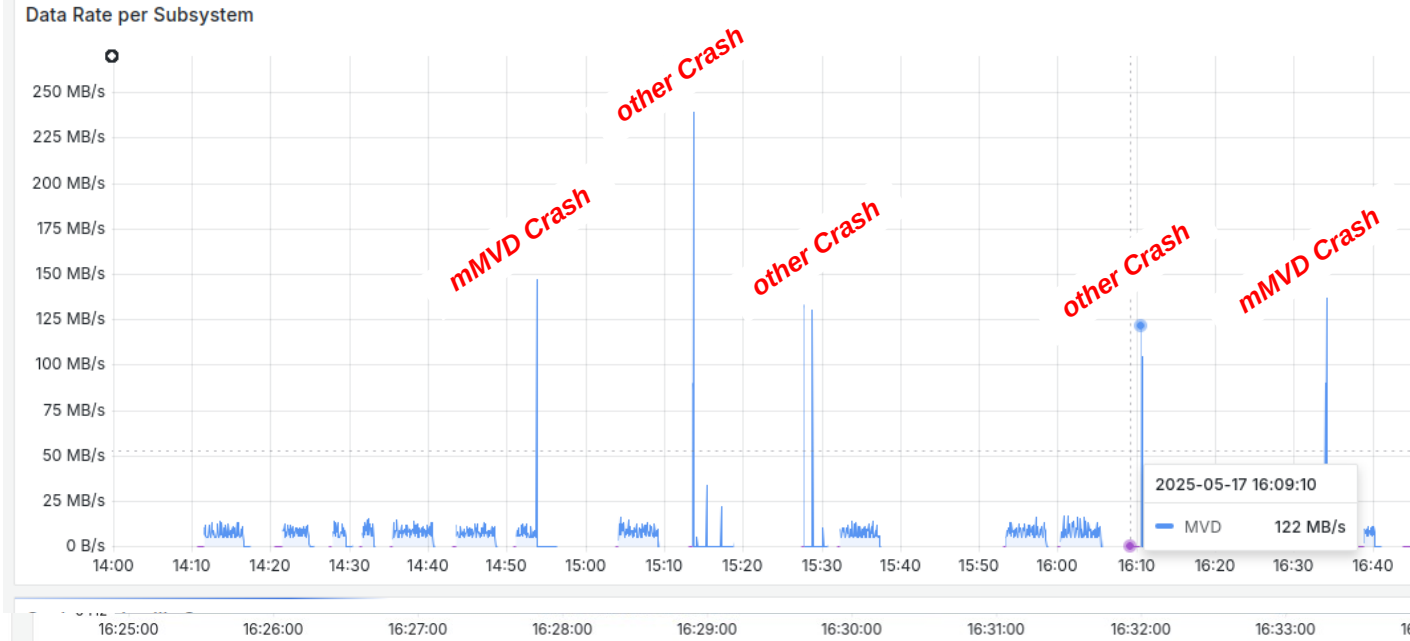
- Sensor-to-sensor synchronization already tested in the lab
- mMVD-to-mCBM synchronization tested in cave
- Pulses converted to light via LEDs in front of both sensors
- Compare pulses of one sensor with BMON

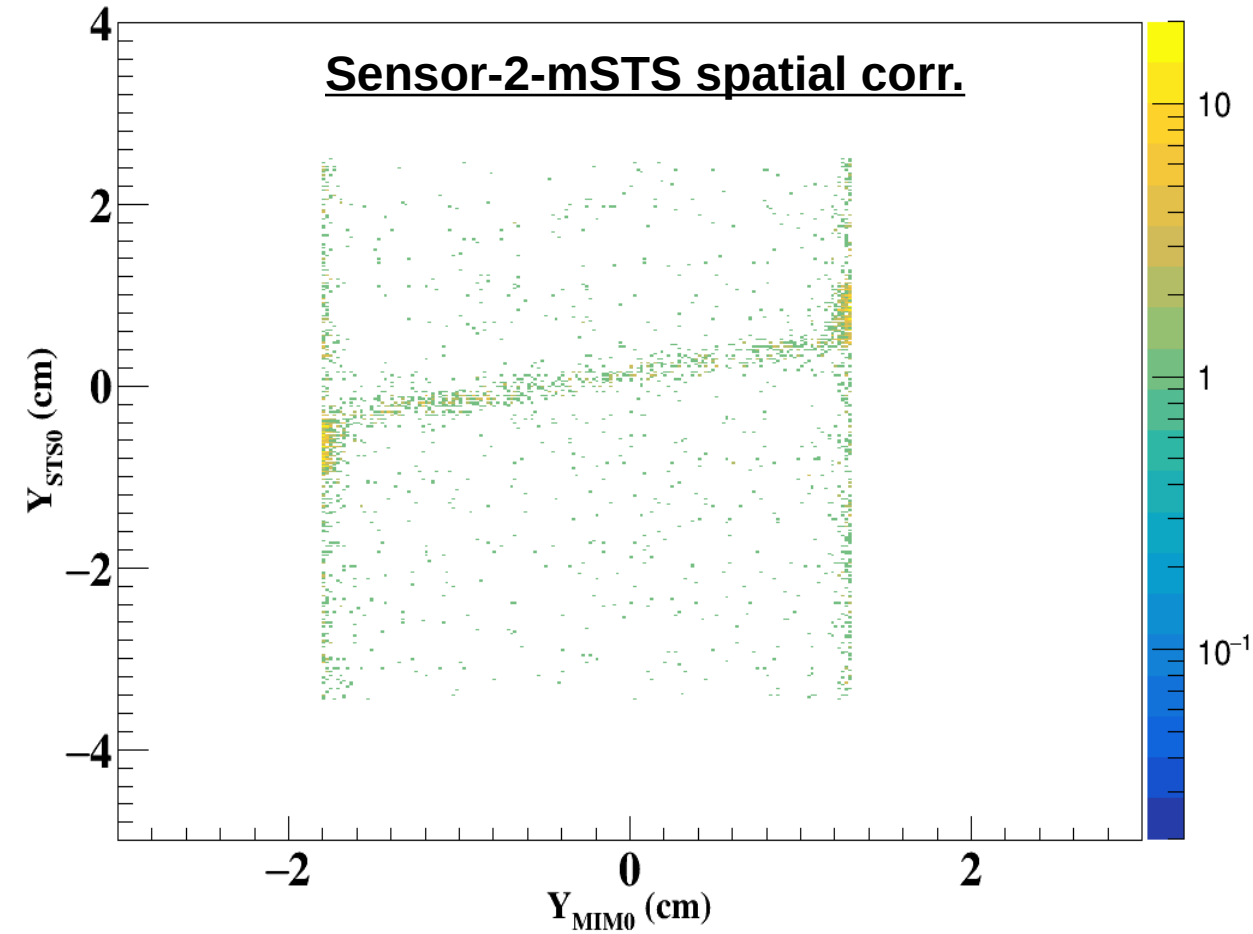
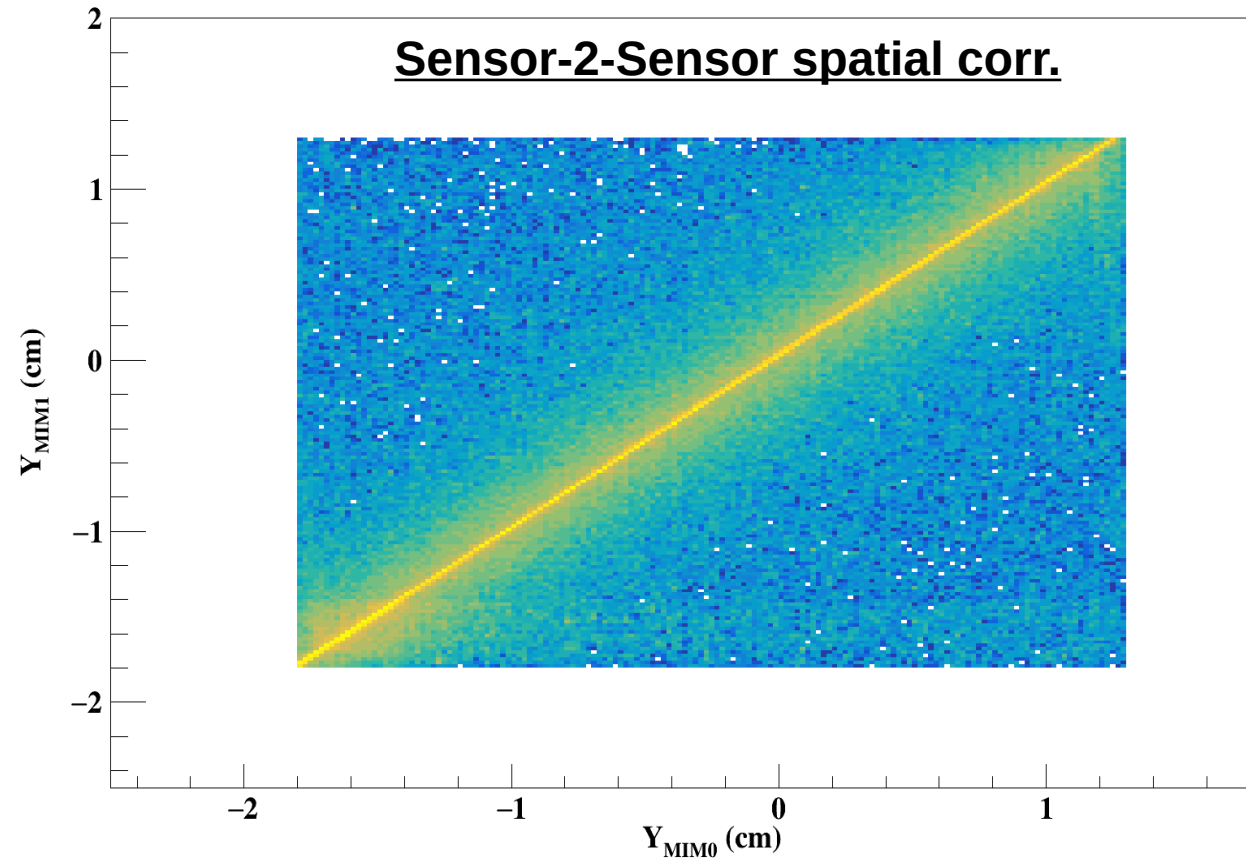




mMVD shows correct beam structure

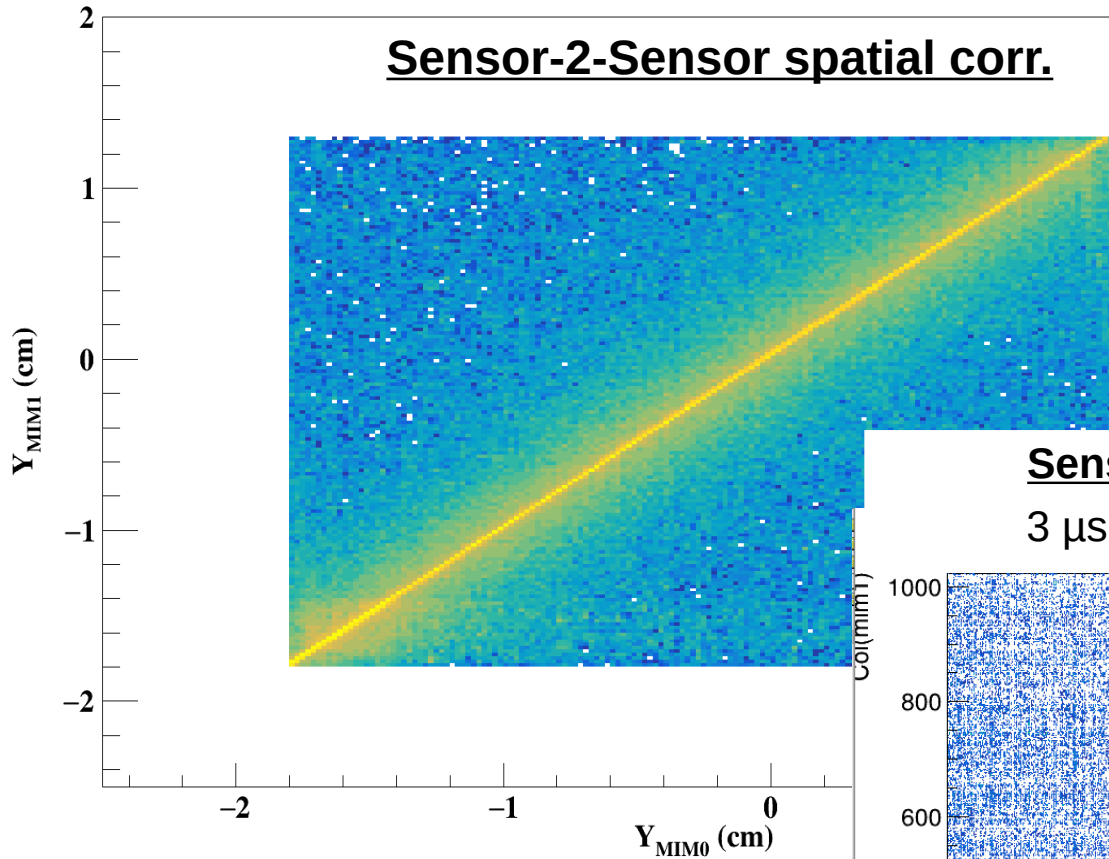
- Occasional crashes were observed on Sensor #1
- Already visible during the pulsing runs
- Most likely an erroneous sensor
- Not a show stopper, most of the time the sensor worked
- Exact problem not understood yet, my guess: bit flip in the frame counter
- A future firmware could handle such a case better



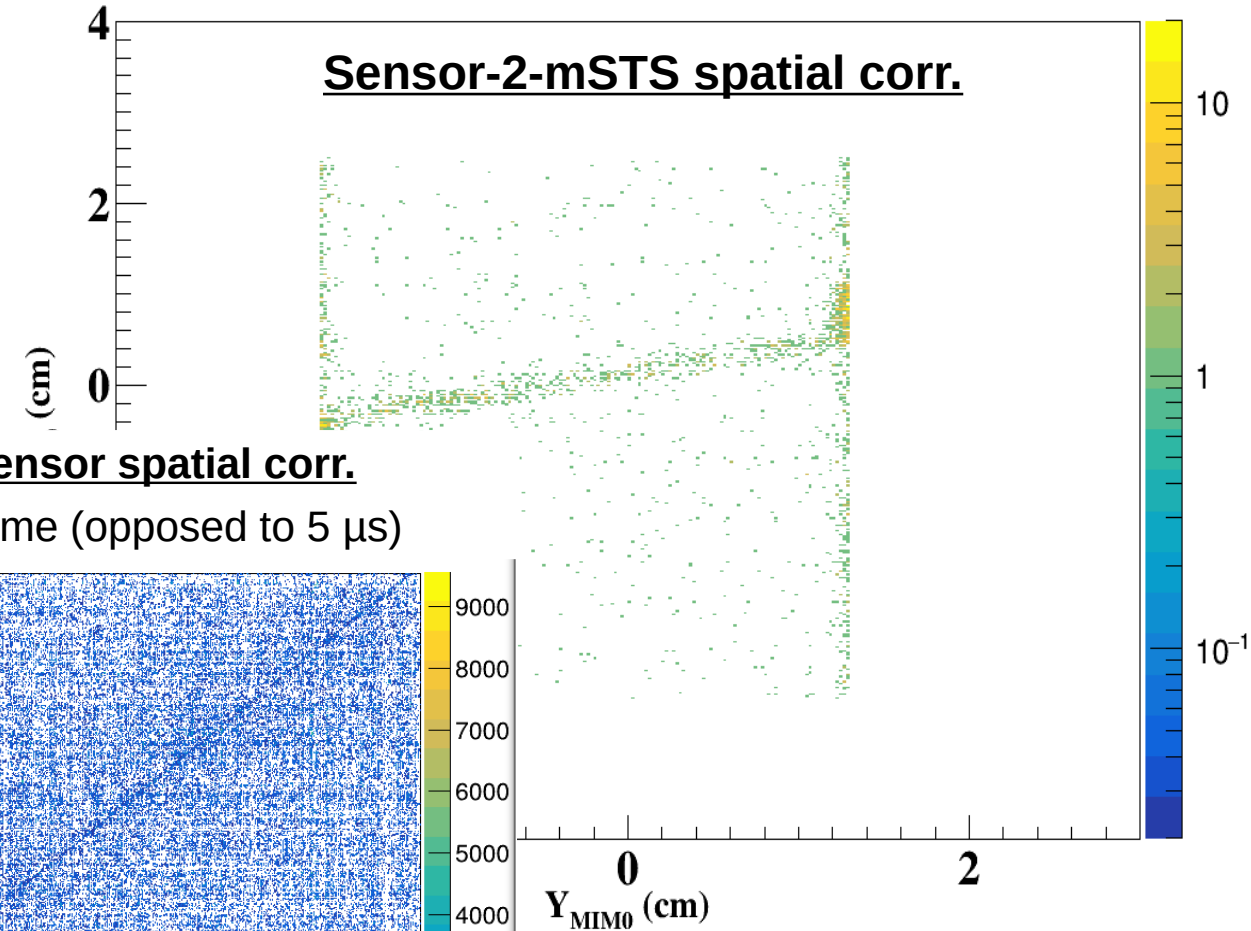


* Readout & Hitfinder params
for mSTS from Feb '25 run
used

Sensor-2-Sensor spatial corr.

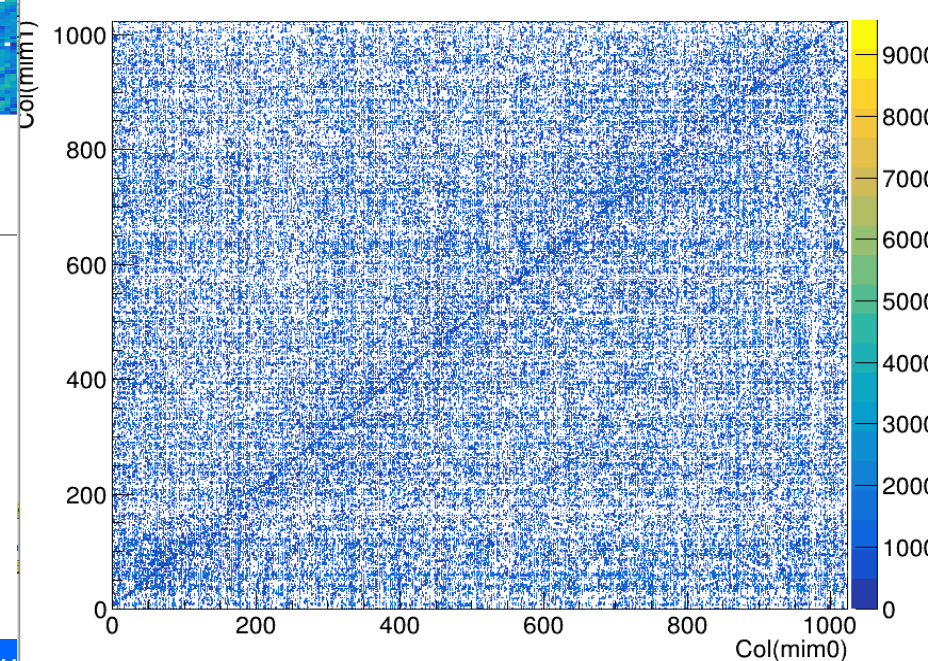


Sensor-2-mSTS spatial corr.



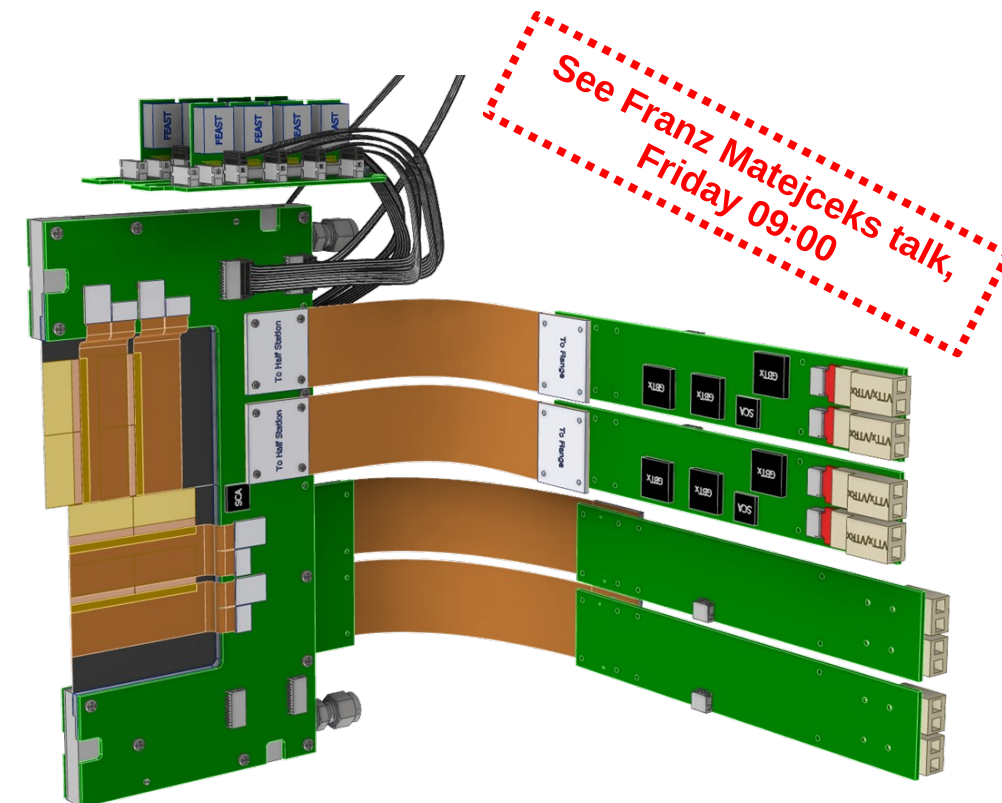
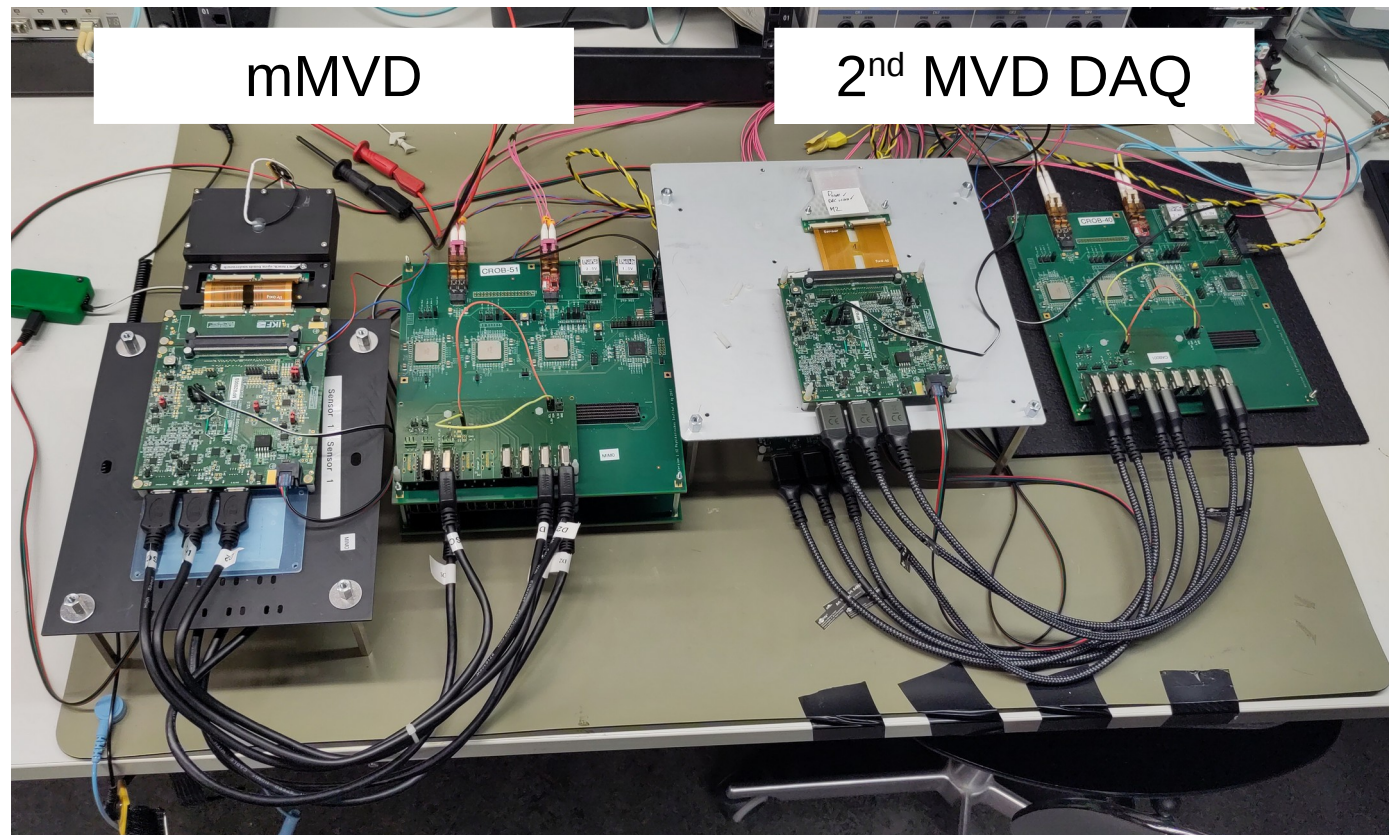
Sensor-2-Sensor spatial corr.

3 μ s frame time (opposed to 5 μ s)



* Readout & Hitfinder params
for mSTS from Feb '25 run
used

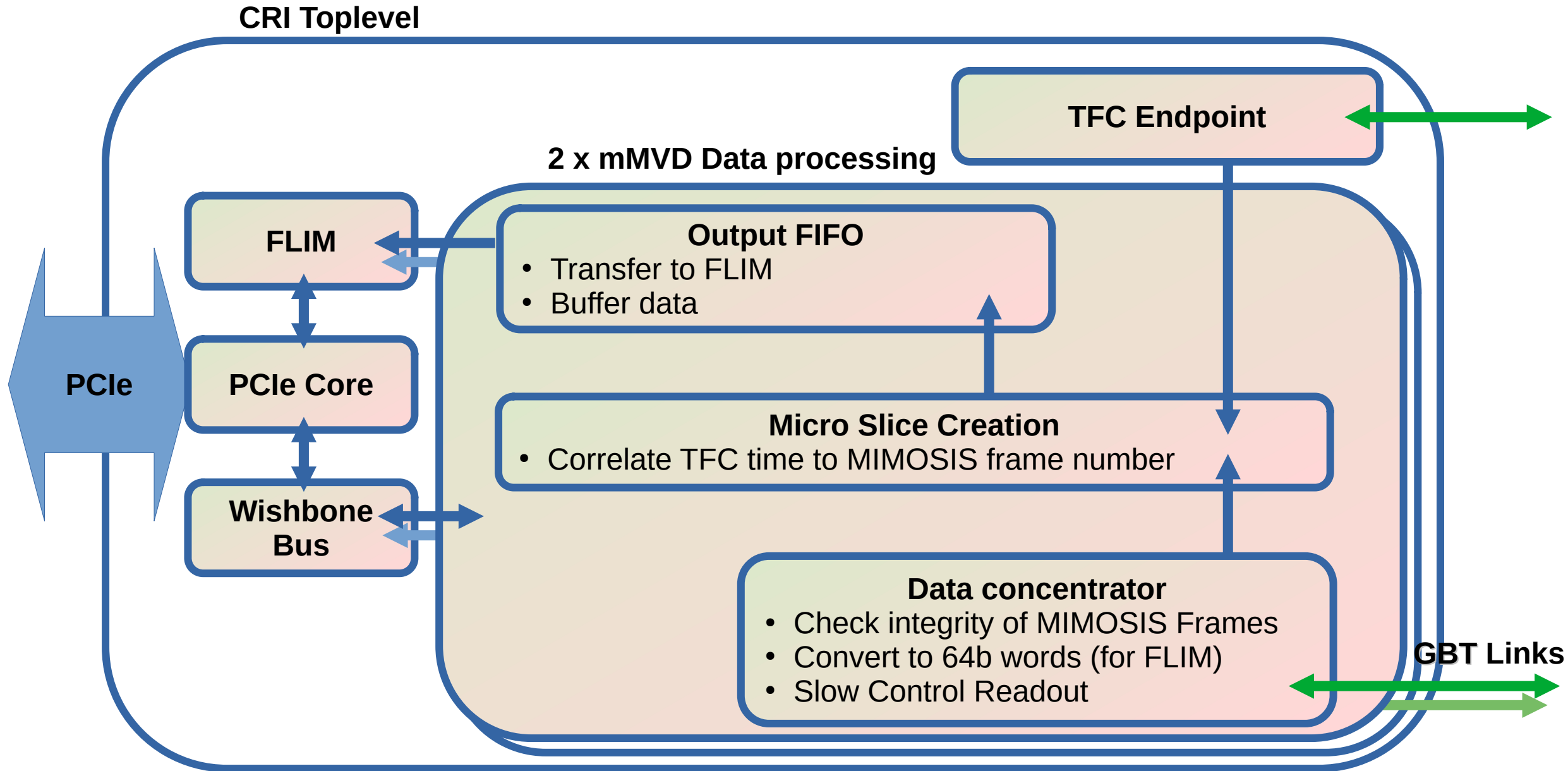
- mMVD taken back to the lab where it will be useful for other tests
 - but is ready to be brought back to mCBM at any time if necessary
- 2nd setup to continue firmware development:
 - Multiple sensors readout by one CROB
 - Inner μ Slice-Format
 - New way to synchronize sensors necessary
- Front-end board for (real) MVD under development



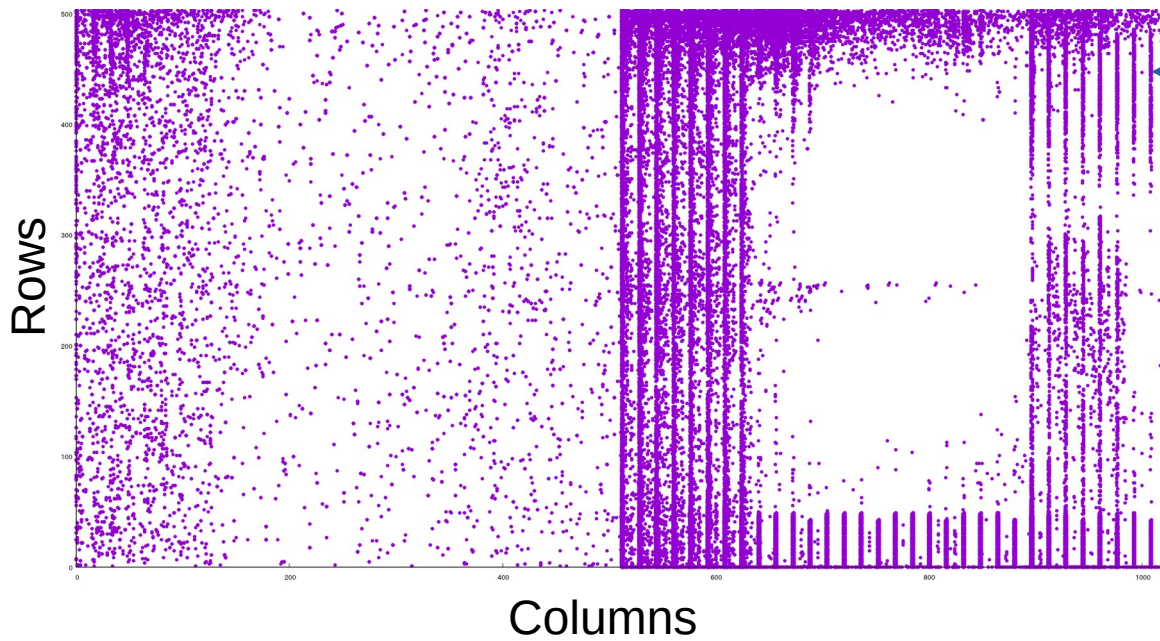
See Franz Matejceks talk,
Friday 09:00

- mMVD successfully integrated in mCBM
- Synchronization with other detectors
- Spatial correlation with mSTS validates mMVD
- Validated mMVD Read-out, compatible to CBM (first time demonstrated)
- Reduction $5 \rightarrow 3 \mu\text{s}$ frame time tested, seems to work
- Important lessons for ongoing development learned
- Firmware and Hardware developments is in the next phase

**Thank you for
your attention!**
Questions?



Sensor Matrix
MIMOSIS1



MVD R/O
tools

μ -Slice R/O,
time slice building

Flesnet

MVD Slow
Control
tools

DCA

R/W FPGA
registers via
Wishbone

PCle

FLIM

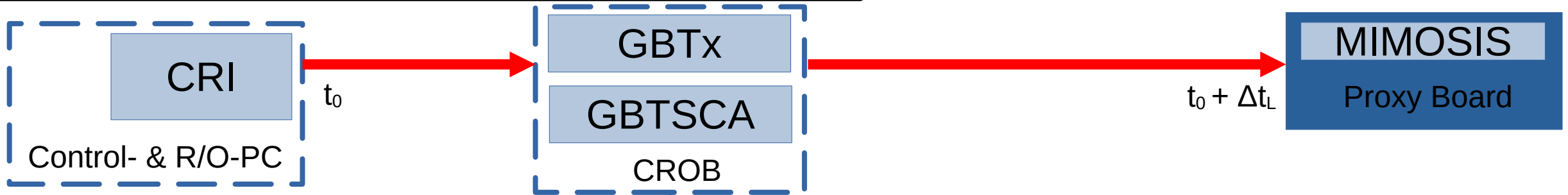
PCle Core

Wishbone
Bus

- Start DAQ
- Reset sensors and state machines
- Some Statistics during operation (Frame counter, frame length, valid checksum counter, buffer fill level, ...)

Background knowledge:

- Sensor starts pixel readout upon a start signal, *frame start*
- *frame end* after $5\ \mu\text{s}$
- Frame gets a *frame number* and is outputted
- When you know the start time you know the hit time



- Send Start signal to sensor at TFC time t_0
- Sensor starts sending frames at $t_s = t_0 + \Delta t_L$, $\Delta t_L = \text{Latency}$
- Frames get packed into μ -Slices together with the start time

Example with μ -Slice size $1\ \mu\text{s}$:

$\mu\text{S-Ind.} = t_i$	$\mu\text{S-Ind.} = t_{i+1}$	$\mu\text{S-Ind.} = t_{i+2}$	$\mu\text{S-Ind.} = t_{i+3}$	$\mu\text{S-Ind.} = t_{i+4}$	$\mu\text{S-Ind.} = t_{i+5}$
Start time t_s	Empty	Empty	Empty	Empty	Start time t_s
Sensor Data Frame # n					Sensor Data Frame # $n+1$

- CRI-MVD repository:

<https://git.cbm.gsi.de/daq/fpga-firmware/cri/mvd-firmware/cri-mvd>

→ Custom CRI MVD Fork

- MIMOSIS / MVD specific python tools:

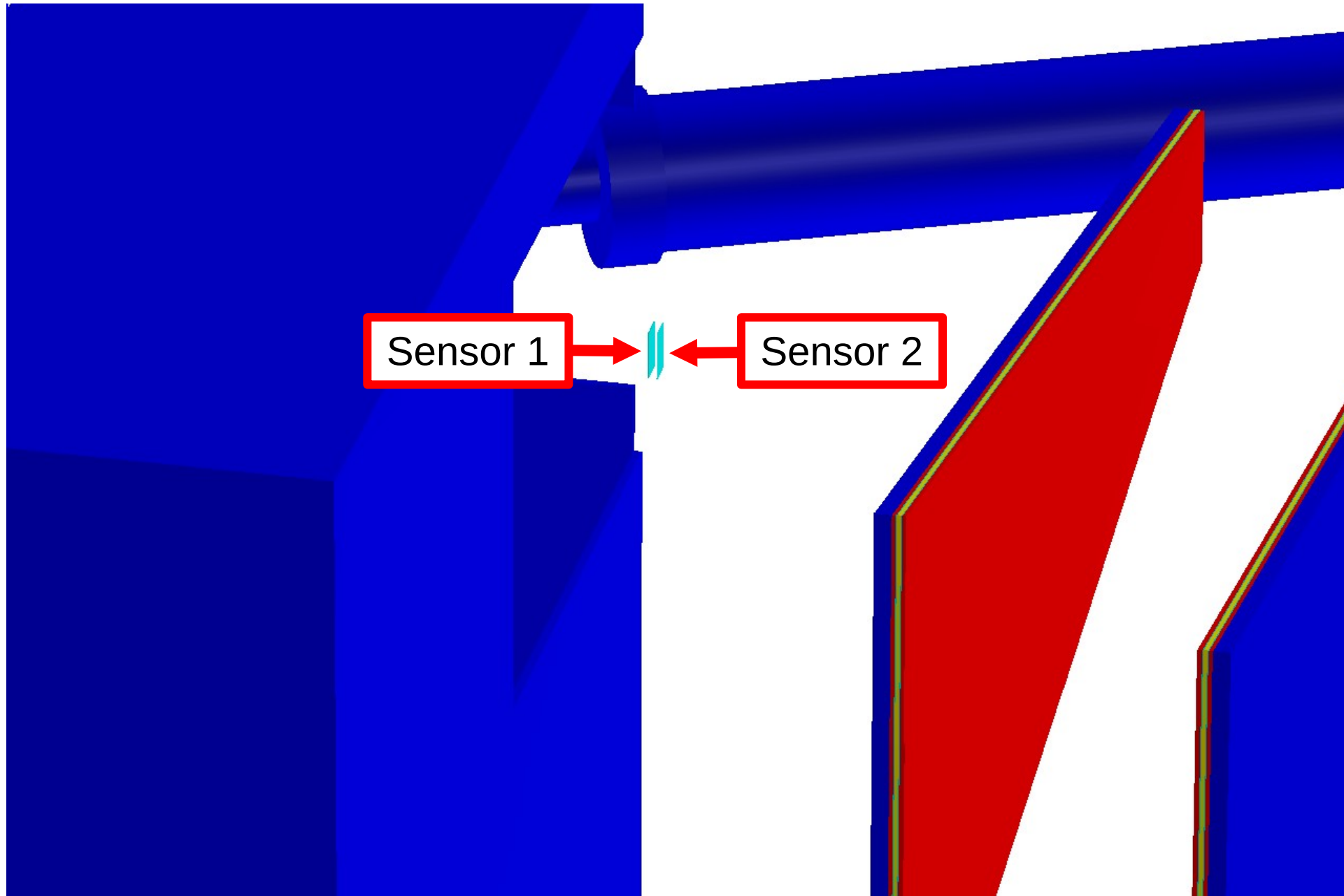
<https://git.cbm.gsi.de/daq/fpga-firmware/cri/mvd-firmware/cri-mvd-tools>

→ based on python-cri

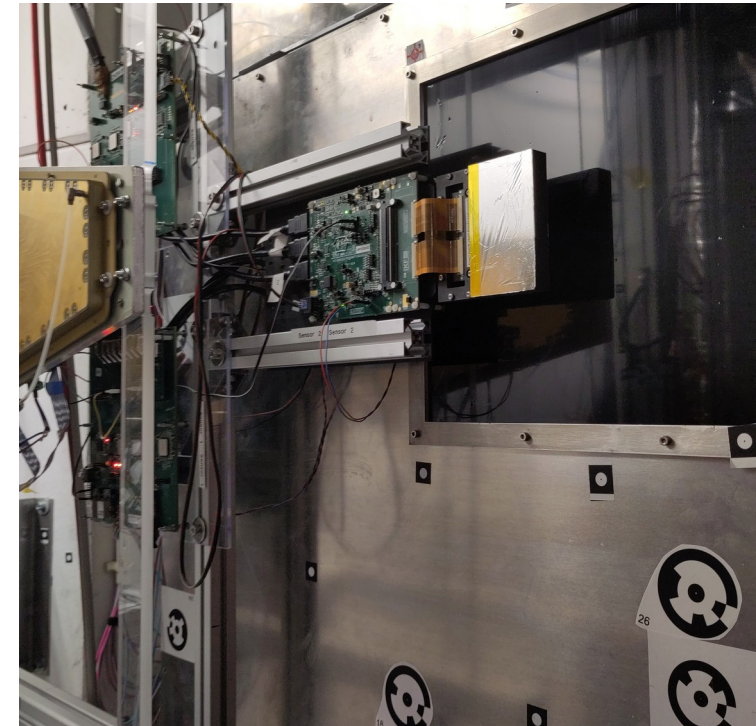
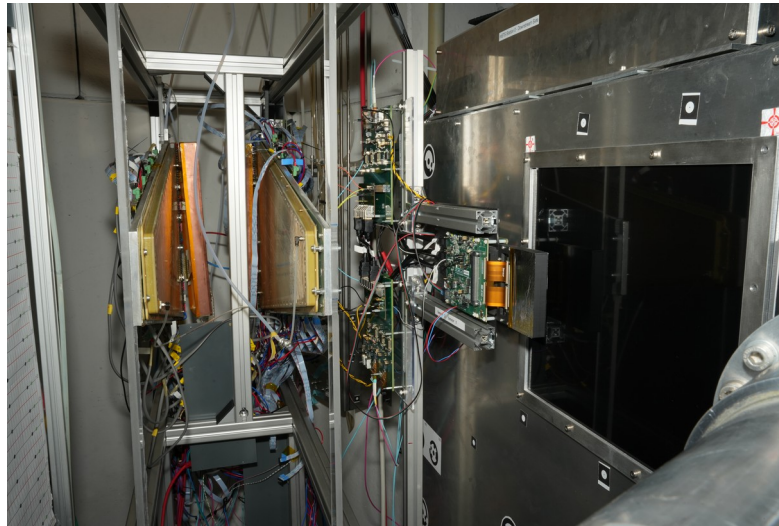
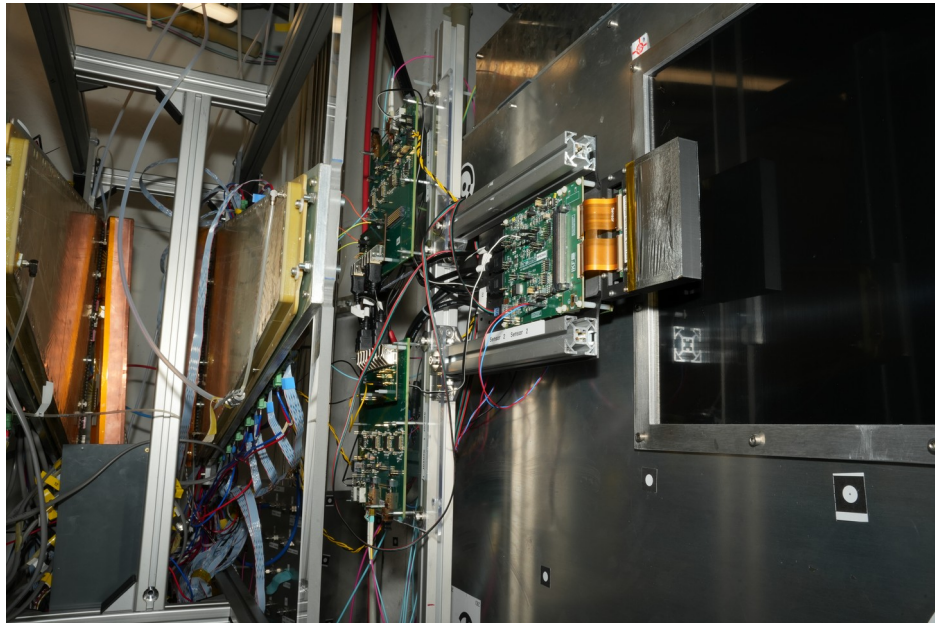
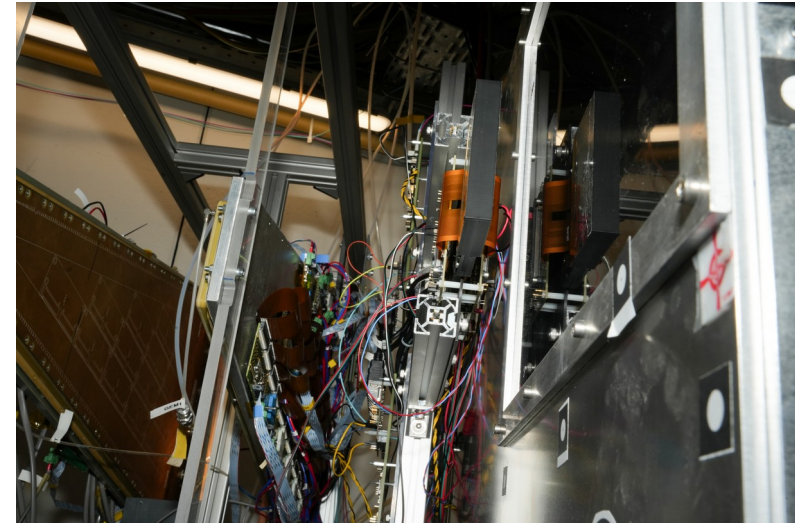
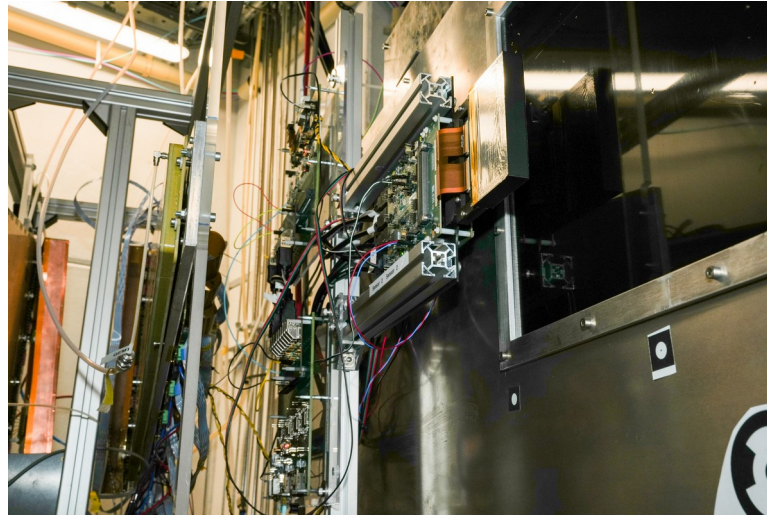
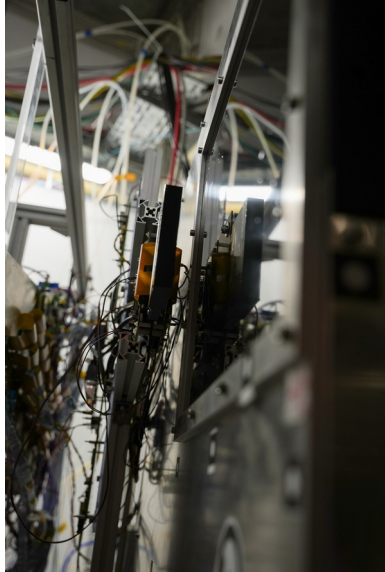
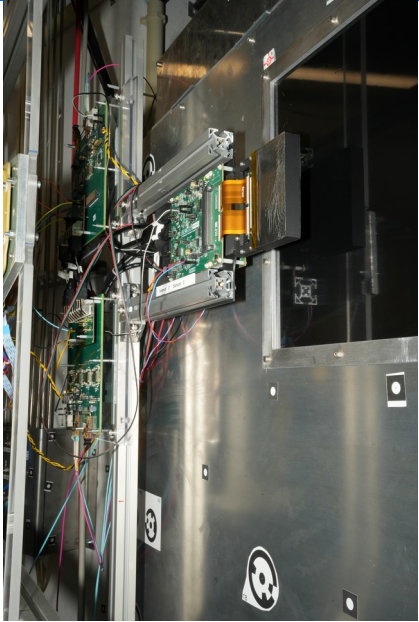
→ Startup procedure, make sensor running

→ R/O data FIFO via slow control

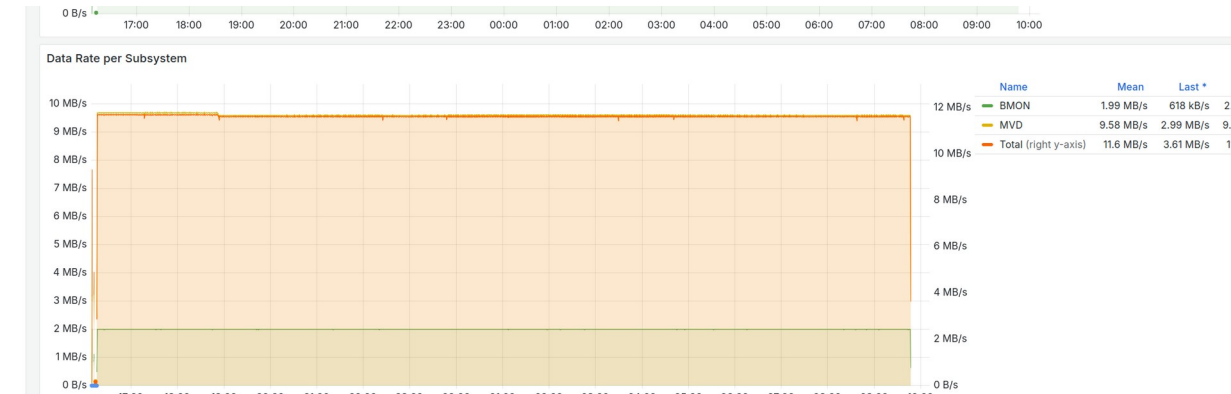
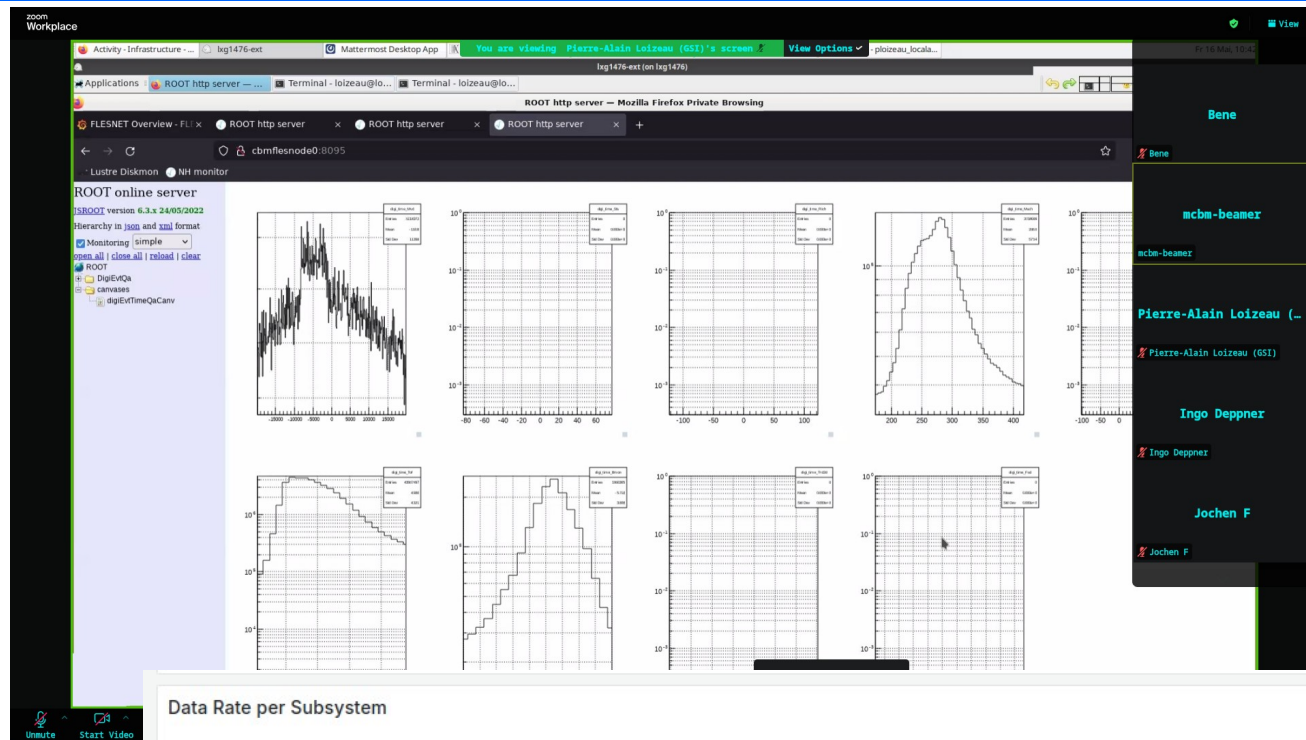
→ Offline data analysis



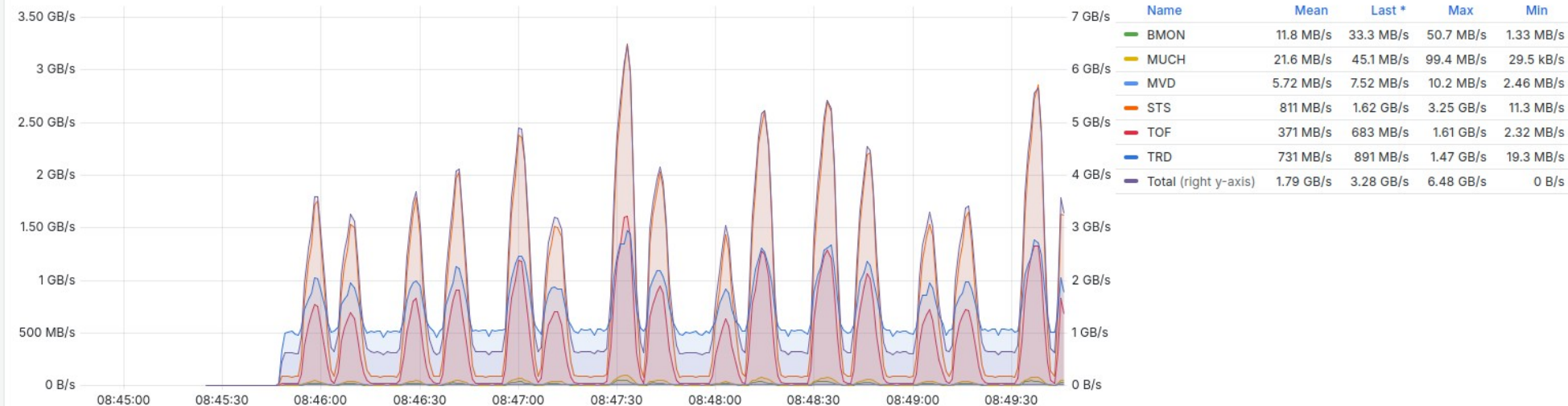
BACKUP – More Pictures mMVD



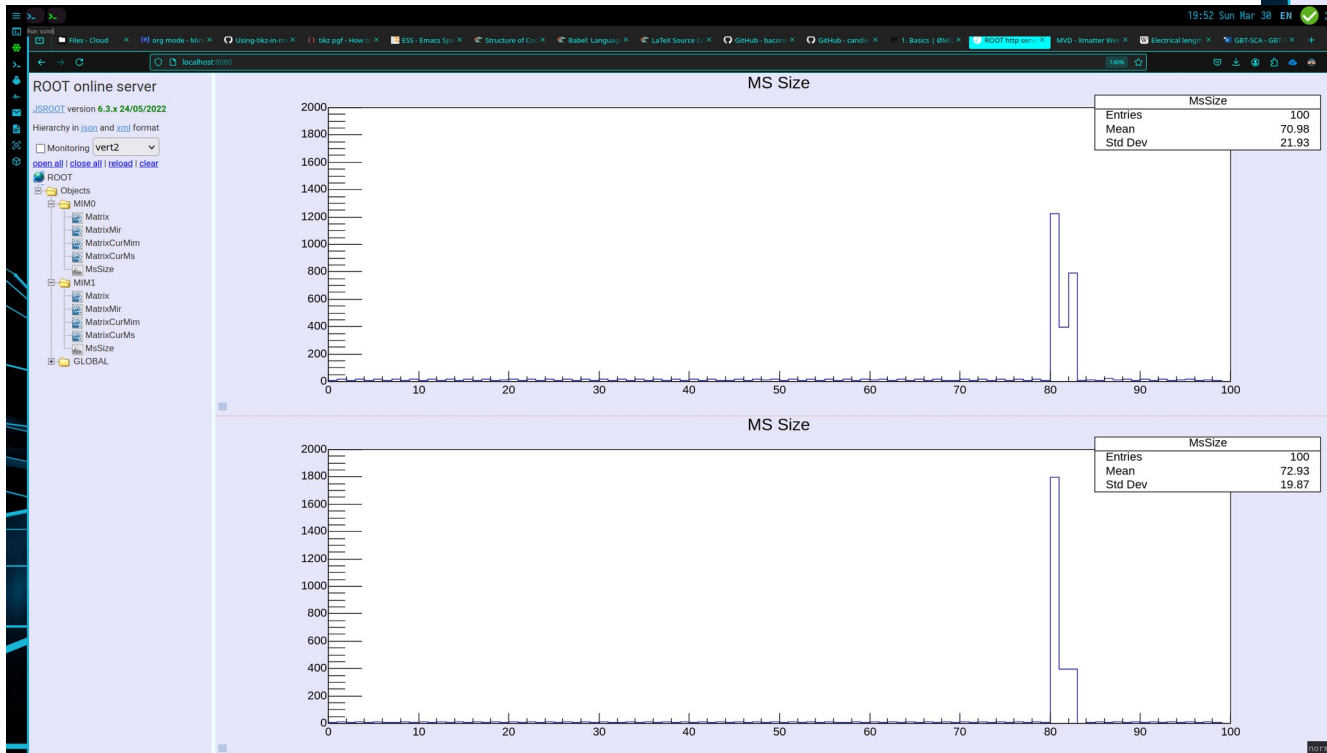
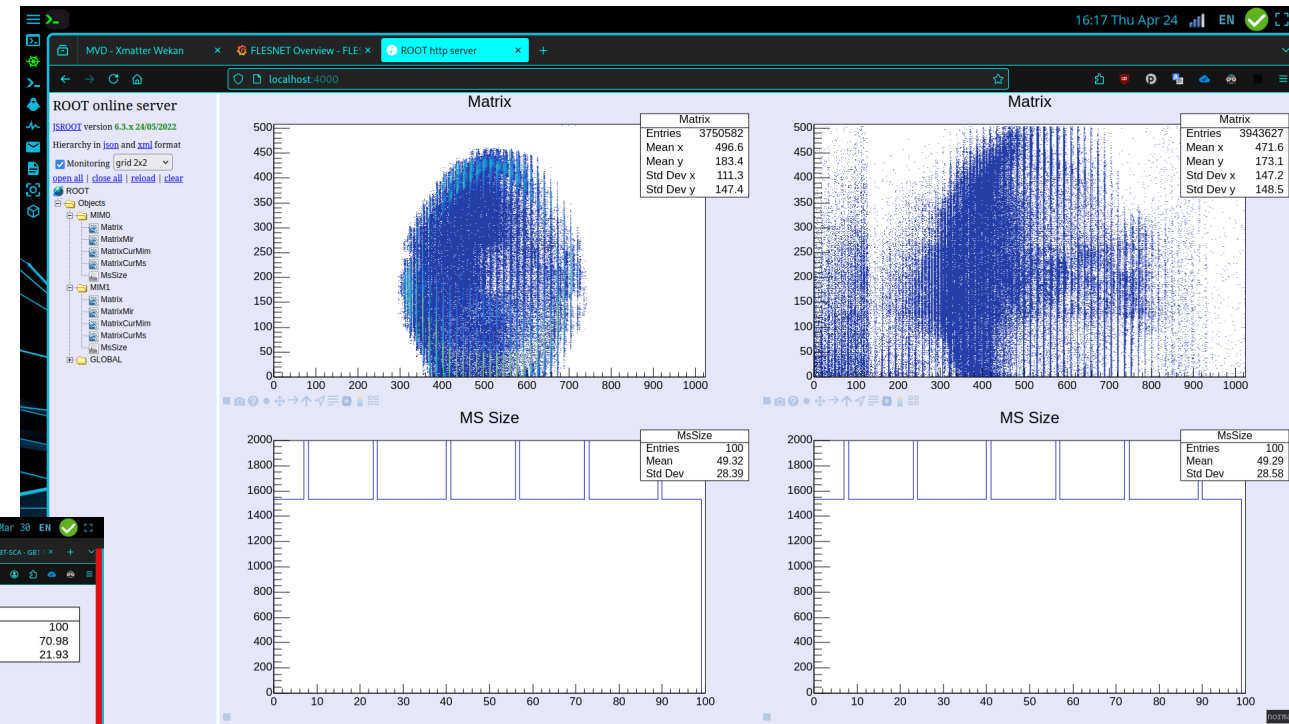
BACKUP – More Pictures Beam time



Data Rate per Subsystem



BACKUP – More Pictures Sync



Lab Setup:

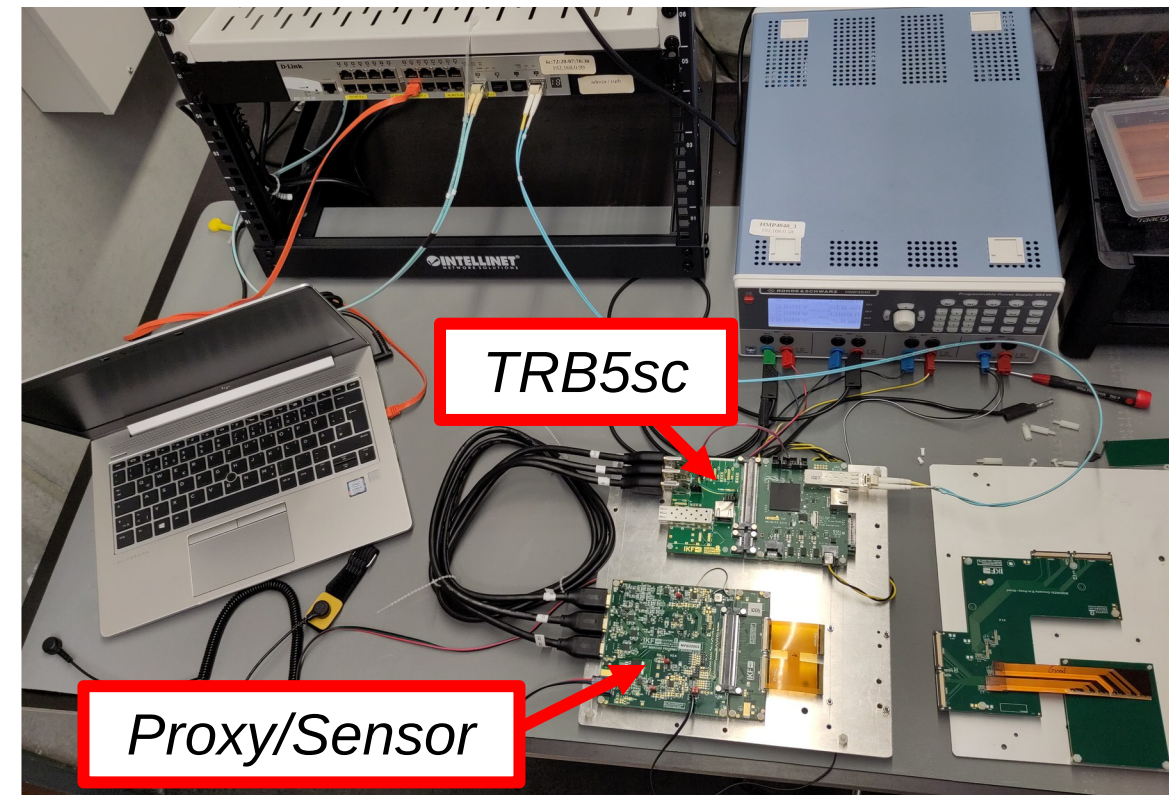
- TRBnet based sensor R/O for sensor test experiments
- Recent update reduces necessary hardware to one FPGA board



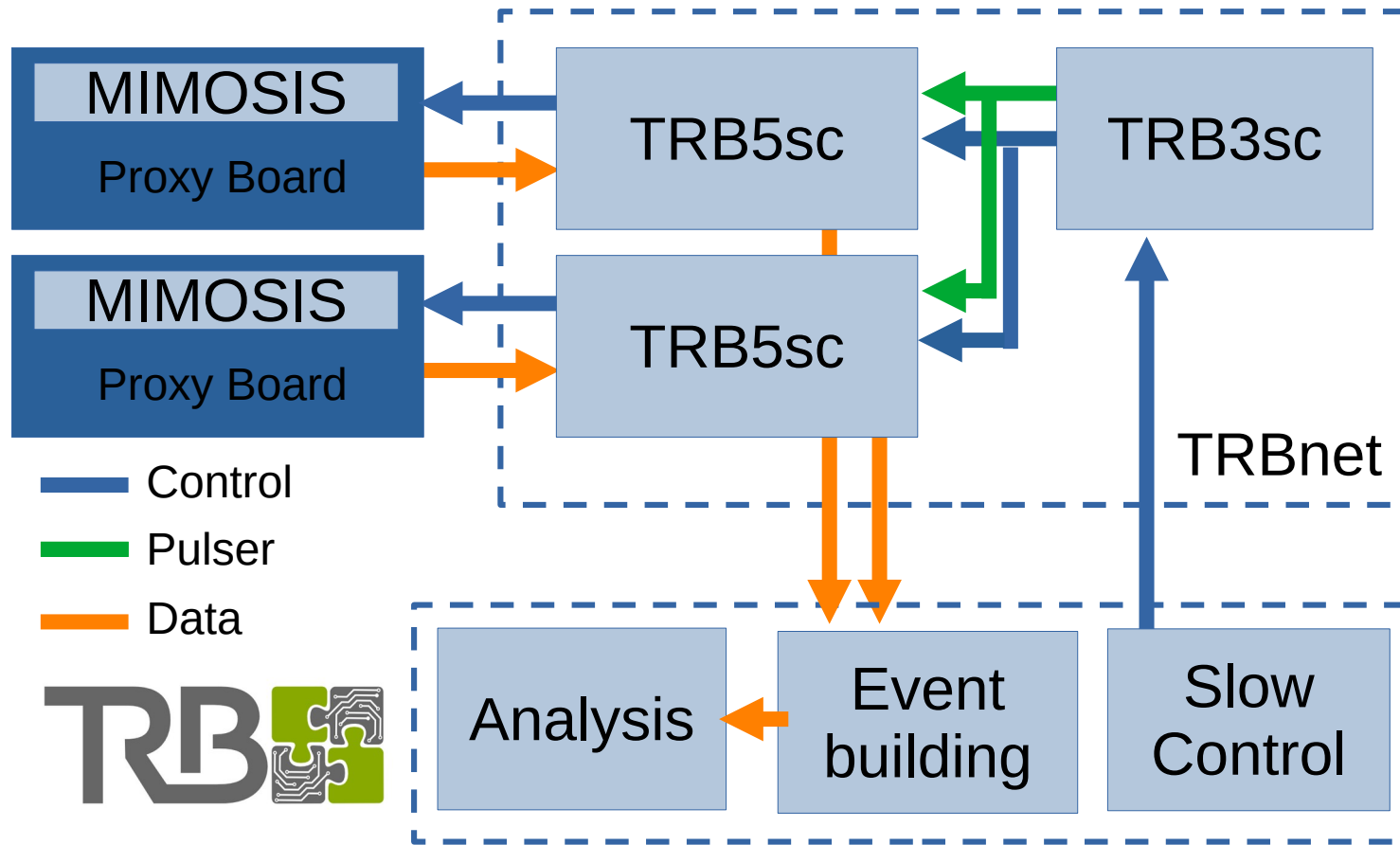
— Control
— Data

- Easy to setup, very portable
- Cost efficient
- R/O of single sensors for test beams and probe station

Finalized single-sensor R/O setup ready for mass-testing and MIMOSIS-QA



BACKUP – R/O Setup for Lab Testing



Synchronized setup used for double sensor R/O

