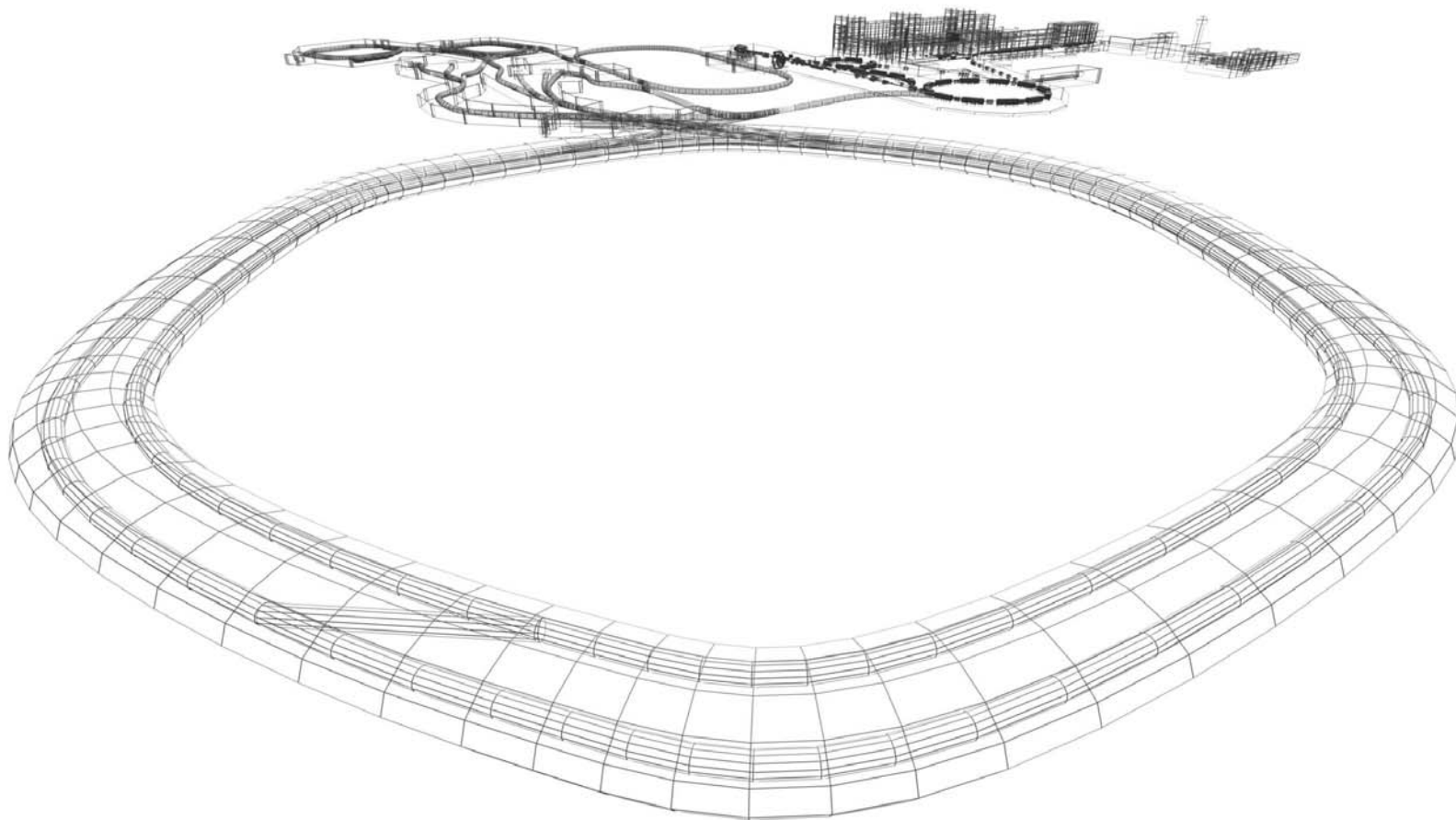


AGAVA Readout



Some Experience

- AGAVA Front Panel input caption!? -> Manual
- AGAVA not ready after power up (with VME sys reset)

Two TEST Setups

AGAVA Standalone: Only validations, no rejection(s)

AGAVA within a small GTS Tree: No trigger processor available
Only rejections, no validation(s)

AGAVA data ready check

```
while (1) // attention, could be infinite !! to be changed.
{
    // first check, that data is ready on the agava
    l_dat = *pl_ag_stat;
    //printm ("AGAVA %d status: 0x%x \n", l_i, l_dat[l_i]); sleep (1);
    if ( (l_dat & 0x200000) == 0x200000) {break;}
}
if ( (l_dat[l_i] & 0x4) == 0x4) // valid trigger
{
    //printm ("accepted trigger AGAVA %d \n", l_i);
}
if ( (l_dat[l_i] & 0x8) == 0x8) // rejected trigger
{
    //printm ("rejected trigger AGAVA %d \n", l_i);
    //sleep (1);
}
```

AGAVA Readout

```
pl_tmp = pl_ag_stat;
*pl_dat++ = *(pl_tmp+ 4); // local tag 24-47
*pl_dat++ = *(pl_tmp+ 5); // local tag 0-23

*pl_dat++ = *(pl_tmp+ 8); // event number

*pl_dat++ = *(pl_tmp+ 0); // status
*pl_dat++ = *(pl_tmp+ 1); // local trigger counter
*pl_dat++ = *(pl_tmp+ 2); // validation trigger counter
*pl_dat++ = *(pl_tmp+ 3); // rejection trigger counter

*pl_dat++ = *(pl_tmp+ 6); // validation tag 24-47
*pl_dat++ = *(pl_tmp+ 7); // validation tag 0-23

*pl_dat++ = *(pl_tmp+ 9); // rejection tag 24-47
*pl_dat++ = *(pl_tmp+10); // rejection tag 0-23
*pl_dat++ = *(pl_tmp+11); // LLP status 0-7
*pl_dat++ = *(pl_tmp+12); // GTS status 0-7
*pl_dat++ = *(pl_tmp+13); // MSG in 0-7

*pl_dat++ = *(pl_tmp+18); // trigger input counter
*pl_dat++ = *(pl_tmp+19); // timeout counter
*pl_dat++ = *(pl_tmp+20); // fast clear delay

// check that busy and data ready is off after reading the registers
l_dat = *pl_ag_stat;
//printm ("AGAVA %d status: 0x%x \n", l_i, l_dat);
if ( l_dat != 0x80)
{
    printm ("ERROR>> AGAVA not ready for next trigger \n");
}
```