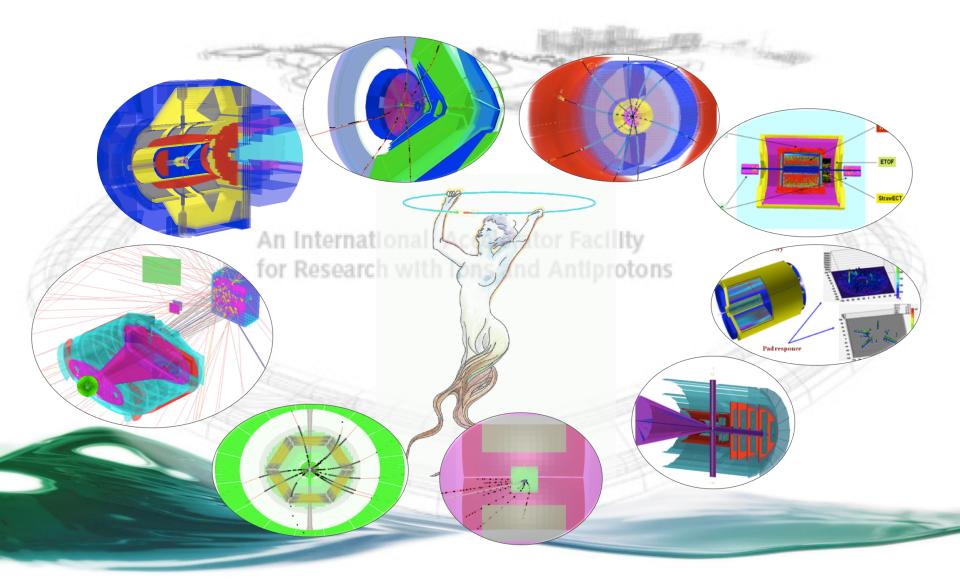
FairRoot Status Report



FairRoot Developers:

International Accelerator Facili

Core Team:

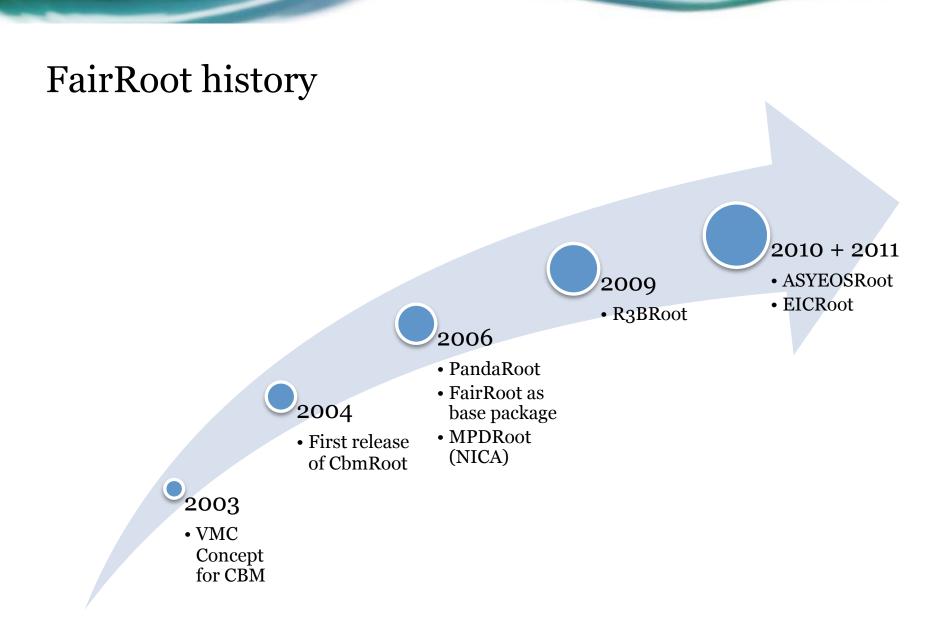
Mohammad Al-Turany	GSI-IT	2003
Denis Bertini	GSI-IT	2003
Florian Uhlig	CBM/IT	2006
Radek Karabowicz	PANDA/IT	2008
Dmytro Kresan	R3B/IT	2011

People participated to major features:

Ilse König Volker Friese Tobias Stockmanns

HADES

CBM PANDA long list of people who have contributed pieces of code to FairRoot since the project started end of 2003



Experiments using FairRoot at FAIR-GSI

- □ Panda, CBM
 - Should be known
- □ ASYEOS
 - Measurement of the symmetry energy for asymmetric nuclear matter
 - Running experiment at GSI using the Aladin Magnet and the Land detector
- □ NUSTAR
 - Adopted by **R3B** and other nuclear physics experiments (ELISE, ASYEOS, EXL (Active Target), RIBRAS (Sao Paulo)...)
 - Adopted by the **NUSTAR SWG** as the main framework for simulation and analysis
 - Chosen as the main framework for the Joint Research Activities JRA05 of ENSAR (SINuRSE: Simulations for Nuclear and Structure in Europe)

Experiments using FairRoot outside FAIR-GSI

• MPD

- Multi Purpose Detector at the Nuclotron-based Ion Collider fAcillity
- □ study of hot and dense baryonic matter
- □ <u>http://nica.jinr.ru/</u>

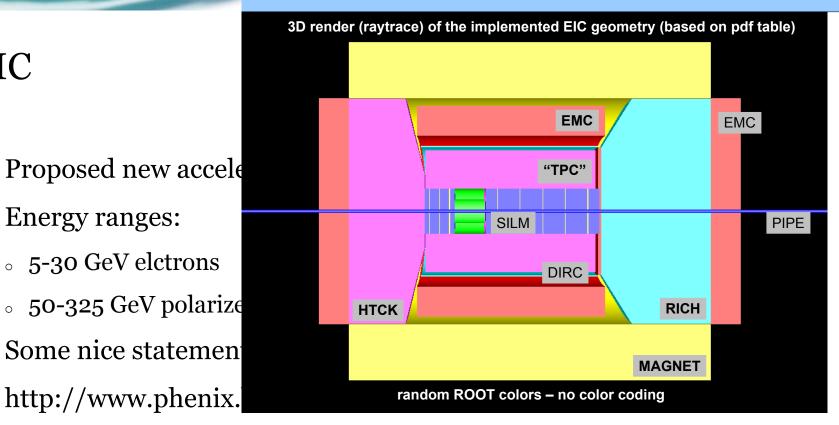
• EIC

Electron Ion Collider

EIC

- Proposed new accelerator at the Brookhaven National Lab
- Energy ranges:
 - 5-30 GeV elctrons
 - $_{\circ}$ 50-325 GeV polarized protons or up to 130 GeV/u gold ions
- Some nice statements taken from a talk given by Yulia Zulka http://www.phenix.bnl.gov/WWW/publish/elke/EIC/TF-Meetings/
- EICRoot opens path forward to detailed simulations and core concept verifications, so we can make stronger case for Letter of Intent / CDR, utilizing newest software designed for HEP, using single software package.
- Well-designed underlying framework (FairRoot) allows rapid progress with simulations. Some basic checks were described in this presentation. Large user base allows "borrowing" detector geometries from other experiments for testing, with minimal effort.

Implemented EIC Geometry Overview



- EICRoot opens path forward to detailed simulations and core concept verifications, so we can make stronger case for Letter of Intent / CDR, utilizing newest software designed for HEP, using single software package.
- Well-designed underlying framework (FairRoot) allows rapid progress with simulations. Some basic checks were described in this presentation. Large user base allows "borrowing" detector geometries from other experiments for testing, with minimal effort.

EIC

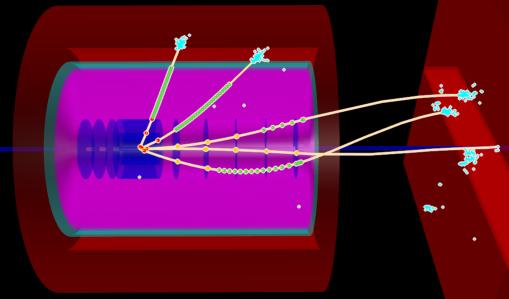
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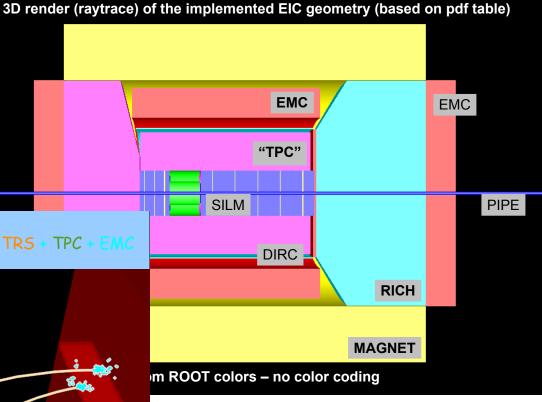
Implemented EIC Geometry Overview

EIC

- Proposed new accele
- Energy ranges:

MC Hits display: five e^{-} generated hits in SIT + TRS + TPC + EMC





Ilations and core concept for Letter of Intent / CDR, Ising single software package.

ot) allows rapid progress with bed in this presentation. Large etries from other experiments

 FairBoxGenerator: 5 electrons with p = 1 GeV, fired at random eta / phi for a hit test
 13

 12.12.11
 Status FairRoot

New and ongoing developments:

- Proof Integration in FairRoot (See talk by Radek)
- MBS API is now in FairRoot:
 - Reading lmd files (ready)
 - Communicating with the DAQ directly (API is available)
- Event time:
 - During Simulation (old)
 - After Simulation (new)
- Signal-Background mixing (event or time based)
- Time dependent simulation (See talk by Tobias)
- New concept for Database connectivity
- GPU usage
- Webtools

PROOF in FairRoot

• PROOF - Parallel ROOt Facility:

- Extension of ROOT allowing transparent analysis of large sets of ROOT files in parallel on remote computer clusters or multi-core computers (ROOT Users Guide)

- The data processed with PROOF can reside on local computer, PROOF cluster disks or grid

• PROOF integration to FairRoot - goals:

- implement the necessary FairRoot changes in the base classes (accessible to all experiment codes using FairRoot)

- minimize changes, which the user has to implement in order to run on PROOF (both in users' code and the starting macros)

Fully integrated in FairRoot

- No changes in user code
- Changes in macro: fRun->Run(start,end,"proof");
- Using PROOF pays off for analysis of large amounts of data
- For results please see Radeks talk

Time dependent simulation

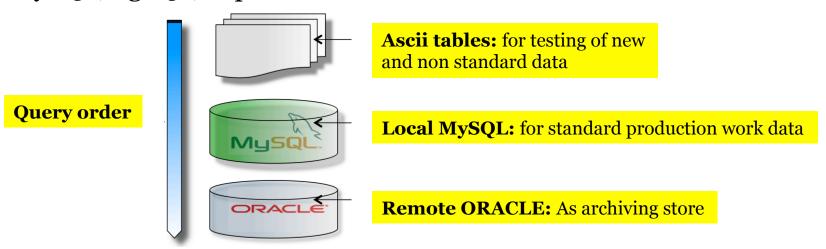
- Monte-Carlo simulations deliver events that can be treated completely independent of each other and time between events does not play a role.
- This is not the reality in experiments:
 - Sensor elements are still blocked from previous hits
 - Electronic is still busy
 - Hits too close in time cannot be distinguished

• •••

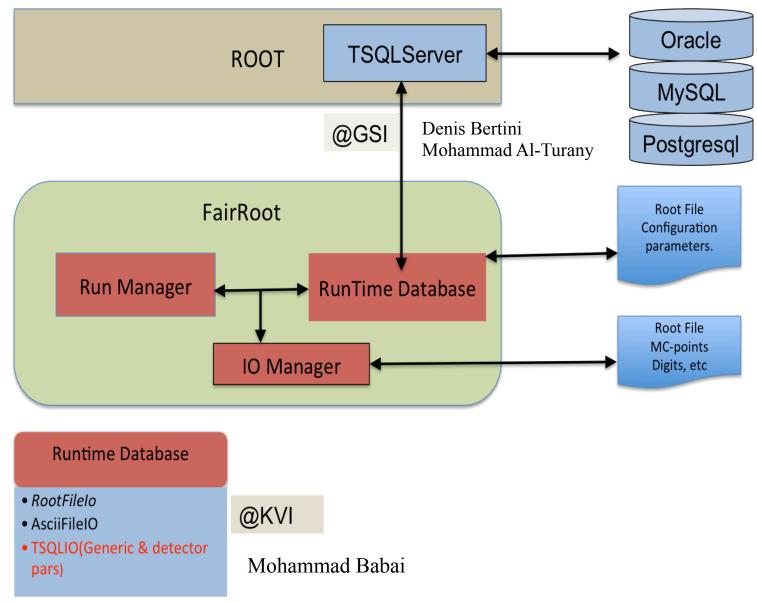
- PANDA and CBM:
 - Continuous beam with Poisson statistics -> many events with short time between them
 - No hardware trigger

FairRoot Database connectivity (Concept)

- **Fast varying** setup in nuclear physics experiments, urging the framework to support Database connectivity
- New Database interface design based on RDBC (TSQLServer) as lowest level API which provides uniform interface to Oracle, MySQL, PgSQL, SapDb.



Database connection (concept)



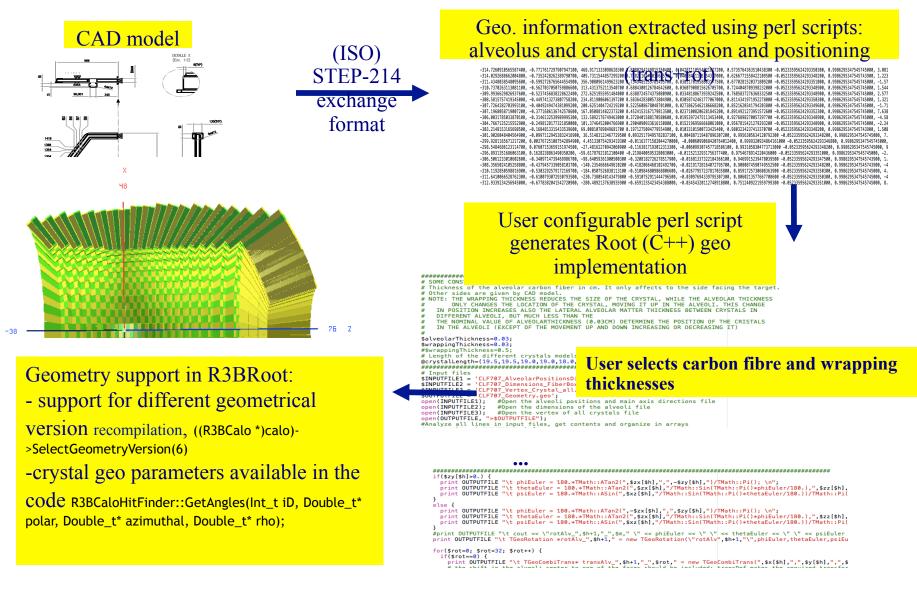
FairRoot Database connectivity (Status)

- Remove the HADES Oracle implementation from repository
- Keep the Run time data as interface
- Replace the Oracle IO classes by a TSQLServer based implementation
- Experiments are free to choose data there favorite data base, design the tables and choose how to validate

CAD to Root converter

- Each experiment (Panda, CBM and R3B) has his own converter
- Is it possible to join the efforts?

R3BRoot : CAD to ROOT a practical approach



R3BRoot



GPU usage examples in FairRoot

Porting track finder/Fitter to CUDA

Original code is not optimized for parallel architectures

Lookup tables are used for the mathematical functions (Code is designed to work on FPGA)

> Redesign the code into many functions (kernels) instead of one main

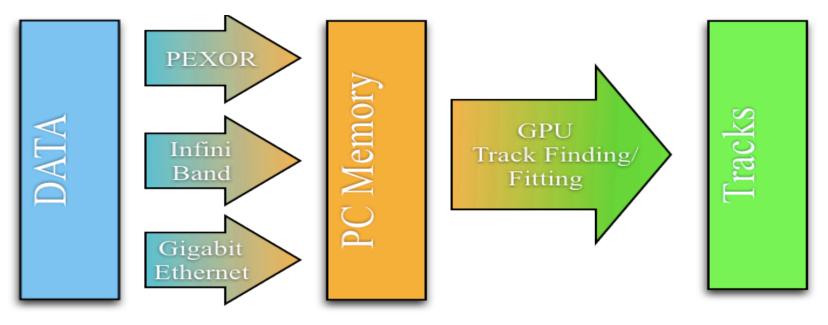
Use the standard mathematical libraries delivered by NVIDIA

A total improvement of factor 200 compared to the original code on an Intel Xeon CPU W3505 @ 2.53 GHz

	CPU (ms)	GPU (ms)	Improvement	Occupancy	Notes
total runtime (without Z-Analysis)	117138	590	199		
startUp()	0.25	0.0122	20	2%	runs (num_points) times
setOrigin()	0.25	0.0119	21	25%	runs (num_points) times
clear Hough and Peaks (memset on GPU)	3	0.0463	65	100%	runs (num_points) times
conformalAndHough()	73	0.8363	87	25%	runs (num_points) times
findPeaksInHoughSpace()	51	0.497	103	100%	runs (num_points) times
findDoublePointPeaksInHoughSpace()	4	0.0645	62	100%	runs (num_points) times
collectPeaks()	4	0.066	61	100%	runs (num_points) times
sortPeaks()	0.25	0.0368	7	2%	runs (num_points) times
resetOrigin()	0.25	0.0121	21	25%	runs (num_points) times
countPointsCloseToTrackAndTrackParams()	22444	0.9581	23426	33%	runs once
collectSimilarTracks()	4	2.3506	2	67%	runs once
collectSimilarTracks2()	7	2.0000	2	2%	runs once
getPointsOnTrack()	0.25	0.0187	13	33%	runs (num_tracks) times
nullifyPointsOfThisTrack()	0.25	0.0106	24	33%	runs (num_tracks) times
clear Hough space (memset on GPU)	2	0.0024	833	100%	runs (num_tracks) times
secondHough()	0.25	0.0734	3	4%	runs (num_tracks) times
findPeaksInHoughSpaceAgain()	290	0.2373	1222	66%	runs (num_tracks) times
collectTracks()	0.25	0.0368	7	2%	runs (num_tracks) times

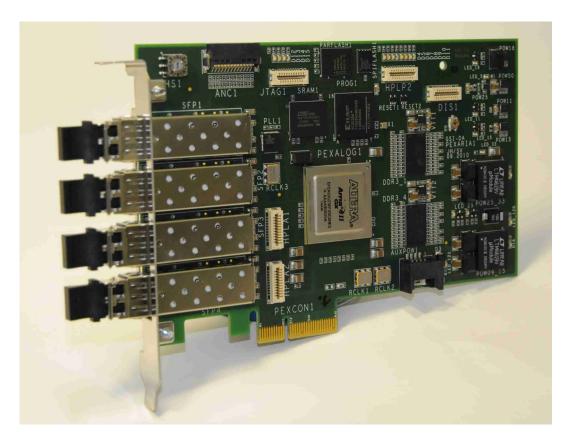
Plans for the Online: Online track finding and fitting with GPU

- In collaboration with the GSI EE, build a proto type for an online system
 - Use the PEXOR card to get data to PC
 - PEXOR driver allocate a buffer in PC memory and write the data to it
 - $_{\circ}~$ The GPU uses the Zero copy to access the Data, analyze it and write the results



PEXARIA (PCI-Express To Optical Link Interface)

- The GSI PEXOR is a PCI express card provides a complete development platform for designing and verifying applications based on the Lattice SCM FPGA family.
- Four 4 gigabit optical transceivers.



NVIDIA GPUDirectTM

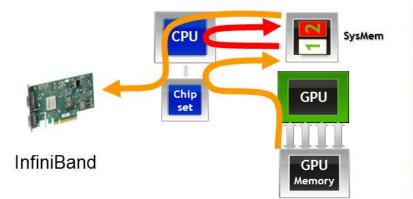
Without GPUDirect

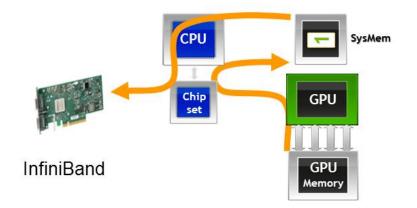
Same data copied three times:

- 1. GPU writes to pinned sysmem1
- 2. CPU copies from sysmem1 to sysmem2
- 3. InfiniBand driver copies from sysmem2

With GPUDirect

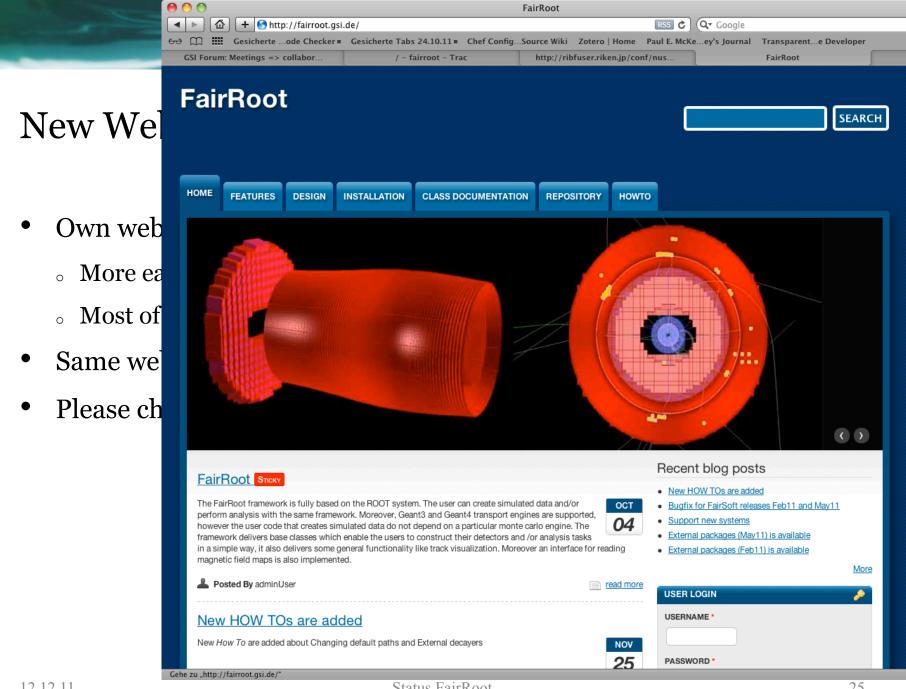
Data only copied twice Sharing pinned system memory makes sysmem-to-sysmem copy unnecessary





New Webpage

- Own webserver with Drupal as Content Management System
 - More easy to maintain then old system
 - $_{\circ}~$ Most of the old content was already moved to the new server
- Same webpage as before: fairroot.gsi.de
- Please check and give feedback



>>

New Dashboard

- Also a new webserver for the DashBoard
- New version of the software
 - $_{\circ}~$ could not be installed on the old server
 - Provides new functionality
- Old data will not be migrated
 - Can still be accessed on the old server
- Experiments can move to the new server at any time

New Dashboard

- Also a new webserver f
- New version of the soft
 - \circ could not be installed o
 - Provides new functiona
- Old data will not be mig
 - ∘ Can still be accessed on
- Experiments can move



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./base/FairGenericStack.cxx	Low	14.63%	35/41	None				Configur	е		Build			Test				
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./base/FairRootManager	205	i	,								- P	
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	209	l i	// Init the com	tainers in Tasks								
	210	5		ainers(fRunId);								
	211 212	5	fTask->SetParTa									
	212	5		<pre>:ainers(fRunId); ==0) { par->GetGeometry();</pre>	3							
	214			er->SetBranchNameList(par->								
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	216 217	0	<pre>} else if (fMixed fLogger->Info()</pre>	iInput) { MESSAGE_ORIGIN,"Initializi:	ng for Mixed input").							
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	219	İ		out we have to set contain	ers to static becauser of	the dif	ferent	run ids				
	220 221		//fRtdb->setCom	<pre>tainersStatic(kTRUE);</pre>								
10	221	0	fEvtHeader = (1	airEventHeader*) fRootMan	ager->GetObject("EventHea	der.");					×	20
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Tests and Code Coverage

- Only small part of code base tested at all
- What about the other part?
 - Not needed? Could be removed !
 - Bugs inside ? Nobody knows !!
- Test coverage should be increased
 - Not possible when testing the full program (to time consuming)
- But even if the program runs and comes to an end, how do one knows that the result is correct?

Unit Tests for FairRoot

- Solution: Unit Tests
 - Test only small blocks of code at the same time
 - Check if the result is correct
- Several Unit Test frameworks available
 - Evaluation is work in progress
 - CxxTest, CppUnit, CppUTest, CppTest (need to be tested)
 - Boost Unit Test, GoogleTest (in the testing phase)
- Most promising up to now is the GoogleTest framework
 - Easy to use
 - All needed functionality up to now
 - Test coverage for test case is above **90**%

Summary

- The Framework which started with two people in 2003 as a test for CBM is now the standard software for at least 3 large collaborations
- Meanwhile a core team of 5 developers and more than 70 registered developers from the different experiments
- QA system, Ticket system, Wiki, discussion forum are actively used.
- Development of general interest usually finds its way to other experiments by moving from the specific experiment implementation to FairRoot (more general things goes even back to ROOT and VMC):
 - CAD TO ROOT converter
 - $_{\circ}$ Event Display
 - Geane track propagator
 - Monte-Carlo validation package
 - Event generators

http://fairroot.gsi.de



Event Time In Simulation

Can be set via the FairPrimaryGenerator :

- Set the min and max limit for event time in ns SetEventTimeInterval(Double_t min, Double_t max)
- Set the mean time for the event in ns SetEventMeanTime(Double_t mean)
- Set the time function for event *SetEventTime(TF1* timeProb)*

Event Time after simulation

Can be set via the FairRunAna

- Set the min and max limit for event time in ns *SetEventTimeInterval(Double_t min, Double_t max)* (Time set via Uniform Random between min and max)
- Set the mean time for the event in ns

SetEventMeanTime(Double_t mean)

(Event time is an exponential deviate.)

Event Time

- In simulation event time is saved in
 - FairMCEventHeader
- After simulation it is in
 - FairEventHeader
- In all cases event time can be access via:
 - o FairRootManager::GetEventTime()

File Header

- Run Id
- List of TObjStrings presenting the class names of tasks used to produce this file
- List of FairFileInfo presenting the input files used to produce this file

FairFileInfo class

- Full path of the file
- Size of file in bytes
- File identifier used
- File order in the chain

Event Header

- Run Id
- Event Time
- Input file identifier, the file description is in the File header
- Monte-Carlo entry number from input chain

Signal-Background mixing

- After simulation
- At the moment we only check that the all input trees contain the same branches (further checks needed!)
- All can be done from the macro

Example of a mixing digi macro

See "Pandaroot/macro/run/example_mix"

• Setting the input files:

//** Set BG file */
fRun->SetBackgroundFile("sim_stt_bg.root");

//** Set first signal file */
fRun->SetSignalFile("sim_stt_s1.root",1);

//** Set second signal file */
fRun->SetSignalFile("sim_stt_s2.root",2);

Signal chain identifiers

Example of a mixing digi macro

• Adding more files to the signal and background chains:

//** Set BG file */
fRun->AddBackgroundFile("sim_stt_bg1.root");

```
//** Set first signal file */
fRun->AddSignalFile("sim_stt_s1_1.root",1);
```

//** Set second signal file */
fRun->AddSignalFile("sim_stt_s2_1.root",2);

Signal chain identifiers

Example: Mix using entries

 For each ~20 entries background one entry from signal chain 1 should be read

fRun->BGWindowWidthNo(20,1)

 for each ~30 entries background one entry from signal chain 2 should be read

fRun->BGWindowWidthNo(30,2)

Example: Mix using time

- Set the event mean time, event time will be a random number generated from (1/T)exp(-x/T)
 fRun->SetEventMeanTime(10);
- Each ~100 ns background 1 entry from signal chain 1 will be read *fRun->BGWindowWidthTime(100,1);*
- Each ~60 ns background 1 entry from signal chain 2 will be read *fRun->BGWindowWidthTime(60,2)*;

Running the macro

[INFO] Maximum No of Event was set manually to : 120 , we will check if there is enough entries for this!!

[INFO] Signal chain No 1 has : 2 entries

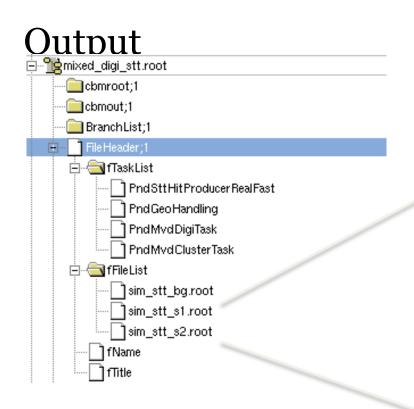
[WARNING] No of Event in signal chain 1 is not enough, the maximum event number will be reduced to : 20

[INFO] Signal chain No 2 has : 20 entries

[WARNING] No of Event in Background chain is not enough for all signals in

chain 2

[INFO] Maximum No of Event will be set to : 20



	class=FairFileInfo							
	fPath	/pandaroot/macro/run/example_mix						
1	fSize	52607						
	fIdentifier	1						
	fInChainId	0						
	fName	sim_stt_s1.root	object identifier					

class=FairFileInf	ò	
fPath	/pandaroot/macr	o/run/example_mix
fSize	243274	
fIdentifier	2	
fInChainId	0	
fName	sim_stt_s2.root	object identifier

To Do

- More checks before mixing:
 - \circ Parameters
 - \circ Geometry
- Mixing signal in a sub-set of the detector with the full back ground simulation