

**Communiqué on the status of preparations of the
“Cluster-Jet Target Control Final Design Report”**

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Cluster - Jet Target Control Final Design Report is in the course of preparation.

It will consist of two parts:

Part I will have a detailed account of all vacuum devices contained in the existing cluster-jet source (generator) and their interfaces to the controller CompactRIO₁ plus existing **and planned devices** in the cluster-jet beam dump, and their interfaces to CompactRIO₂ . The essential information is neatly arranged in tabular form, therefore our expectations can be easily verified with the existing realities and be corrected by the Uni-Münster where they diverge.

The latter part will be send to the interested parties in the beginning of next week.

Part II of the Design Report will contain a description of the planned slow-control system that is planned to be delivered to FZ-Jülich for the joined tests with software and the hardware residing there since 2018.

This part is under preparation and we hope to complete it by the end of April 2021.

In the same time our hopes are that the recent proposal to incorporate the MicroBlaze processor into the CRIO 9039 FPGA will find acceptance of both Targets and DCS subsystem coordinators.

THANK YOU FOR YOUR ATTENTION!