Status of the PANDA TPC Simulation

Felix Böhmer

Physik Department E18 Technische Universität München Germany

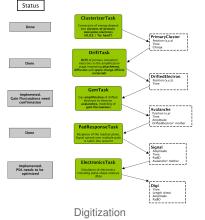
PANDA Collaboration Meeting June 15 2010, Stockholm, Sweden





TPC Simulation Software Status





Status (Digi & Reco):

• Implementation work finished

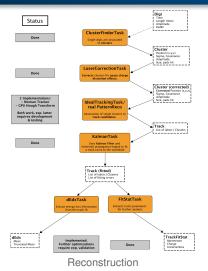
Recent Developments:

- Integrated Test Chamber geometry into PANDAROOT
- Bridged data taking to PANDAROOT
- Implemented Hough Transform PR for real data (2D)

Things to be done:

- Test & optimize
 - · Gain fluct. simulation
 - Pulse Shape Analysis
 - Clustering algorithms
 - Pattern recognition
 - dE/dx extraction & analysis
 - ... based on real measurements

TPC Simulation Software Status



Status (Digi & Reco):

• Implementation work finished

Recent Developments:

- Integrated Test Chamber geometry into PANDAROOT
- Bridged data taking to PANDAROOT
- Implemented Hough Transform PR for real data (2D)

Things to be done:

- Test & optimize
 - · Gain fluct. simulation
 - Pulse Shape Analysis
 - Clustering algorithms
 - Pattern recognition
 - dE/dx extraction & analysis
 - ... based on real measurements

Technische Universität München

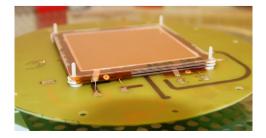




Hardware:

- $10 \times 10 \text{ cm}^2$ active area
- Triple-Gem stack for amplification
- 8 cm drift length
- 1500 hexagonal pads:
 - 1.5 mm outer radius
 - 1.25 mm outer radius

- T2K AFTER chip (Saclay)
- 4 chips × 74 (64) channels per FE card
- 0.4 W power consumption / chip
- Analog sampling at 10 50 MHz
- Noise: < 600 e⁻
- Sensitivity: 0.063 fC / ADC ch. = 397 e⁻ / ADC ch.



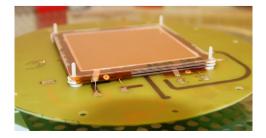




Hardware:

- $10 \times 10 \text{ cm}^2$ active area
- Triple-Gem stack for amplification
- 8 cm drift length
- 1500 hexagonal pads:
 - 1.5 mm outer radius
 - 1.25 mm outer radius

- T2K AFTER chip (Saclay)
- 4 chips × 74 (64) channels per FE card
- 0.4 W power consumption / chip
- Analog sampling at 10 50 MHz
- Noise: < 600 e⁻
- Sensitivity: 0.063 fC / ADC ch. = 397 e⁻ / ADC ch.







Hardware:

- $10 \times 10 \text{ cm}^2$ active area
- Triple-Gem stack for amplification
- 8 cm drift length
- 1500 hexagonal pads:
 - 1.5 mm outer radius
 - 1.25 mm outer radius

- T2K AFTER chip (Saclay)
- 4 chips × 74 (64) channels per FE card
- 0.4 W power consumption / chip
- Analog sampling at 10 50 MHz
- Noise: < 600 *e*⁻
- Sensitivity: 0.063 fC / ADC ch. = 397 e⁻ / ADC ch.



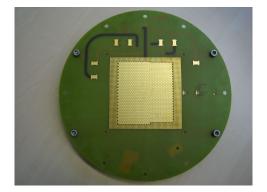




Hardware:

- $10 \times 10 \text{ cm}^2$ active area
- Triple-Gem stack for amplification
- 8 cm drift length
- 1500 hexagonal pads:
 - 1.5 mm outer radius
 - 1.25 mm outer radius

- T2K AFTER chip (Saclay)
- 4 chips × 74 (64) channels per FE card
- 0.4 W power consumption / chip
- Analog sampling at 10 50 MHz
- Noise: < 600 *e*⁻
- Sensitivity: 0.063 fC / ADC ch. = 397 e⁻ / ADC ch.







Hardware:

- $10 \times 10 \text{ cm}^2$ active area
- Triple-Gem stack for amplification
- 8 cm drift length
- 1500 hexagonal pads:
 - 1.5 mm outer radius
 - 1.25 mm outer radius

- T2K AFTER chip (Saclay)
- 4 chips × 74 (64) channels per FE card
- 0.4 W power consumption / chip
- Analog sampling at 10 50 MHz
- Noise: < 600*e*⁻
- Sensitivity: 0.063 fC / ADC ch. = 397 e⁻ / ADC ch.



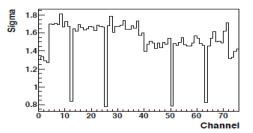




Hardware:

- $10 \times 10 \text{ cm}^2$ active area
- Triple-Gem stack for amplification
- 8 cm drift length
- 1500 hexagonal pads:
 - 1.5 mm outer radius
 - 1.25 mm outer radius

- T2K AFTER chip (Saclay)
- 4 chips × 74 (64) channels per FE card
- 0.4 W power consumption / chip
- Analog sampling at 10 50 MHz
- Noise: < 600 e⁻
- Sensitivity: 0.063 fC / ADC ch. = 397 e⁻ / ADC ch.

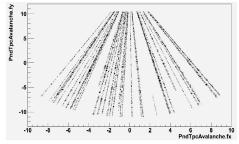


Simulation





- Simulating "cosmics events" within the PANDAROOT framework (ionization, drift, GEM amplification, avalanche spread)
- Signal induction
- ADC sampling
- Pulse shape analysis (PSA) (also used for data, of course)

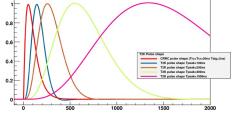


Avalanches produced in the GEM stack





- Simulating "cosmics events" within the PANDAROOT framework (ionization, drift, GEM amplification, avalanche spread)
- Signal induction
- ADC sampling
- Pulse shape analysis (PSA) (also used for data, of course)

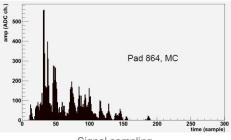


Pulse shape of AFTER-T2K for different sampling times





- Simulating "cosmics events" within the PANDAROOT framework (ionization, drift, GEM amplification, avalanche spread)
- Signal induction
- ADC sampling
- Pulse shape analysis (PSA) (also used for data, of course)

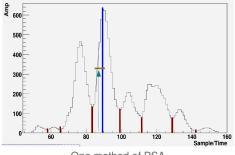


Signal sampling





- Simulating "cosmics events" within the PANDAROOT framework (ionization, drift, GEM amplification, avalanche spread)
- Signal induction
- ADC sampling
- Pulse shape analysis (PSA) (also used for data, of course)



One method of PSA

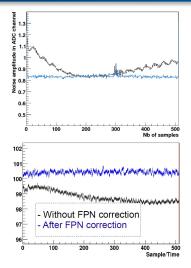
Cosmics @ Munich



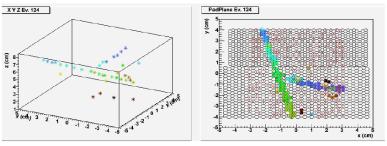


Zero suppression on the ADC level

- Offline correction:
 - Common noise suppression
- Fixed Pattern Noise (FPN) correction:
 - One observes a time dependence of the signals along the analog memory of the AFTER-T2K chip
 - Can be corrected using unconnected channels (see picture)



An Example Cosmic Event @ Munich Lab



Left: 3D-View; Right: Digis and clusters on the padplane

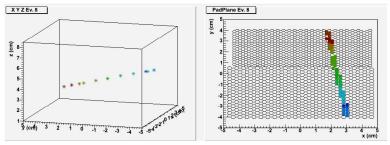
- Example cosmic track emits delta electron
- Pattern recognition finds both
- Full analysis of large dataset ongoing

Technische Universität Müncher

Cosmics vs. "MC Cosmics"



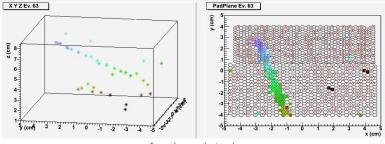
A Track:



A simulated "cosmic track"



A Track:

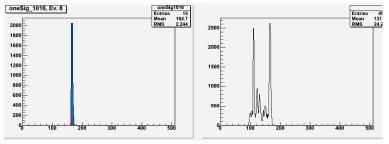


A real cosmic track





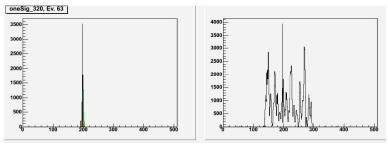
Samples:



Corresponding samples in simulation



Samples:



Real event samples

Conclusion



Status Quo:

- We have a working test detector
- We are taking data, analysis ongoing
- Full set of track finding & fitting algorithms in place and performing well
- Our full detector simulation is able to reproduce the data taken

Next Steps:

- Analyze taken data
- New beam test coming up this summer at CERN (COMPASS) with high-energetic muons (→ First dE/dx benchmark)
- Online monitoring program under development
- Optimize simulation based on experimental results