Controls
Dry Run 9 Activities

Machine Meeting, 2018-07-31

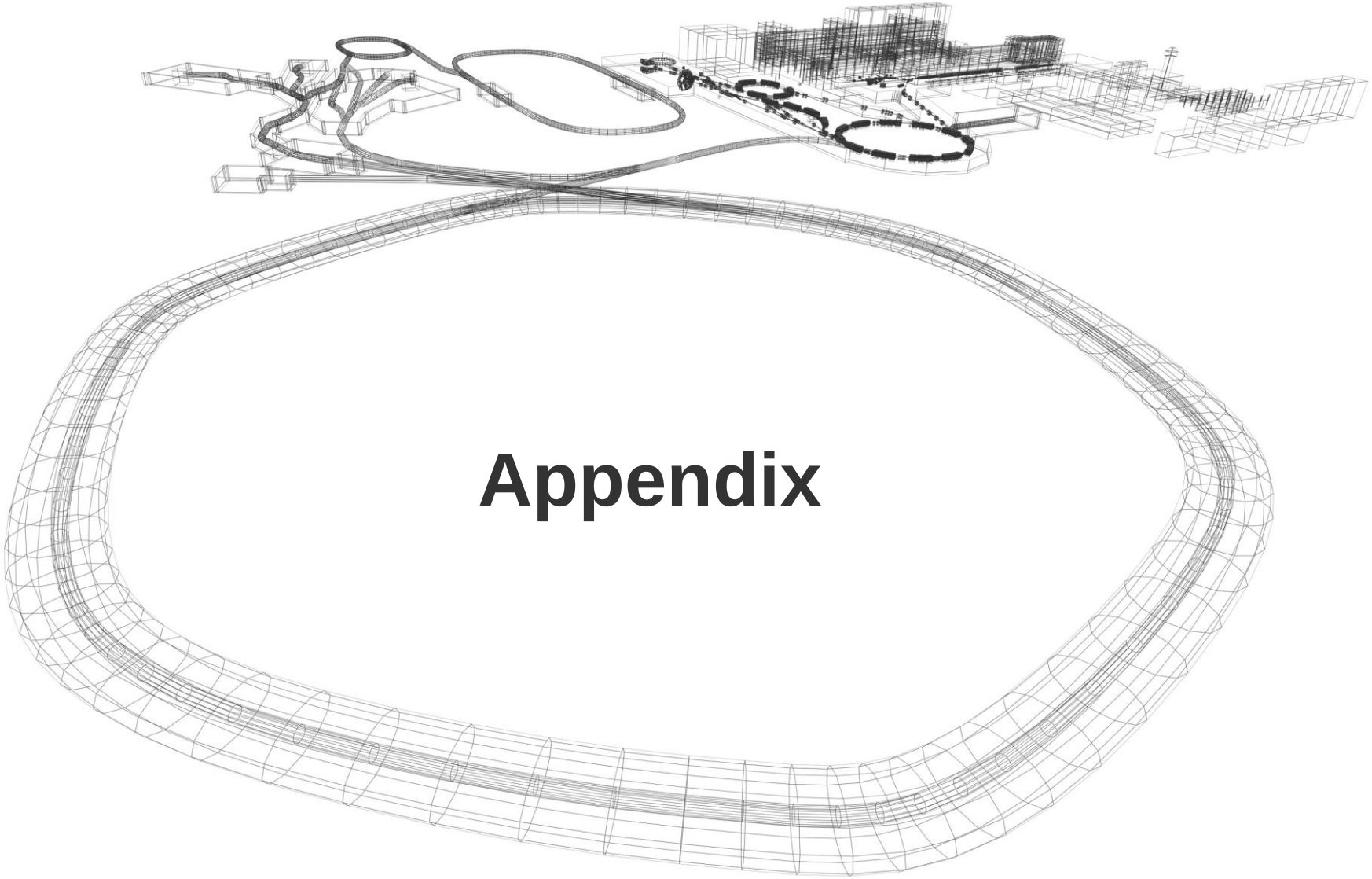
Hanno Hüther

Dry Run 9
(July 31st until August 3rd)

- Test bug fixes for known issues (priority 1)
  - Test bug fix for late events from Data Master
  - Test bug fix for UNILAC beam request issues
  - Additional diagnosis has been put into place (logging, debugging, „Ready to SIS“ event on scope, …)
  - Supported by operators for trying to cause errors (performing trims, activating / deactivating patterns, …)
- Analyze remaining function generator issues (priority 1)
  - In-depth diagnosis on-site via logic analyzer tool
- Investigate transfer from UNILAC taking occasionally taking longer (priority 2)
  - Hypothesis: It’s not a “Schwebung”, but related to the UNILAC Pulszentrale / communication with the WR-to-MIL gateway

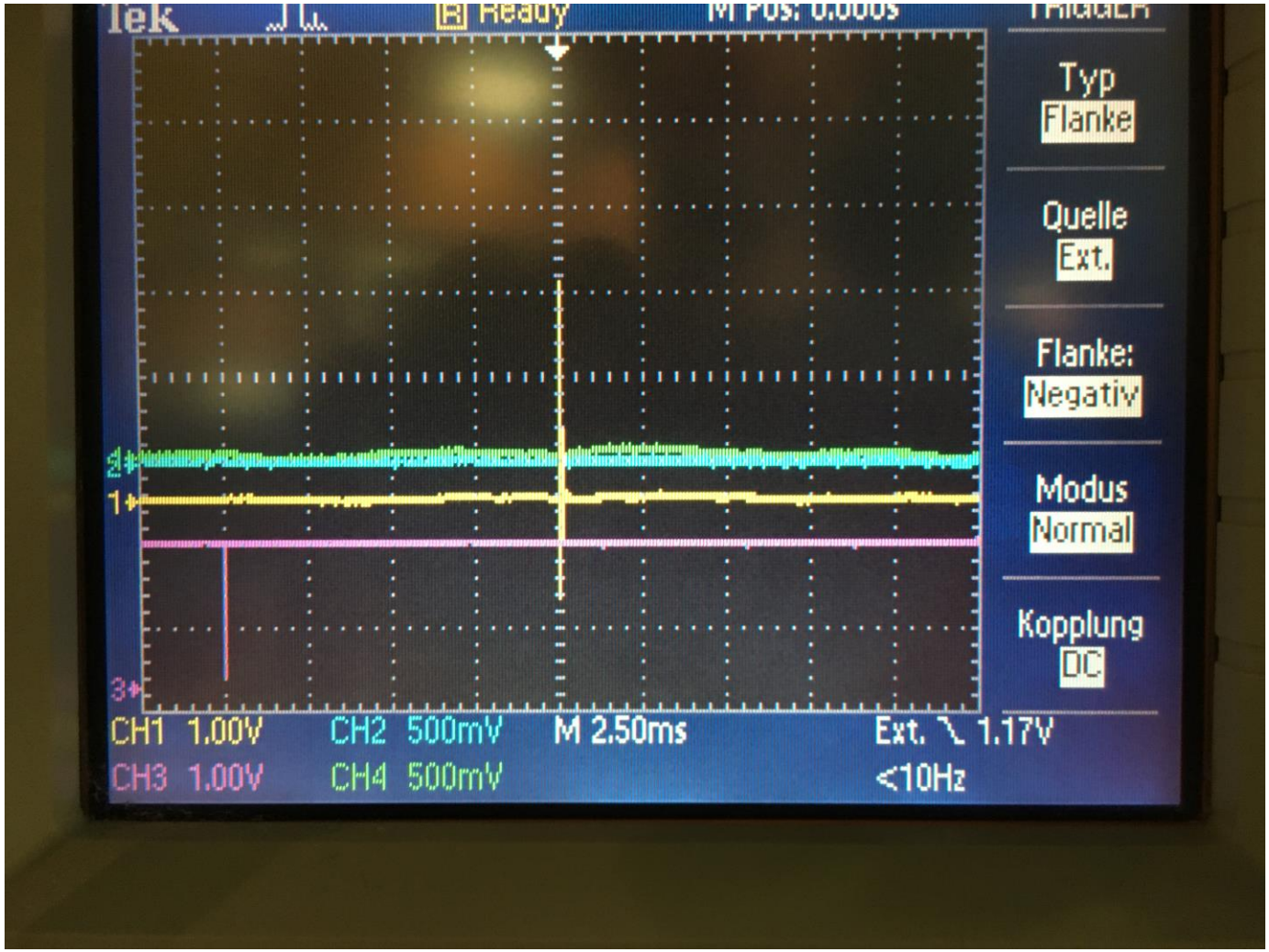
Dry Run 9
(July 31st until August 3rd)

- SIS18 Cooler tests (priority 3)
  - Preparations shall be completed until Wednesday evening as requested
  - Time slots will probably be available on Thursday evening and on Friday (no promises, but highly likely)
- SIS18 ⇔ ESR coupling tests (priority 4)
  - First iteration of LSA implementation ready for testing
- Any other requests for testing time (priority 4)
  - RF and potentially others
  - Only if it does not interfere with testing activities priority 1 to 3
- Additional testing after Dry Run 9
  - Would be helpful under the premise that devices can be operated with load



Appendix

EVT_READY_TO_SIS on scope @ HKR
Thanks to H. Reeg!



Picture:
D. Beck

Transfer from UNILAC occasionally taking longer

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dm-unipz:    TRANSFER INS SIS              | INJEKTION
                    nr          Gesamtdauer des Transfer
                    |           |    Dauer TK Reservierung
                    |           |    |          Maschine
                    |           |    |          |       Nr der Injektion (Multi)
                    |           |    |          |       |    Gesamtdauer Injektion nach BREQ
                    |           |    |          |       |    |    Warten auf ACK des BREQ
                    |           |    |          |       |    |    |              von UNIPZ
                    |           |    |          |       |    |    |    Warten auf Ready2SIS
                    |           |    |          |       |    |    |    |    R2S->MB_TRIGGER
                    |           |    |          |       |    |    |    |    |
dm-unipz: TRANS 00002963,  727( 13)ms, va  4 | INJ 01  471(16/ 461 -> 9.979)ms
dm-unipz: TRANS 00002964,  727( 13)ms, va  4 | INJ 01  471(16/ 461 -> 9.979)ms
dm-unipz: TRANS 00002965,  727( 13)ms, va  4 | INJ 01  471(16/ 461 -> 9.979)ms
dm-unipz: TRANS 00002966,  727( 13)ms, va  4 | INJ 01  471(16/ 461 -> 9.979)ms
dm-unipz: TRANS 00002967,  727( 13)ms, va  4 | INJ 01  471(16/ 461 -> 9.979)ms
dm-unipz: TRANS 00002968, 1727( 13)ms, va  4 | INJ 01 1471(17/1461 -> 9.979)ms
dm-unipz: TRANS 00002969,  727( 13)ms, va  4 | INJ 01  471(17/ 461 -> 9.979)ms
dm-unipz: TRANS 00002970,  727( 13)ms, va  4 | INJ 01  471(17/ 461 -> 9.979)ms
dm-unipz: TRANS 00002971,  728( 13)ms, va  4 | INJ 01  472(17/ 462 -> 9.979)ms
dm-unipz: TRANS 00002972,  728( 13)ms, va  4 | INJ 01  472(17/ 462 -> 9.979)ms
dm-unipz: TRANS 00002973,  728( 13)ms, va  4 | INJ 01  472(17/ 462 -> 9.979)ms
dm-unipz: TRANS 00002974, 1728( 13)ms, va  4 | INJ 01 1472(17/1462 -> 9.979)ms
dm-unipz: TRANS 00002975,  728( 13)ms, va  4 | INJ 01  472(17/ 462 -> 9.979)ms
dm-unipz: TRANS 00002976,  728( 13)ms, va  4 | INJ 01  472(17/ 462 -> 9.979)ms
dm-unipz: TRANS 00002977,  728( 13)ms, va  4 | INJ 01  472(17/ 462 -> 9.979)ms
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Diagram:
D. Beck