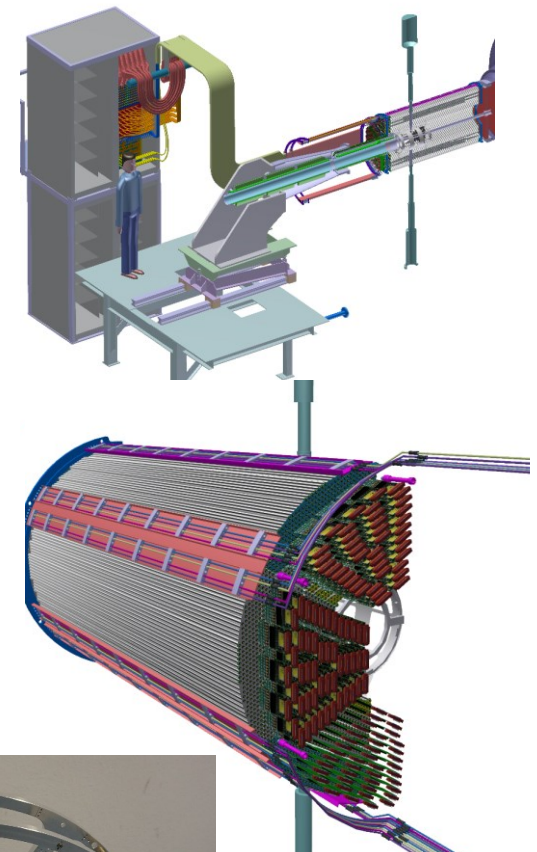


# ASIC/TRB FE-LAYOUT



- FE-scheme in progress
  - FE-layout & cabling scheme up to RO racks
  - FE-cooling
    - closed volume  $\sim 2 \times 20$  l,  $2 \times 84$  W (40mW/ch)
    - experiment hall:  $\sim 25^\circ$
    - forced convection (FANs)
    - volume exchange rate & flow scheme
  
- test system set up & scheme (Julich)
  - 1:1 FE-volume, covered walls
  - 3 sectors  $\times$  10 PCB (stack)
  - 135 $\times$  heat sources (= FE-boards)
  - temperature sensors
  - air flow scheme



STT FE-layout



STT FE-volume for cooling tests, walls will be covered