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FPGA based read-out systems (CBM)

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The central part of many state-of-the-art data acquisition systems is formed by programmable logic devices (FPGA). During the recent upgrade of the HADES detector at GSI, a huge set of dedicated electronics and software has been developed and validated in experiment. Several FAIR experiments already profited from these developments in the past years. I will present this framework and on-going extension work to cope with new requirements with respect to the planned high-rate experiments at FAIR. Special emphasis will be put on synergies between different groups at FAIR and other institutions world-wide.

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